ACER, INC.
V I 1 2 G

Processor	80486SX/80486DX/80486DX2
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	Acer
Max. Onboard DRAM	128MB
Cache	128/256KB
BIOS	Award
Dimensions	254mm x 218mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None
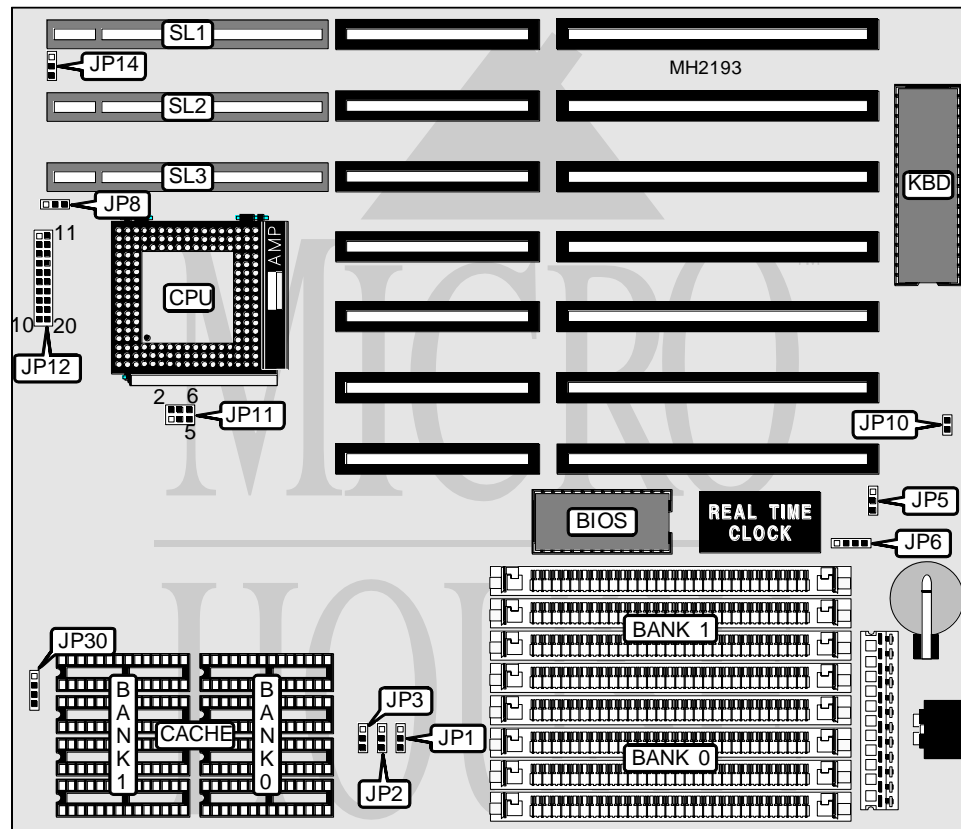
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP6	Break switch	JP12 (pins 16-18)
Power LED & keylock	JP12 (pins 1-5)	Reset switch	JP12 (pins 19-20)
Speaker	JP12 (pins 7-10)	Chassis fan power	JP30
Green PC connector	JP12 (pins 12-13)	32-bit VESA local bus slots	SL1 - SL3

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP5	Open
CMOS memory clear	JP5	pins 1 & 2 closed
í Factory configured - do not alter	JP8	Open
í Factory configured - do not alter	JP10	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
1MB	NONE	(4) 256K x 9
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
16MB	NONE	(4) 4M x 9
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
65MB	(4) 16M x 9	(4) 256K x 9
68MB	(4) 16M x 9	(4) 1M x 9
80MB	(4) 16M x 9	(4) 4M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP1	JP2	JP3
128KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed
256KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed

CPU SPEED CONFIGURATION	
Speed	JP11
25MHz	pins 1 & 2, 3 & 4, 5 & 6 closed
33MHz	pins 1 & 2, 3 & 4 closed
40MHz	pins 1 & 2, 5 & 6 closed
50iMHz	pins 1 & 2, 3 & 4, 5 & 6 closed
50MHz	pins 1 & 2 closed
66iMHz	pins 1 & 2, 3 & 4 closed

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BUS SPEED CONFIGURATION	
CPU speed	JP14
<=33MHz	pins 1 & 2 closed
>33MHz	pins 2 & 3 closed