ROBOTECH, INC. COBRA-RT VER. A

Processor CX486S/80486SX/CX487S/80486DX/ODP486SX/80486DX2/ 80486DX4/Pentium

Overdrive

Processor Speed 25/33/40/50(internal)/50/66(internal)MHz

Chip Set SIS Max. Onboard DRAM 64MB

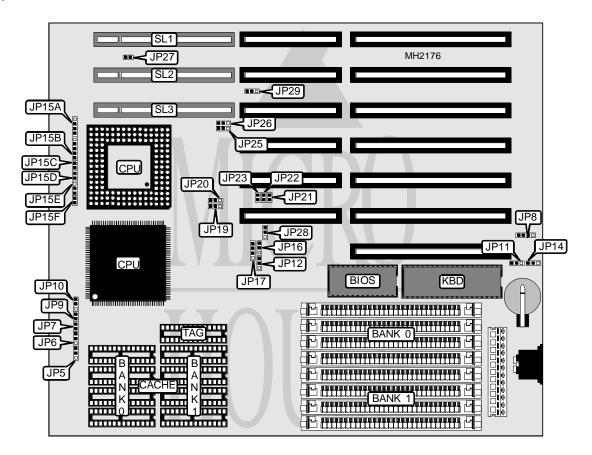
Cache 32/64/128/256KB

BIOS AMI

Dimensions 254mm x 220mm

I/O Options 32-bit VESA local bus slots (3)

NPU Options None



CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	JP8	Reset switch	JP15D	
Speaker	JP15A	Turbo switch	JP15E	
Power LED & keylock	JP15B	+5vDC ground	JP15F	
Turbo LED	JP15C	32-bit VESA local bus slots	SL1 - SL3	

Continued on next page. . .

ROBOTECH, INC. COBRA-RT VER. A

. . . continued from previous page

USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í CMOS memory normal operation	JP8	pins 2 & 3 closed		
CMOS memory clear	JP8	pins 3 & 4 closed		
í Power good signal detect from power supply	JP11	pins 1 & 2 closed		
Power good signal detect from board	JP11	pins 2 & 3 closed		
í CPU type select 1 CPU on board (PQFP or PGA)	JP12	pins 1 & 2 closed		
CPU type select 2 CPU on board (PGA enabled PQFP disabled)	JP12	pins 2 & 3 closed		
í Green PC mode enabled	JP14	pins 2 & 3 closed		
Green PC mode disabled	JP14	pins 1 & 2 closed		
í VESA bus address type select asynchronous (40/50mhz CPU)	JP19	pins 1 & 2 closed		
VESA bus address type select synchronous	JP19	pins 2 & 3 closed		
í VESA bus data type select synchronous	JP20	pins 1 & 2 closed		
VESA bus data type select asynchronous	JP20	pins 2 & 3 closed		
í VGA card select normal	JP28	pins 1 & 2 closed		
VGA card select Weitek Power 9000 VGA only	JP28	pins 2 & 3 closed		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
1MB	(4) 256K x 9	NONE		
2MB	(4) 256K x 9	(4) 256K x 9		
4MB	(4) 1M x 9	NONE		
5MB	(4) 256K x 9	(4) 1M x 9		
8MB	(4) 1M x 9	(4) 1M x 9		
16MB	(4) 4M x 9	NONE		
17MB	(4) 4M x 9	(4) 256K x 9		
20MB	(4) 4M x 9	(4) 1M x 9		
32MB	(4) 4M x 9	(4) 4M x 9		
64MB	(4) 16M x 9	NONE		

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

	CACHE JUMPER CONFIGURATION				
Size	JP5	JP6	JP7	JP9	JP10
32KB	2 & 3	Open	Open	Open	Open
64KB	1 & 2	Open	Open	Closed	Open
128KB	2 & 3	2 & 3	Closed	Closed	Open
256KB	1 & 2	1 & 2	Closed	Closed	2 & 3
256KB	1 & 2	1 & 2	Closed	Closed	1 & 2
Note: Pins designated should be in the closed position.					

Continued on next page. . .

ROBOTECH, INC. COBRA-RT VER. A

 \dots continued from previous page

CPU TYPE CONFIGURATION			
Туре	JP16	JP17	
CX486S	Open	pins 2 & 3 closed	
80486SX	Open	pins 2 & 3 closed	
80487SX	pins 2 & 3 closed	pins 1 & 2, 3 & 4 closed	
ODP486SX	pins 2 & 3 closed	pins 1 & 2, 3 & 4 closed	
80486DX/DX2/DX4	pins 1 & 2 closed	pins 1 & 2, 3 & 4 closed	
Pentium Overdrive	pins 1 & 2 closed	pins 1 & 2, 3 & 4 closed	

	СР	U SPEED CONFIGURATION	ON	
Speed	JP21	JP22	JP23	JP27
25MHz	Closed	Open	Closed	Open
33MHz	Closed	Closed	Open	Open
40MHz	Open	Open	Closed	Closed
50iMHz	Closed	Open	Closed	Open
50MHz	Open	Closed	Open	Closed
66iMHz	Closed	Closed	Open	Open

VESA BUS CLOCK CONFIGURATION			
Setting JP25 (SL2) JP26 (SL1) JP29 (SL3)			
Synchronous	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
Asynchronous	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed

CIRRUS LOGIC VIDEO CARD CONFIGURATION (VER. AB)			
CPU speed	JP19	JP20	
25MHz	pins 2 & 3 closed	pins 1 & 2 closed	
33MHz	pins 2 & 3 closed	pins 1 & 2 closed	
40MHz	pins 1 & 2 closed	pins 1 & 2 closed	
50iMHz	pins 2 & 3 closed	pins 1 & 2 closed	
50MHz	pins 1 & 2 closed	pins 1 & 2 closed	
66iMHz	pins 2 & 3 closed	pins 1 & 2 closed	

CIRRUS LOGIC VIDEO CARD CONFIGURATION (VER. AC)			
CPU speed	JP19	JP20	
25MHz	pins 2 & 3 closed	pins 1 & 2 closed	
33MHz	pins 2 & 3 closed	pins 1 & 2 closed	
40MHz	pins 1 & 2 closed	pins 2 & 3 closed	
50iMHz	pins 2 & 3 closed	pins 1 & 2 closed	
50MHz	pins 1 & 2 closed	pins 2 & 3 closed	
66iMHz	pins 2 & 3 closed	pins 1 & 2 closed	