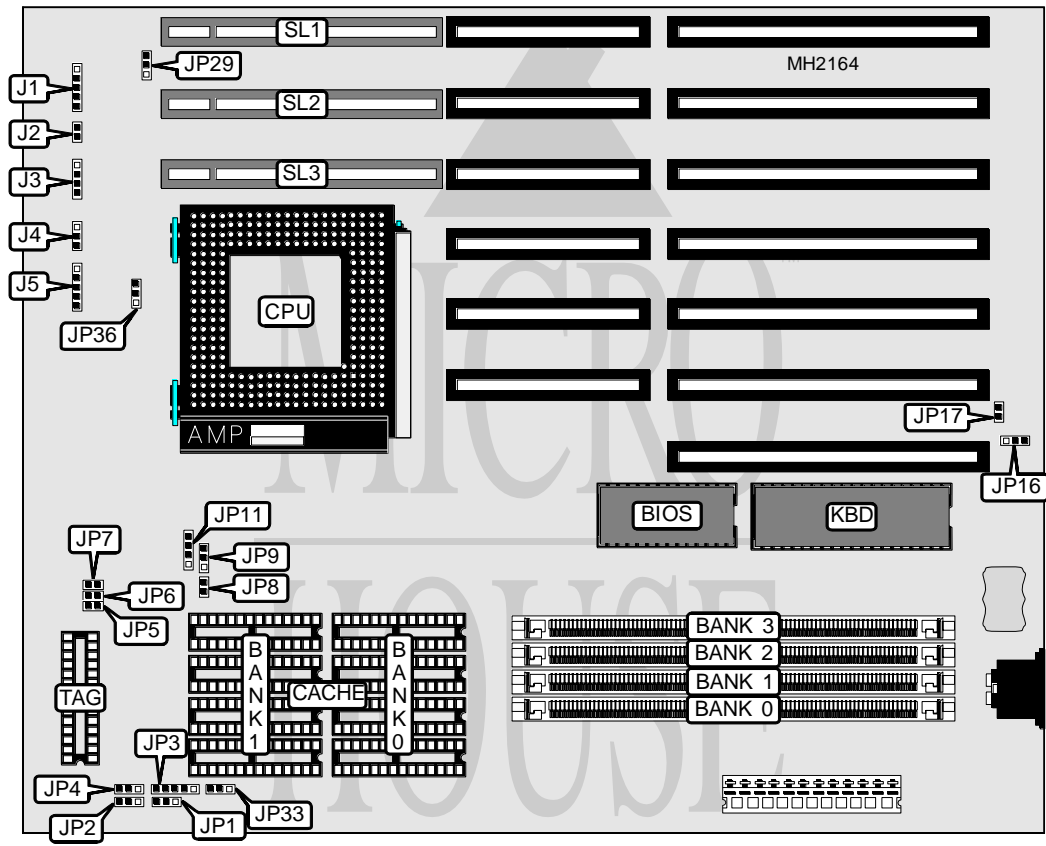


ADVANCED INTEGRATION RESEARCH, INC.

486SH REV. 3.0

Processor	80486SX/80487SX/ODP486SX/80486DX/80486DX2/80486DX4/Pentium Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	128MB
Cache	128/256/512KB
BIOS	AMI
Dimensions	254mm x 218mm
I/O Options	32-bit VESA local bus slots (3)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Turbo LED	J4
Reset switch	J2	Turbo switch	J5
Speaker	J3	32-bit VESA Local bus slots	SL1 - SL3

Continued on next page. . .

ADVANCED INTEGRATION RESEARCH, INC.
486SH REV. 3.0

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP16	pins 1 & 2 closed
CMOS memory clear	JP16	pins 2 & 3 closed
í Monitor type select VGA/EGA/monochrome	JP17	Open
Monitor type select CGA	JP17	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 512K x 36	NONE	NONE	NONE
4MB	(1) 512K x 36	(1) 512K x 36	NONE	NONE
4MB	(1) 1M x 36	NONE	NONE	NONE
6MB	(1) 512K x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 512K x 36	(1) 512K x 36	(1) 1M x 36	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 2M x 36	NONE	NONE	NONE
12MB	(1) 512K x 36	(1) 512K x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
16MB	(1) 2M x 36	(1) 2M x 36	NONE	NONE
16MB	(1) 4M x 36	NONE	NONE	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
18MB	(1) 512K x 36	(1) 4M x 36	NONE	NONE
20MB	(1) 512K x 36	(1) 512K x 36	(1) 4M x 36	NONE
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
24MB	(1) 512K x 36	(1) 512K x 36	(1) 1M x 36	(1) 4M x 36
24MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	NONE
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	NONE
32MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 8M x 36	NONE	NONE	NONE
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	NONE
36MB	(1) 1M x 36	(1) 8M x 36	NONE	NONE
36MB	(1) 512K x 36	(1) 512K x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
48MB	(1) 4M x 36	(1) 8M x 36	NONE	NONE
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	NONE
64MB	(1) 16M x 36	NONE	NONE	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	(1) 8M x 36	NONE	NONE
68MB	(1) 1M x 36	(1) 16M x 36	NONE	NONE
68MB	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36	NONE
72MB	(1) 1M x 36	(1) 1M x 36	(1) 16M x 36	NONE
72MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36

Continued on next page. . .

ADVANCED INTEGRATION RESEARCH, INC.

486SH REV. 3.0

... continued from previous page

DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
80MB	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36	NONE
80MB	(1) 4M x 36	(1) 16M x 36	NONE	NONE
96MB	(1) 4M x 36	(1) 4M x 36	(1) 16M x 36	NONE
96MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36
96MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	NONE
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36
128MB	(1) 16M x 36	(1) 16M x 36	NONE	NONE

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION					
Size	JP1	JP2	JP3	JP4	JP33
128KB	1 & 2	1 & 2	1 & 2, 3 & 4	1 & 2	2 & 3
256KB	2 & 3	2 & 3	2 & 3, 4 & 5	1 & 2	2 & 3
512KB	1 & 2	2 & 3	1 & 2, 3 & 4	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION				
Type	JP8	JP9	JP11	JP36
80486SX	pins 2 & 3 closed	Open	Open	pins 1 & 2 closed
80487SX	pins 1 & 2 closed	Closed	pins 2 & 3 closed	pins 1 & 2 closed
ODP486SX	pins 1 & 2 closed	Closed	pins 2 & 3 closed	pins 1 & 2 closed
80486DX/DX2	pins 1 & 2 closed	Closed	pins 3 & 4 closed	pins 1 & 2 closed
80486DX4	pins 1 & 2 closed	Closed	pins 3 & 4 closed	pins 2 & 3 closed
Pentium Overdrive	pins 1 & 2 closed	Closed	pins 2 & 3 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION				
Speed	JP5	JP6	JP7	JP29
25MHz	Closed	Closed	Open	pins 1 & 2 closed
33MHz	Open	Closed	Closed	pins 1 & 2 closed
40MHz	Closed	Open	Open	pins 2 & 3 closed
50iMHz	Closed	Closed	Open	pins 1 & 2 closed
50MHz	Open	Open	Closed	pins 2 & 3 closed
66iMHz	Open	Closed	Closed	pins 1 & 2 closed