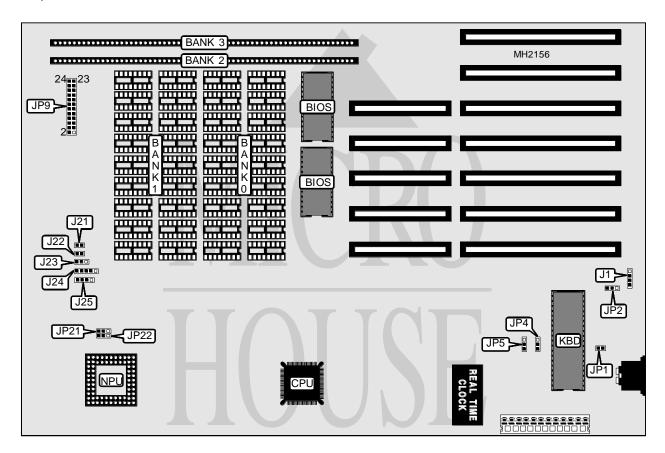
## UNIDENTIFIED SW-386SX

80386SX **Processor Processor Speed** 16/20MHz Chip Set Intel Max. Onboard DRAM 8MB Cache None

**BIOS** AMI/Phoenix **Dimensions** 330mm x 218mm

I/O Options None **NPU Options** 80387SX



| CONNECTIONS      |          |                     |          |
|------------------|----------|---------------------|----------|
| Purpose          | Location | Purpose             | Location |
| External battery | J1       | Turbo switch        | J23      |
| Turbo LED        | J21      | Power LED & keylock | J24      |
| Reset switch     | J22      | Speaker             | J25      |

Continued on next page. . .

## UNIDENTIFIED SW-386SX

. . . continued from previous page

| USER CONFIGURABLE SETTINGS          |        |                   |
|-------------------------------------|--------|-------------------|
| Function                            | Jumper | Position          |
| í Turbo switch enabled              | J23    | Open              |
| Turbo switch disabled               | J23    | pins 2 & 3 closed |
| í Factory configured - do not alter | JP1    | Open              |
| í CMOS memory normal operation      | JP2    | pins 1 & 2 closed |
| CMOS memory clear                   | JP2    | pins 2 & 3 closed |
| í BIOS type select AMI              | JP4    | pins 1 & 2 closed |
| BIOS type select Phoenix            | JP4    | pins 2 & 3 closed |
| í Monitor type select color         | JP5    | pins 1 & 2 closed |
| Monitor type select monochrome      | JP5    | pins 2 & 3 closed |

| DRAM CONFIGURATION (TYPE A) |            |            |              |              |
|-----------------------------|------------|------------|--------------|--------------|
| Size                        | Bank 0     | Bank 1     | Bank 2       | Bank 3       |
| 512KB                       | (18) 41256 | NONE       | NONE         | NONE         |
| 1MB                         | (18) 41256 | (18) 41256 | NONE         | NONE         |
| 2MB                         | (18) 41256 | (18) 41256 | (2) 256K x 9 | (2) 256K x 9 |
| N                           |            |            |              |              |

Type A -- base memory = banks 0 & 1 and extended memory = banks 2 & 3. Type B - base memory = banks Note: 2 & 3 and extended memory = banks 0 & 1.

| DRAM CONFIGURATION (TYPE B) |              |              |            |            |
|-----------------------------|--------------|--------------|------------|------------|
| Size                        | Bank 0       | Bank 1       | Bank 2     | Bank 3     |
| 512KB                       | (2) 256K x 9 | NONE         | NONE       | NONE       |
| 1MB                         | (2) 256K x 9 | (2) 256K x 9 | NONE       | NONE       |
| 2MB                         | (2) 1M x 9   | NONE         | NONE       | NONE       |
| 4MB                         | (2) 1M x 9   | (2) 1M x 9   | NONE       | NONE       |
| 8MB                         | (2) 1M x 9   | (2) 1M x 9   | (2) 1M x 9 | (2) 1M x 9 |

Type A -- base memory = banks 0 & 1 and extended memory = banks 2 & 3. Type B - base memory = banks Note: 2 & 3 and extended memory = banks 0 & 1.

| DRAM JUMPER CONFIGURATION  |  |  |  |
|--|--|--|--|
| JP9  |  |  |  |
| pins 1 & 2, 3 & 4, 5 & 6, 7 & 8, 9 & 10, 11 & 12, 13 & 14, 15 & 16, 1  |  |  |  |
| & 18, 19 & 20, 21 & 22, 23 & 24 closed                                 |  |  |  |
| pins 1 & 3, 2 & 4, 5 & 7, 6 & 8, 9 & 11, 10 & 12, 13 & 15, 14 & 16, 17 |  |  |  |
| & 19, 18 & 20, 21 & 23, 22 & 24 closed                                 |  |  |  |
|  |  |  |  |

Type A -- base memory = banks 0 & 1 and extended memory = banks 2 & 3. Type B - base memory = banks Note: 2 & 3 and extended memory = banks 0 & 1.

| DRAM MODE CONFIGURATION |                   |                   |  |
|-------------------------|-------------------|-------------------|--|
| Mode                    | JP21              | JP22              |  |
| F1                      | pins 1 & 2 closed | pins 2 & 3 closed |  |
| F4                      | pins 1 & 2 closed | pins 1 & 2 closed |  |
| W01                     | pins 2 & 3 closed | pins 1 & 2 closed |  |
| W02                     | pins 2 & 3 closed | pins 2 & 3 closed |  |