MULTI-TECH SYSTEMS, INC. MT386SBC/486SLC

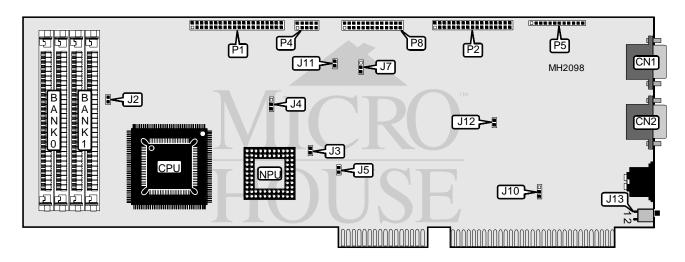
Processor 80386SX/CX486SLC

Processor Speed 25MHz **Chip Set UMC** Max. Onboard DRAM 16MB Cache None **BIOS** Phoenix

Dimensions 340mm x 105mm

I/O Options Floppy drive interface, IDE interface, parallel port, serial ports (2), VGA port

NPU Options 80387SX



CONNECTIONS				
Purpose	Location	Purpose	Location	
Serial port 1	CN1	Serial port 2	P4	
VGA port	CN2	Power LED & keylock	P5 pins 1-5	
IDE interface LED	J11	External switch reset	P5 pin 7	
Turbo LED	J12	Keyboard inhibit switch	P5 pin 8	
Hardware reset switch	J13	Speaker	P5 pins 9-10	
IDE interface	P1	Turbo switch	P5 pins 11-12	
Floppy drive interface	P2	Parallel port	P8	

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Pipeline control disabled	J2	Open
Pipeline control disabled	J2	Closed
í NPU asynchronous with CPU	J3	Open
NPU synchronous with CPU	J3	Closed
í IDE interface enabled	J4	pins 1 & 2 closed
IDE interface disabled	J4	pins 2 & 3 closed
í Hardware reset when DCD on COM1 goes from on to off enabled	J5	Closed
Hardware reset when DCD on COM1 goes from on to off disabled	J5	Open
í Parallel port unidirectional (printer)	J7	pins 2 & 3 closed
Parallel port bidirectional	J7	pins 1 & 2 closed
í IRQ9 connected to AT bus	J10	pins 2 & 3 closed
IRQ9 connected to internal VGA	J10	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
512KB	(2) 256K x 9	NONE		
1MB	(2) 256K x 9	(2) 256K x 9		
4MB	(2) 1M x 9	(2) 1M x 9		
8MB	(2) 4M x 9	NONE		
16MB	(2) 4M x 9	(2) 4M x 9		