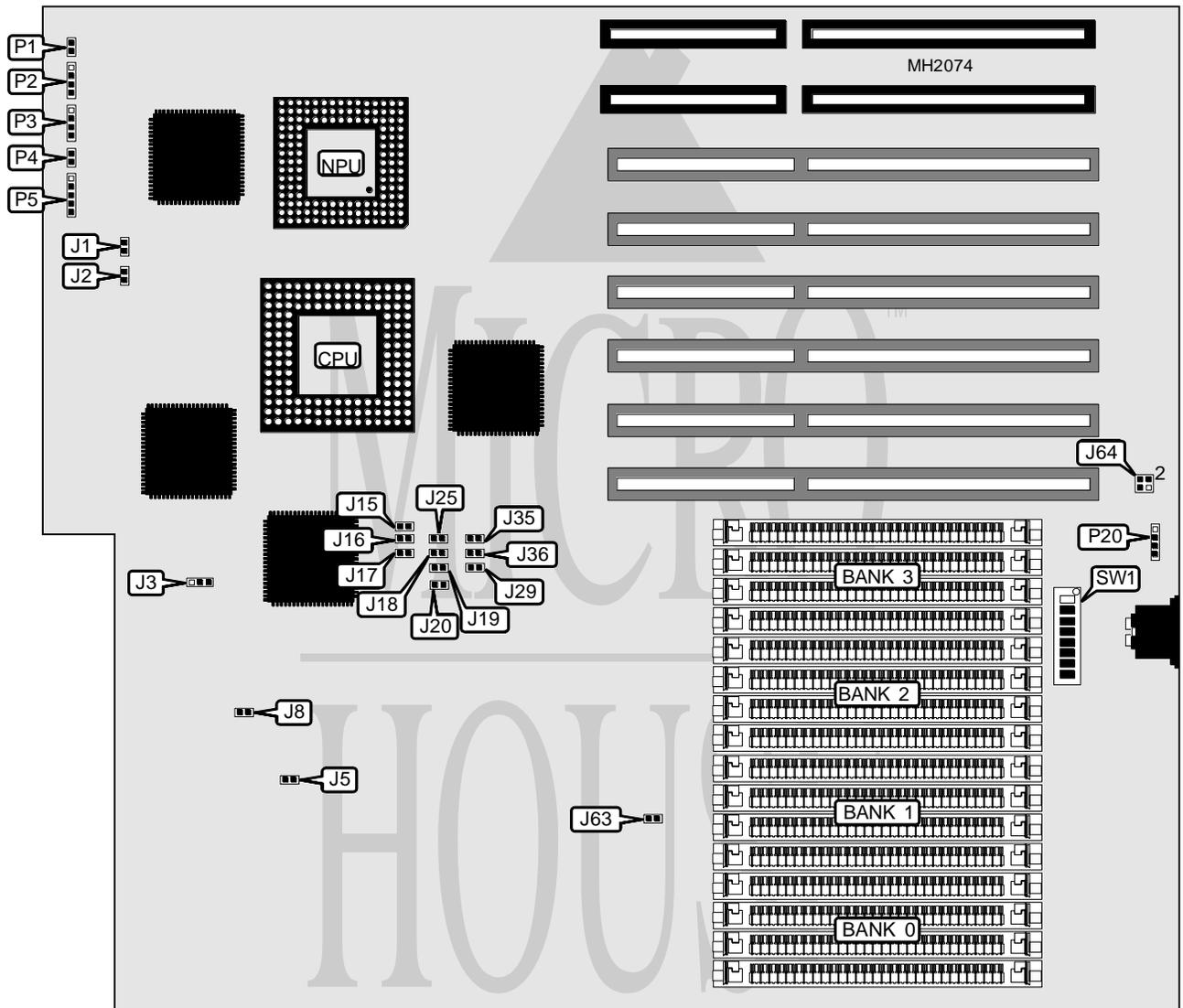


INFORMTECH INTERNATIONAL, INC.

IT486EX VER. 1

Processor	80486SX/80486DX
Processor Speed	25/33MHz
Chip Set	Intel
Max. Onboard DRAM	64MB
Cache	64/128KB (with external cache card)
BIOS	Phoenix
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167



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CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	P1	Reset switch	P4
Turbo LED	P2	Power LED & keylock	P5
Speaker	P3	External battery	P20

USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Cache write protect disabled	J5	Closed
Cache write protect enabled	J5	Open
í On board DRAM access enabled	J8	Closed
On board DRAM access disabled	J8	Open
í I/O recovery time select 8-bit = 11BLCK, 16-bit = 3BLCK	J16	Closed
No recovery time	J16	Open
í 80486 internal cache enabled	J17	Closed
80486 internal cache disabled	J17	Open
í Turbocache486 enabled	J20	Closed
Turbocache486 disabled	J20	Open
í BIOS duplicated (to 00FF0000 hex)	J36	Closed
BIOS not duplicated	J36	Open
í Turbo mode enabled	P1	Open
Turbo mode disabled	P1	Closed
í Factory configured - do not alter	SW1/7	Off
í Monitor type select monochrome	SW1/8	Off
Monitor type select color	SW1/8	On

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
10MB	(4) 1M x 9	(4) 1M x 9	(4) 256K x 9	(4) 256K x 9
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
34MB	(4) 4M x 9	(4) 4M x 9	(4) 256K x 9	(4) 256K x 9
34MB	(4) 256K x 9	(4) 256K x 9	(4) 4M x 9	(4) 4M x 9
40MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9

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MEMORY BANK CONFIGURATION (BANK 0 & 1)		
SIMM Type	SW1/1	SW1/2
No memory installed	Off	Off
256KB	On	Off
1MB	Off	On
4MB	On	On

MEMORY BANK CONFIGURATION (BANK 2 & 3)		
SIMM Type	SW1/3	SW1/4
No memory installed	Off	Off
256KB	On	Off
1MB	Off	On
4MB	On	On

MEMORY SPEED/CPU MHz CONFIGURATION (25MHz)		
SIMM Type	SW1/5	SW1/6
256KB	Off	Off
1MB	On	On
4MB	On	On

Note: SW1/5 corresponds to Banks 0 & 1 and SW1/6 corresponds to Banks 2 & 3.

MEMORY SPEED/CPU MHz CONFIGURATION (33MHz)		
SIMM Type	SW1/5	SW1/6
256KB	Off	Off
1MB	On	On
4MB	Off	Off

Note: SW1/5 corresponds to Banks 0 & 1 and SW1/6 corresponds to Banks 2 & 3.

TURBOCACHE486 CONFIGURATION			
Module	J17	J18	J19
82485ES	Open	Open	Open
82485MA/MB	Closed	Closed	Closed

TURBOCACHE486 JUMPER CONFIGURATION				
Module	J1	J2	J29	J63
82485ES	Open	Open	Open	Open
82485MA/MB	Closed	Closed	Closed	Closed

CPU SPEED CONFIGURATION		
Speed	J3	J15
25MHz	pins 2 & 3 closed	Closed
33MHz	pins 1 & 2 closed	Open

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BATTERY TYPE CONFIGURATION	
Battery type	J64
4.5V (without internal current limiting resistor)	pins 3 & 4 closed
3.6V-4.5V (with internal current limiting resistor)	pins 1 & 3 closed
6V (without internal current limiting resistor)	pins 2 & 4 closed
6V (with internal current limiting resistor)	pins 1 & 2 closed

BIOS CONTROL CONFIGURATION		
ROM type	J25	J35
28F512	Closed	Closed
28F010	Closed	Open
27C512	Open	Closed
27C010	Open	Open