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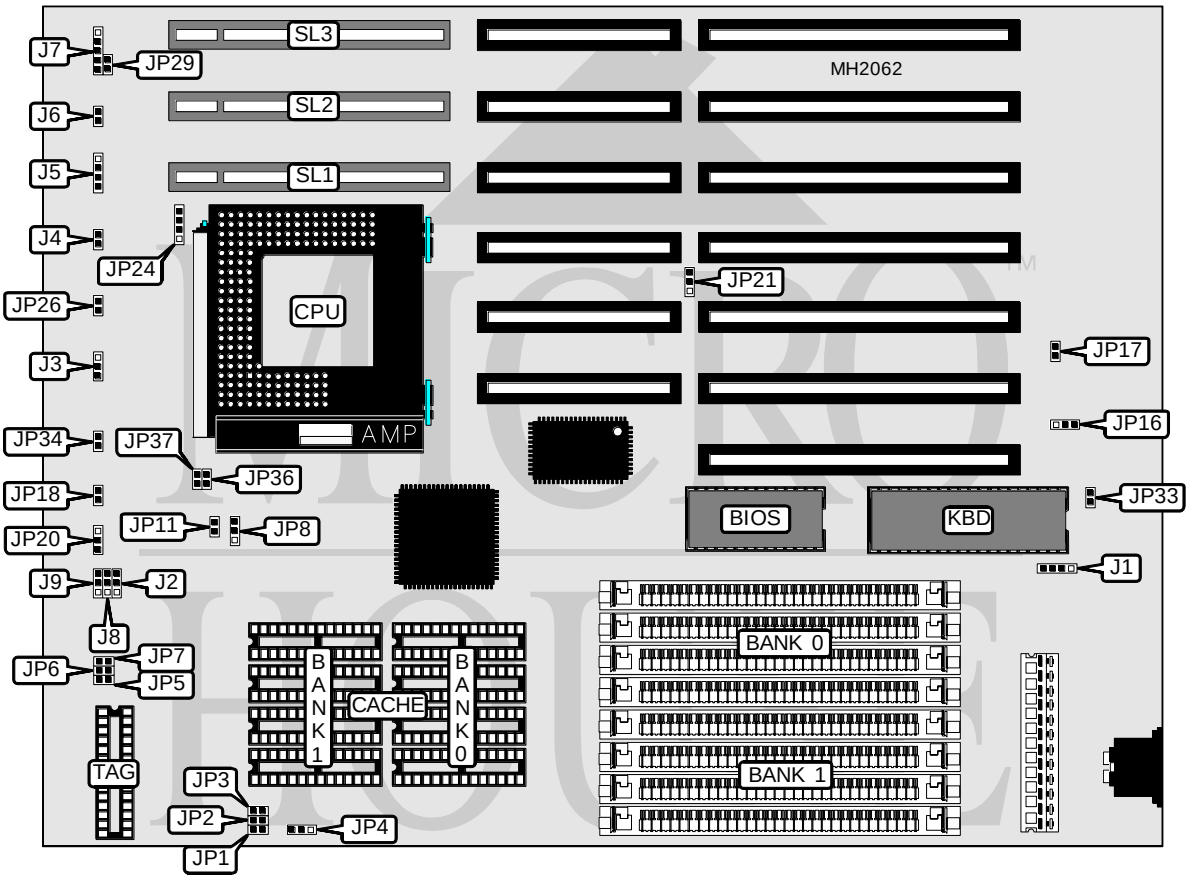
G A - 4 8 6 V F

Processor	80486SX/80487SX/80486DX/80486DX2/80486DX4/Pentium Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz
Chip Set	SiS
Max. Onboard DRAM	128MB
Cache	64/128/256/512KB
BIOS	AWARD
Dimensions	230mm x 218mm
I/O Options	32-bit VESA local bus slots (3), green PC feature
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Power LED & keylock	J7
Turbo switch	J3	External power	JP33
Turbo LED	J4	LED connector	JP34
Speaker	J5	32-bit VESA Local bus slots	SL1-SL3
Reset switch	J6		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
Turbo mode enabled	J3	pins 2 & 3 closed
Turbo mode disabled	J3	pins 1 & 2 closed
80486DX2, DX or OverDrive installed	JP8	pins 1 & 2 closed
80486SX installed	JP8	pins 2 & 3 closed
í Non AMD CPU	JP11	Closed
AMD CPU	JP11	Open
í For Cyrix CPU installed	JP18	Open
For normal operation	JP18	Closed
í Normal CPU ADS# operation	JP20	pins 2 & 3 closed
Delay CPU ADS# clock operation	JP20	pins 1 & 2 closed
Cyrix CPU in use	*JP21	pins 1 & 2 closed
Other CPU in use	*JP21	pins 2 & 3 closed
í DX4CPU x 2	JP24	pins 3 & 4 closed
í DX4CPU x 3	JP24	Open
í Cyrix CPU	JP24	pins 2 & 3 closed
CMOS memory normal operation	JP16	pins 1 & 2 closed
CMOS memory clear	JP16	pins 2 & 3 closed
Green PC mode disabled	JP26	Open
Green PC mode enabled	JP26	Closed
í Monitor type select CGA	JP17	Closed
Monitor type select other	JP17	Open
Note: *This option applies to Revision 6 only.		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
65MB	(4) 256K x 9	(4) 16M x 9
68MB	(4) 1M x 9	(4) 16M x 9
80MB	(4) 4M x 9	(4) 16M x 9
128MB	(4) 16M x 9	(4) 16M x 9

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CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8/(1) 32K x 8
256KB	(4) 64K x 8	NONE	(1) 16K x 8/(1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP1	JP2	JP3	JP4
64KB	Open	Open	Open	N/A
128KB	Closed	Open	Open	pins 1 & 2 closed
256KB	Closed	Closed	Open	pins 2 & 3 closed
256KB	Closed	Closed	Open	pins 1 & 2 closed
512KB	Closed	Closed	Closed	pins 1 & 2 closed

VL-BUS SPEED CONFIGURATION	
CPU Type	JP29
DX-50/40 MHz	Closed
Other Speed	Open

CPU VOLTAGE SELECTION		
CPU Type	JP36	JP37
5 Voltage CPU	Closed	Closed
3.3 Voltage DX4 CPU	Open	Open

CPU SPEED SELECTION			
Speed	JP5	JP6	JP7
25MHz	Open	Open	Closed
33MHz	Closed	Closed	Closed
40MHz	Open	Closed	Closed
50MHz	Closed	Open	Open
50iMHz	Open	Open	Closed
66iMHz	Closed	Closed	Closed
75iMHz	Open	Open	Closed
100iMHz	Closed	Closed	Closed

DELAY LOCAL BUS CLOCK			
	J2 (VESA1)	J8(VESA2)	J9(VESA3)
íFor normal operation	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
DX-33, DX-40, DX-50, DX2-66, DX4-75, DX4-100*	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
Note: *For some VL-Bus interface cards that need more address setup time in above listed speeds.			