## **OLIVETTI** P500

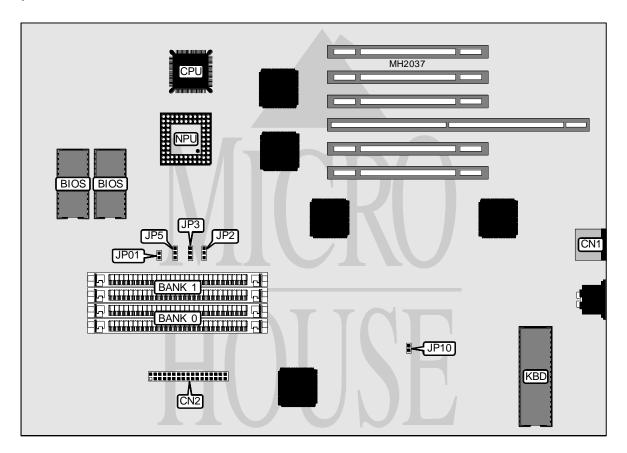
80386SX **Processor Processor Speed** 16MHz

Chip Set **Texas Instruments** 

Max. Onboard DRAM 4MB Cache None **BIOS** Olivetti

**Dimensions** 330mm x 218mm I/O Options Floppy drive interface

**NPU Options** 80387SX



CONNECTIONS				
Purpose	Location	Purpose	Location	
PS/2 mouse port	CN1	Floppy drive interface	CN2	

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USER CONFIGUE	USER CONFIGURABLE SETTINGS				
Function	Jumper	Position			
í SIMMS installed Bank 0	JP01	Open			
SIMMS installed Bank 0 & Bank 1	JP01	Closed			
í SIMM type select 1M x 9	JP5	pins 1 & 2 closed			
SIMM type select 256K x 9	JP5	pins 2 & 3 closed			
í CMOS memory clear	JP10	Open			
CMOS memory normal operation	JP10	Closed			

	DRAM CONFIGURATION				
Size	Bank 0	Bank 1			
1MB	(2) 256K x 9	(2) 256K x 9			
2MB	(2) 1M x 9	NONE			
4MB	(2) 1M x 9	(2) 1M x 9			

DRAM JUMPER CONFIGURATION				
Size	JP2 JP3			
1MB installed	pins 1 & 2 closed	pins 1 & 2 closed		
2MB or 4MB installed	pins 2 & 3 closed	pins 2 & 3 closed		