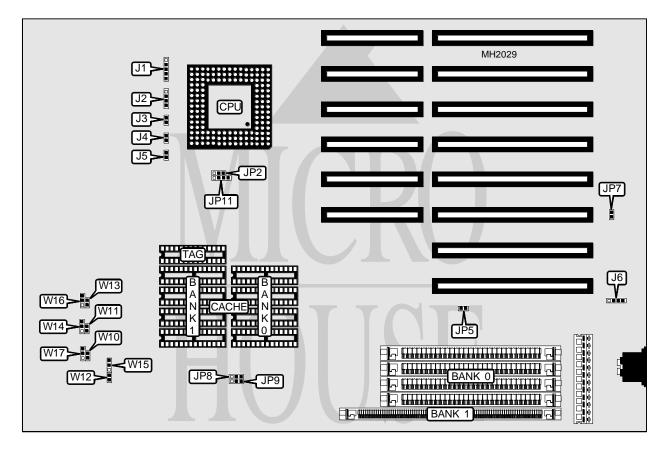
RELIALOGIC CORPORATION PRIVATE, LTD. R486DX 33 MIBBC

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options
NPU Options

804865X/804875X/80486DX/80486DX2 25/33/50(internal)/66(internal)MHz UMC 32MB 64/128/256KB AMI 330mm x 218mm None None



CONNECTIONS				
Purpose	Location	Purpose	Location	
Power LED & keylock	J1	Turbo switch	J4	
Speaker	J2	Reset switch	J5	
Turbo LED	J3	External battery	J6	

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í Monitor type select CGA	JP5	Closed		
Monitor type select monochrome EGA/VGA	JP5	Open		
í Battery type select internal	JP7	Closed		
Battery type select external	JP7	Open		
í Factory configured - do not alter	JP8	Open		
í Factory configured - do not alter	JP9	pins 1 & 2 closed		

DRAM CONFIGURATION					
Size	Bank 0	Bank 1			
1MB	(4) 256K x 9	NONE			
2MB	(4) 256K x 9	(1) 256K x 36			
4MB	(4) 1M x 9	NONE			
5MB	(4) 256K x 9	(1) 1M x 36			
8MB	(4) 1M x 9	(1) 1M x 36			
16MB	(4) 4M x 9	NONE			
20MB	(4) 1M x 9	(1) 4M x 36			
32MB	(4) 4M x 9	(1) 4M x 36			

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	
64KB	(4) 8K x 8	(4) 8K x 8	(2) 16K x 4	
128KB	(4) 32K x 8	NONE	(2) 16K x 4	
256KB	(4) 32K x 8	(4) 32K x 8	(2) 16K x 4	

CACHE JUMPER CONFIGURATION								
Size	W10	W11	W12	W13	W14	W15	W16	W17
64KB	Open	Open	Open	Open	1&2	1&2	1&2	Open
128KB	Closed	Open	Closed	Open	1&2	1&2	2&3	1&2
256KB	Closed	Closed	Closed	Closed	2&3	2&3	1&2	2&3
Note: Pins designated should be in the closed position.								

CPU TYPE CONFIGURATION					
Туре	JP2	JP11			
80486SX	Open	pins 2 & 3 closed			
80487SX	pins 1 & 2 closed	pins 1 & 2, 3 & 4 closed			
80486DX/DX2	pins 2 & 3 closed	pins 1 & 2, 3 & 4 closed			