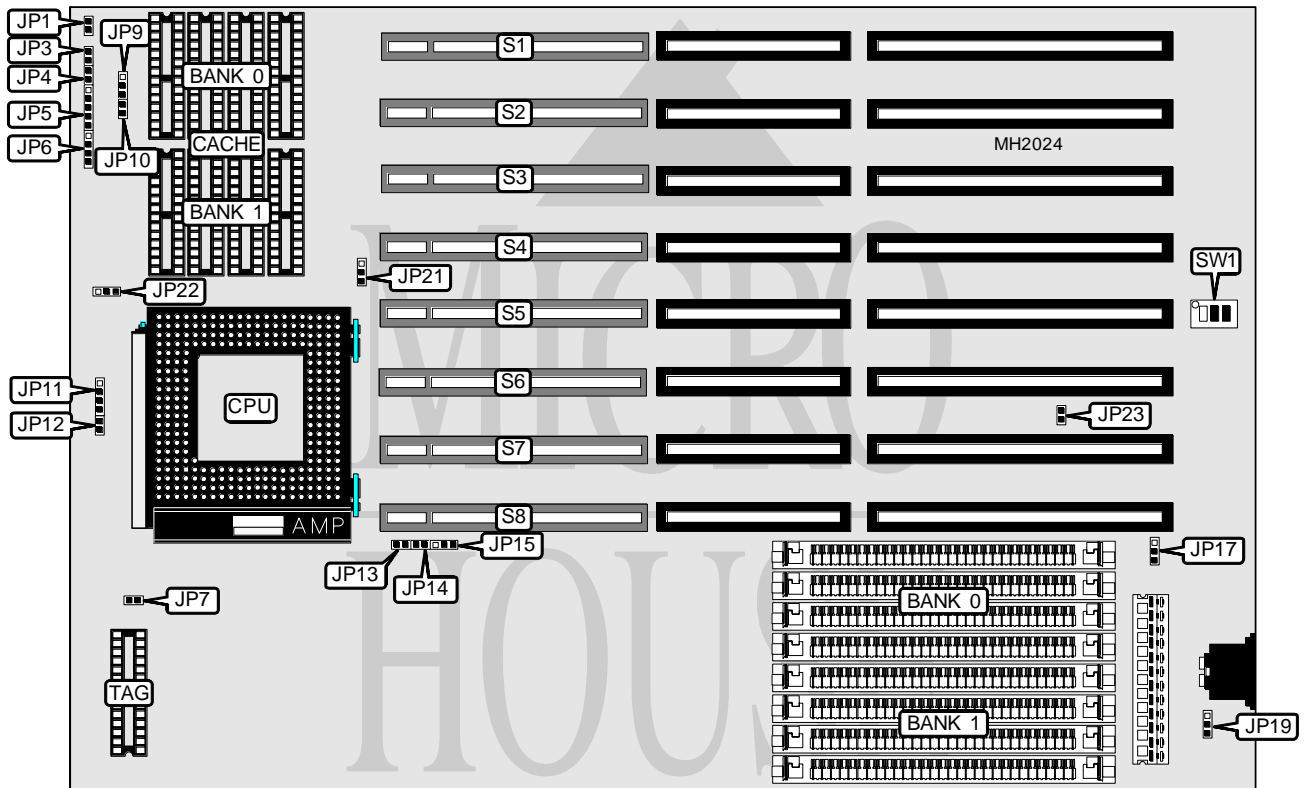


AMPTROM INTERNATIONAL, INC.

DX-9000

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	25/33/40/50(internal)/50/66(internal)/MHz
Chip Set	Hint
Max. Onboard DRAM	128MB
Cache	256/1024KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (8)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	JP1	Power LED & keylock	JP5
Turbo LED	JP3	Speaker	JP6
Turbo switch	JP4	32-bit VESA Local bus slots	S1 - S8

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DX-9000

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP17	pins 1 & 2 closed
CMOS memory clear	JP17	Open
í Factory configured - do not alter	JP19	pins 1 & 2 closed
í Factory configured - do not alter	JP21	pins 2 & 3 closed
í Non P9000 graphics card installed	JP22	pins 1 & 2 closed
P9000 graphics card installed	JP22	pins 2 & 3 closed
í Factory configured - do not alter	JP23	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
68MB	(4) 1M x 9	(4) 16M x 9
68MB	(4) 16M x 9	(4) 1M x 9
80MB	(4) 16M x 9	(4) 4M x 9
80MB	(4) 4M x 9	(4) 16M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 128K x 8

CACHE JUMPER CONFIGURATION			
Size	JP7	JP9	JP10
256KB	Open	pins 1 & 2 closed	Open
1MB	Closed	pins 2 & 3 closed	Open

CPU TYPE CONFIGURATION		
Type	JP11	JP12
80486SX	pins 2 & 3 closed	Open
80487SX	pins 1, 2, 3 & 4 closed	pins 2 & 3 closed
80486DX/DX2	pins 1, 2, 3 & 4 closed	pins 1 & 2 closed

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CPU SPEED CONFIGURATION			
Speed	SW1/1	SW1/2	SW1/3
25MHz	Off	Off	On
33MHz	On	Off	Off
40MHz	On	On	Off
50iMHz	Off	Off	On
50MHz	Off	On	Off
66iMHz	On	Off	Off

VESA WAIT STATE/BUS SPEED CONFIGURATION				
CPU speed	Wait states	JP13 (ID3)	JP14 (ID2)	JP15
< 33MHz	0 wait states	Open	Open	pins 1 & 2 closed
> 33MHz	1 wait state	Closed	Closed	pins 2 & 3 closed