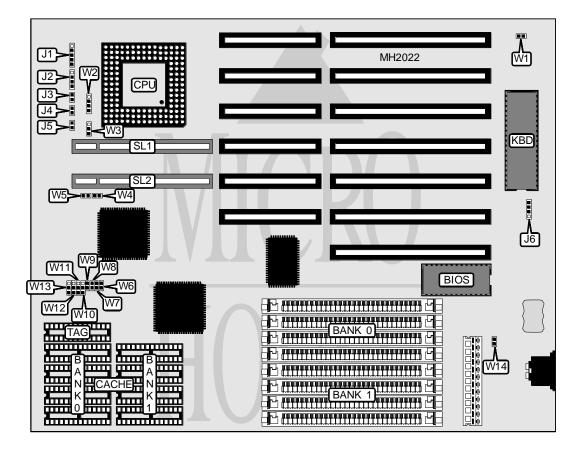
INFORMTECH INTERNATIONAL, INC. 486 V L

80486SX/80487SX/ODP486SX/80486DX/80486DX2 Processor **Processor Speed** 25/33/50(internal)/66(internal)MHz UMC Chip Set Max. Onboard DRAM 32MB 64/128/256KB Cache BIOS AMI Dimensions 330mm x 218mm I/O Options None **NPU Options** None



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1 Reset switch		J5
Speaker	J2	External battery	J6
Turbo switch	J3	32-bit VESA local bus slots	SL1 & SL2
Turbo LED	J4		

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USER CONFIGURABLE SETTINGS			
Function Jumper Position			
í Monitor type select color	W1	Closed	
Monitor type select monochrome	W1	Open	
í CMOS memory normal operation	W14	Closed	
CMOS memory clear	W14	Open	

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size Bank 0 Bank 1 TAG				
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	
128KB	(4) 32K x 8	NONE	(1) 8K x 8	
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	

CACHE JUMPER CONFIGURATION				
Size W6 W7 W8 W9				
64KB	Open	Open	Open	Open
128KB	Open	Closed	Open	Open
256KB	Closed	Closed	Closed	Closed

CACHE JUMPER CONFIGURATION (CON'T)				
Size W10 W11 W12 W13				
64KB	pins 1 & 2 closed	Off	pins 1 & 2 closed	pins 1 & 2 closed
128KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
256KB	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed

	CPU TYPE CONFIGURATION	
СРИ Туре	W2	W3
80486SX	pins 2 & 3 closed	Open
80487SX	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed
ODP486SX	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed
80486DX	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed
80486DX2	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed

VESA WAIT STATE/BUS SPEED CONFIGURATION			
CPU speed	Wait states	W4	W5
< 33MHz	0 wait states	Open	Open
> 33MHz	1 wait state	Closed	Closed

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