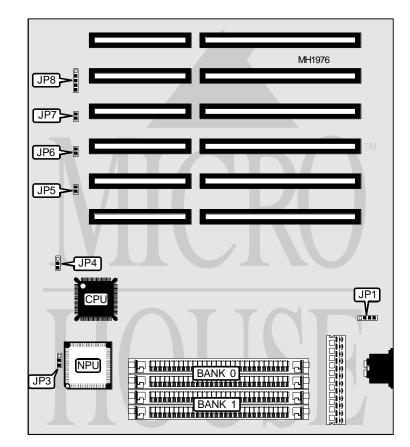
ASLAN COMPUTER CORPORATION S 6 3 0 1 / S 6 4 0 1

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options
NPU Options

803865X/CX486SLC 25/33/40MHz SARC 16MB None AMI 220mm x 163mm None 803875X



CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	JP1	Turbo LED	JP7	
Turbo switch	JP5	Power LED & keylock	JP8	
Reset switch	JP6			

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... continued from previous page.

USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í NPU synchronous with CPU	JP3	pins 1 & 2 closed		
NPU asynchronous with CPU	JP3	pins 2 & 3 closed		
í CPU speed select 25/33 MHz	JP4	pins 2 & 3 closed		
CPU speed select 40 MHz	JP4	pins 1 & 2 closed		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
512K	(2) 256K x 9	NONE		
1MB	(2) 256K x 9	(2) 256K x 9		
1MB	(2) 512K x 9	NONE		
2MB	(2) 512K x 9	(2) 512K x 9		
2MB	(2) 1M x 9	NONE		
4MB	(2) 1M x 9	(2) 1M x 9		
8MB	(2) 4M x 9	NONE		
16MB	(2) 4M x 9	(2) 4M x 9		