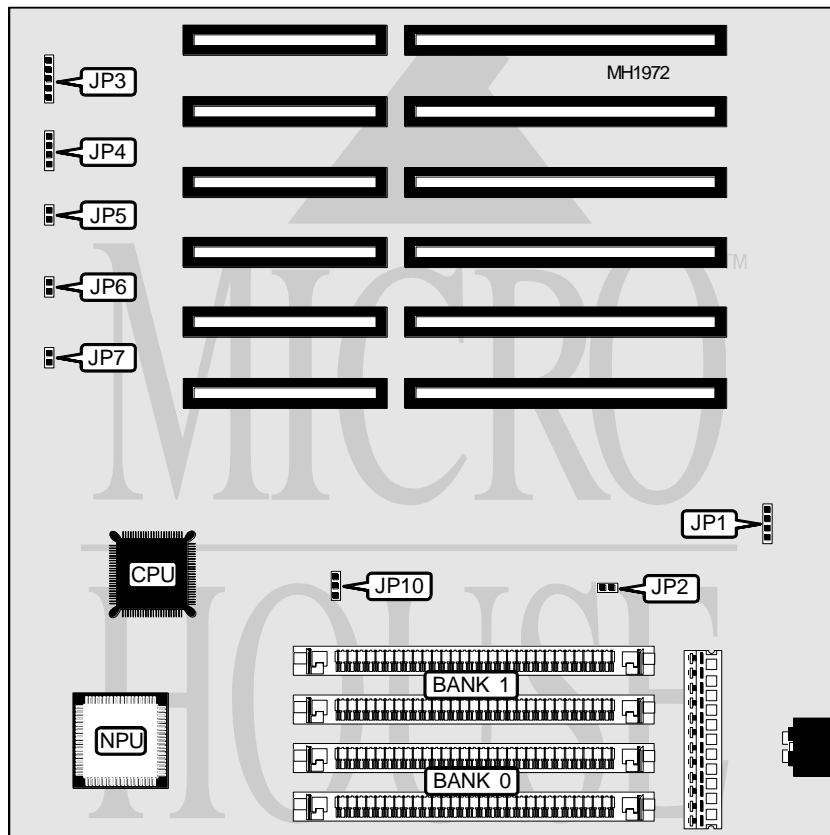


# AMPTRON INTERNATIONAL, INC.

## P 9 - 6 5 0 C

<b>Processor</b>	80386SX
<b>Processor Speed</b>	40MHz
<b>Chip Set</b>	Unidentified
<b>Max. Onboard DRAM</b>	16MB
<b>Cache</b>	None
<b>BIOS</b>	AMI
<b>Dimensions</b>	220mm x 220mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Turbo LED	JP5
Power LED & keylock	JP3	Turbo switch	JP6
Speaker	JP4	Reset switch	JP7

Continued next page...

# AMPTRON INTERNATIONAL, INC.

## P9-650C

... continued from previous page.

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal (internal battery)	JP1	pins 2 & 3 closed
CMOS memory normal (external battery)	JP1	pins 1 & 4 closed
CMOS memory clear	JP1	pins 3 & 4 closed
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open
í CPU speed select 40MHz	JP10	pins 2 & 3 closed
CPU speed select 33MHz	JP10	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512K	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	(2) 4M x 9
16MB	(2) 4M x 9	(2) 4M x 9