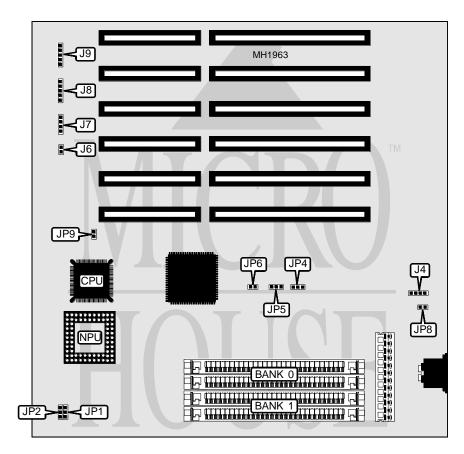
DIAMOND FLOWER, INC. 3865X-25/33/40ANE

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
BIOS
Dimensions
NPU Options

803865X/CX4865LC 25/33/40MHz ALI 16MB AMI 220mm x 170mm 80387SX



CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	J4	Turbo LED/Turbo switch	18	
Reset switch	J6	Power LED & keylock	19	
Speaker	J7			

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í Synchronous with CPU	JP2	pins 1 & 2 closed		
Asynchronous with CPU	JP2	pins 2 & 3 closed		
í Monitor type select color	JP4	pins 2 & 3 closed		
Monitor type select monochrome	JP4	pins 1 & 2 closed		
í Parity check disabled	JP6	Closed		
Parity check enabled	JP6	Open		
í CMOS memory normal operation (External Battery)	JP8	Open		
CMOS memory normal operation (Internal Battery)	JP8	Closed		
CPU speed select (33MHz)	JP9	Closed		
CPU speed select (40MHz)	JP9	Open		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
512KB	(2) 256K x 9	NONE		
1MB	(2) 256K x 9	(2) 256K x 9		
2MB	(2) 1M x 9	NONE		
4MB	(2) 1M x 9	(2) 1M x 9		
8MB	(2) 4M x 9	NONE		
16MB	(2) 4M x 9	(2) 4M x 9		

	CPU SPEED SELECT	
Speed	JP1	JP5
25MHz	pins 1 & 2 closed	pins 2 & 3 closed
33MHz	pins 2 & 3 closed	pins 2 & 3 closed
40MHz	pins 1 & 2 closed	pins 1 & 2 closed