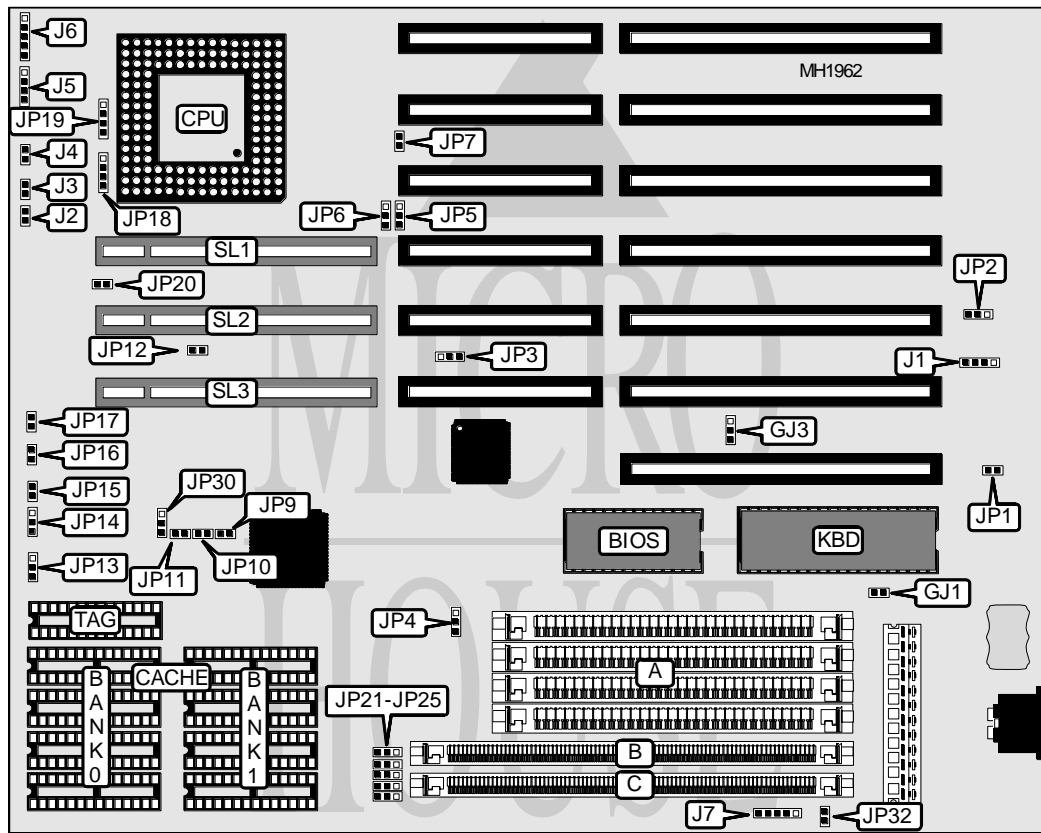


PINE TECHNOLOGY P T - 4 2 9 G

Processor 80486SX/Cx487S/80486DX/80486DX2/Pentium Overdrive
Processor Speed 25/33/40/50/50(internal)/66(internal)MHz
Chip Set C & T
Max. Onboard DRAM 48MB
Cache 32/64/128/256KB
BIOS AMI
Dimensions 220mm x 250mm
I/O Options 32-bit VESA local bus slots (3)
NPU Options None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Power LED & keylock	J6
Reset switch	J2	Green AUX # 1 connector	J7
Turbo switch	J3	Green AUX # 2 connector	JP32
Turbo LED	J4	32 bit VESA slots	SL1, SL2, SL3
Speaker	J5		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í On board battery connect	JP1	Closed
On board battery disconnect	JP1	Open
í External power good	JP2	pins 1 & 2 closed
Internal power good	JP2	pins 2 & 3 closed
í Normal VGA card installed	JP3	pins 1 & 2 closed
POWER-9000 VGA card installed	JP3	pins 2 & 3 closed
í Normal 3 VESA master	JP4	pins 2 & 3 closed
Enable Cx487S & MCA only for master mode	JP4	pins 1 & 2 closed
í Enable CPU (486DX2) test logic	JP7	Open
Disable CPU (486DX2) test logic	JP7	Closed
í Normal operation	GJ1	Open
Enable green function	GJ1	Closed
í AMI MEGAKEY keyboard BIOS	GJ3	Open
Phoenix MULTIKEY keyboard BIOS	GJ3	Closed

DRAM CONFIGURATION			
Size	Bank A	Bank B	Bank C
1MB	(4) 256K x 9	NONE	NONE
1MB	NONE	(1) 256K x 36	NONE
1MB	NONE	NONE	(1) 256K x 36
2MB	(4) 256K x 9	(1) 256K x 36	NONE
2MB	(4) 256K x 9	NONE	(1) 256K x 36
2MB	NONE	(1) 256K x 36	(1) 256K x 36
3MB	(4) 256K x 9	(1) 256K x 36	(1) 256K x 36
4MB	NONE	(1) 1M x 36	NONE
4MB	NONE	NONE	(1) 1M x 36
4MB	(4) 1M x 9	NONE	NONE
5MB	(4) 1M x 9	(1) 256K x 36	NONE
5MB	(4) 1M x 9	NONE	(1) 256K x 36
5MB	(4) 256K x 9	(1) 1M x 36	NONE
5MB	(4) 256K x 9	NONE	(1) 1M x 36
6MB	(4) 1M x 9	(1) 256K x 36	(1) 256K x 36
6MB	(4) 256K x 9	(1) 1M x 36	(1) 256K x 36
6MB	(4) 256K x 9	(1) 256K x 36	(1) 1M x 36
8MB	NONE	(1) 1M x 36	(1) 1M x 36
8MB	(4) 1M x 9	(1) 1M x 36	NONE
8MB	(4) 1M x 9	NONE	(1) 1M x 36
9MB	(4) 1M x 9	(1) 1M x 36	(1) 256K x 36
9MB	(4) 1M x 9	(1) 256K x 36	(1) 1M x 36
12MB	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36

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DRAM CONFIGURATION (CON'T)			
Size	Bank A	Bank B	Bank C
18MB	(4) 256K x 9	(1) 4M x 36	(1) 256K x 36
18MB	(4) 256K x 9	(1) 256K x 36	(1) 4M x 36
18MB	(4) 4M x 9	(1) 256K x 36	(1) 256K x 36
21MB	(4) 1M x 9	(1) 4M x 36	(1) 256K x 36
21MB	(4) 1M x 9	(1) 256K x 36	(1) 4M x 36
24MB	(4) 1M x 9	(4) 1M x 36	(1) 4M x 36
24MB	(4) 1M x 9	(1) 4M x 36	(4) 1M x 36
33MB	(4) 256K x 9	(1) 4M x 36	(1) 4M x 36
36MB	(4) 1M x 9	(1) 4M x 36	(1) 4M x 36
48MB	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36

8 BIT SIMM (BANK A) CONFIGURATION	
BANK	JP21
Bank 0	pins 1 & 2 closed
Bank 2	pins 2 & 3 closed

SINGLE 32 BIT SIMM (BANK B or C) CONFIGURATION				
Bank	Bank B = Bank 0 or Bank 1		Bank C = Bank 1 or Bank 2	
	JP22	JP23	JP24	JP25
Bank 0	pins 1 & 2 closed	Open	N/A	N/A
Bank 1	pins 2 & 3 closed	Open	pins 1 & 2 closed	Open
Bank 2	N/A	N/A	pins 2 & 3 closed	Open

DOUBLE 32 BIT SIMM (BANK B or C) CONFIGURATION				
Bank	Bank B = Bank 0 & 1 Bank 1 & 2		Bank C = Bank 1 & 2 or Bank 2 & 3	
	JP22	JP23	JP24	JP25
Bank 0	pins 1 & 2 closed	pins 1 & 2 closed	N/A	N/A
Bank 1	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
Bank 2	N/A	N/A	pins 2 & 3 closed	pins 2 & 3 closed

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	8K x 8
128KB	(4) 32K x 8	NONE	8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	32K x 8

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CACHE JUMPER CONFIGURATION					
Size	JP13	JP14	JP15	JP16	JP17
32KB	Open	pins 2 & 3	Open	Open	Open
64KB	Open	pins 1 & 2	Open	Closed	Open
128KB	pins 2 & 3	pins 2 & 3	Open	Closed	Closed
256KB	pins 1 & 2	pins 1 & 2	Closed	Closed	Closed

Note: Pins designated should be in the closed position.

AV9107-05 CLOCK CONFIGURATION			
Speed	JP10	JP11	JP30
25MHz	Closed	Open	pins 2 & 3 closed
33MHz	Open	Closed	pins 2 & 3 closed
40MHz	Open	Open	pins 2 & 3 closed
50iMHz	Closed	Open	pins 2 & 3 closed
50MHz	Closed	Open	pins 1 & 2 closed
66iMHz	Open	Closed	pins 2 & 3 closed

CH9007E CLOCK CONFIGURATION			
Speed	JP9	JP10	JP11
25MHz	Closed	Closed	Closed
33MHz	Open	Closed	Closed
40MHz	Closed	Open	Closed
50iMHz	Closed	Closed	Closed
50MHz	Open	Open	Open
66iMHz	Open	Closed	Closed

MX8315 CLOCK CONFIGURATION			
Speed	JP9	JP10	JP11
25MHz	Open	Open	Closed
33MHz	Closed	Closed	Closed
40MHz	Open	Closed	Closed
50iMHz	Open	Open	Closed
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Closed

CPU TYPE CONFIGURATION		
CPU Type	JP18	JP19
80486SX	pins 2 & 3 closed	Open
80486DX	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed
80486DX2	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed
Pentium Overdrive	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed

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VESA DELAY CONFIGURATION		
50 MHz Delay	JP5	JP6
No delay	pins 1 & 2 closed	pins 1 & 2 closed
1-T state	pins 2 & 3 closed	pins 2 & 3 closed

VESA WAIT STATE CONFIGURATION			
CPU speed	Wait states	JP12	JP20
< 33MHz	0 wait states	Open	Open
> 33MHz	1 wait state	Closed	Closed