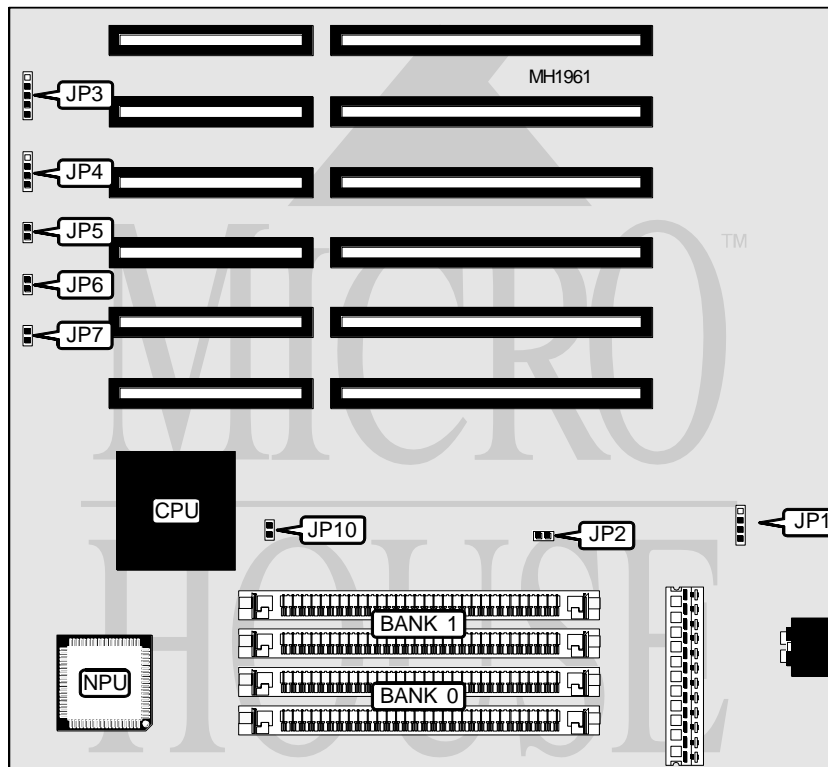


AMPTRON INTERNATIONAL , INC.

SLC - 650C

Processor	CX486SLC/CX486SLC2
Processor Speed	33/40/50MHz
Chip Set	SARC
Max. Onboard DRAM	16MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Speaker	JP4
Power LED & keylock	JP3	Turbo switch	JP6
Turbo LED connector	JP5	Reset switch	JP7

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SLC-650C

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
Monitor type select monochrome	JP2	Open
Monitor type select color	JP2	Closed
í Use rechargeable batteries	JP1	pins 2 & 3 closed
Clear CMOS	JP1	pins 3 & 4 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512 K	(2) 256K x 9	NONE
1M	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

CPU SPEED CONFIGURATION	
Speed	JP10
33MHz	pins 1 & 2 closed
40MHz	pins 2 & 3 closed