FIRST INTERNATIONAL COMPUTER, INC.
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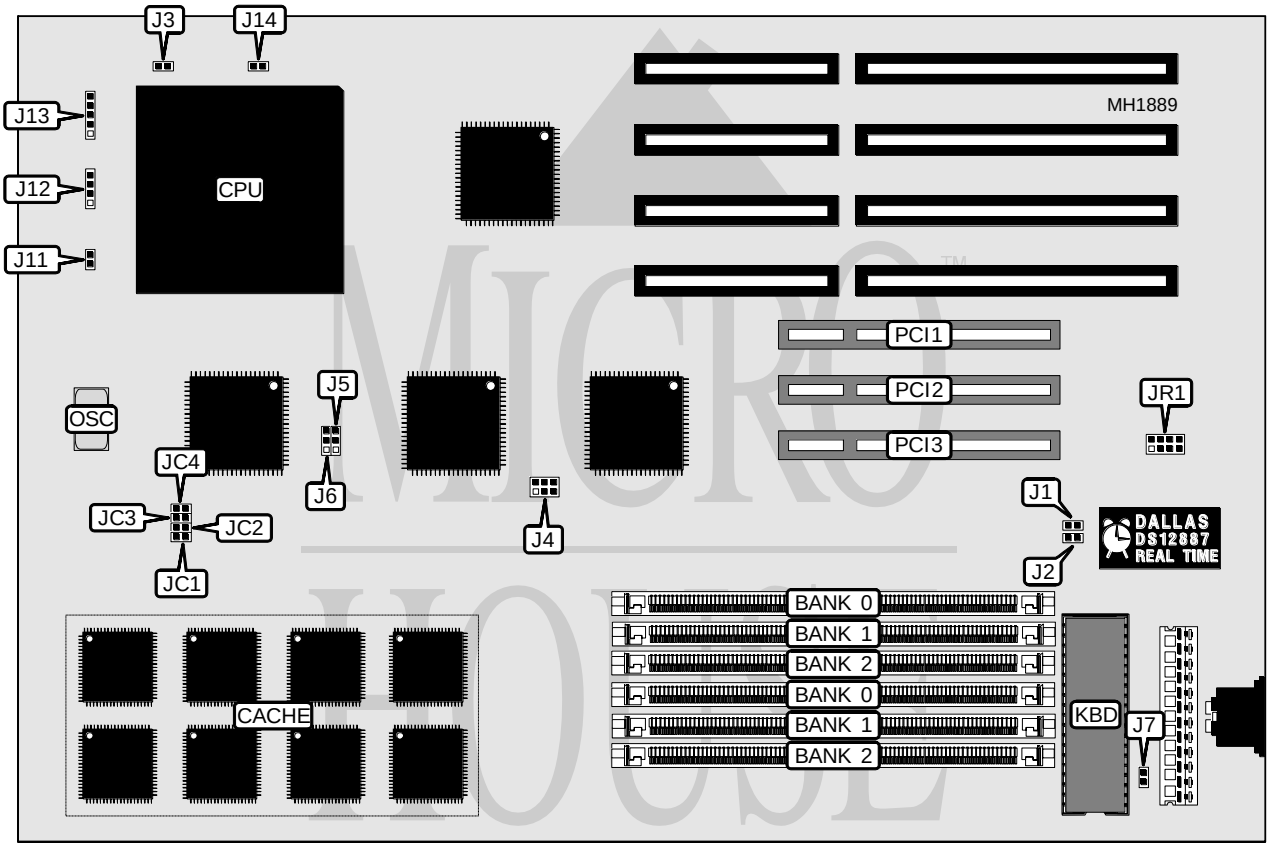
Processor	Pentium
Processor Speed	60/66MHz
Chip Set	Intel
Max. Onboard DRAM	192MB
Cache	256KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit PCI local bus slots (3)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J11	32-bit PCI Local bus slot	PCI1
Speaker	J12	32-bit PCI Local bus slot	PCI2
Power LED & keylock	J13	32-bit PCI Local bus slot	PCI3

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í RTC reset (DS12887A only) disabled	J1	Open
RTC reset (DS12887A only) enabled	J1	Closed
í Flash EPROM disabled	J2	Open
Flash EPROM enabled	J2	Closed
í Internal write-back cache enabled	J3	Open
Internal write-through cache enabled	J3	Closed
í External parity check enabled	J4	pins 1 & 3 and 2 & 4
External parity check disabled	J4	pins 1 & 2 and 3 & 4
í A31 set low	J5	pins 1 & 2 closed
A31 pulled high	J5	pins 2 & 3 closed
í A30 set low	J6	pins 1 & 2 closed
A30 pulled high	J6	pins 2 & 3 closed
Software RTC clear supporting AMI BIOS	J7	pins 2 & 3 closed
Software RTC clear supporting Award BIOS	J7	pins 1 & 2 closed
í Internal parity check disabled	J8	Open
Internal parity check enabled	J8	Closed
í Factory configured - do not alter	J14	Open
Note: Pins designated should be in the closed position. The location of J8 is unidentified.		

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 3
2MB	(2) 1M x 36	NONE	NONE
4MB	(2) 1M x 36	(2) 1M x 36	NONE
6MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36
8MB	(2) 4M x 36	NONE	NONE
10MB	(2) 4M x 36	(2) 1M x 36	NONE
12MB	(2) 4M x 36	(2) 1M x 36	(2) 1M x 36
16MB	(2) 4M x 36	(2) 4M x 36	NONE
18MB	(2) 4M x 36	(2) 4M x 36	(2) 1M x 36
24MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36
32MB	(2) 16M x 36	NONE	NONE
34MB	(2) 16M x 36	(2) 1M x 36	NONE
36MB	(2) 16M x 36	(2) 1M x 36	(2) 1M x 36
40MB	(2) 16M x 36	(2) 4M x 36	NONE
42MB	(2) 16M x 36	(2) 4M x 36	(2) 1M x 36
48MB	(2) 16M x 36	(2) 4M x 36	(2) 4M x 36
64MB	(2) 16M x 36	(2) 16M x 36	NONE
64MB	(2) 32M x 36	NONE	NONE
66MB	(2) 16M x 36	(2) 16M x 36	(2) 1M x 36
66MB	(2) 32M x 36	(2) 1M x 36	NONE
68MB	(2) 32M x 36	(2) 1M x 36	(2) 1M x 36
72MB	(2) 16M x 36	(2) 16M x 36	(2) 4M x 36
72MB	(2) 32M x 36	(2) 4M x 36	NONE
74MB	(2) 32M x 36	(2) 4M x 36	(2) 1M x 36
80MB	(2) 32M x 36	(2) 4M x 36	(2) 4M x 36

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DRAM CONFIGURATION (Cont.)			
Size	Bank 0	Bank 1	Bank 3
96MB	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36
96MB	(2) 32M x 36	(2) 16M x 36	NONE
98MB	(2) 32M x 36	(2) 16M x 36	(2) 1M x 36
104MB	(2) 32M x 36	(2) 16M x 36	(2) 4M x 36
128MB	(2) 32M x 36	(2) 16M x 36	(2) 16M x 36
128MB	(2) 32M x 36	(2) 32M x 36	NONE
130MB	(2) 32M x 36	(2) 32M x 36	(2) 1M x 36
136MB	(2) 32M x 36	(2) 32M x 36	(2) 4M x 36
160MB	(2) 32M x 36	(2) 32M x 36	(2) 16M x 36
192MB	(2) 32M x 36	(2) 32M x 36	(2) 32M x 36

PCI IRQ CONFIGURATION	
IRQ	JR1
í9	pins 3 & 4 closed
5	pins 1 & 2 closed
11	pins 5 & 6 closed
12	pins 7 & 8 closed

STANDARD/BURST ANSY SRAM SELECTION				
SRAM	JC1	JC2	JC3	JC4
í Standard	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
Burst Ansy	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed

TECHNICAL NOTE
To change between 60MHz and 66MHz, replace the Oscillator in OSC.