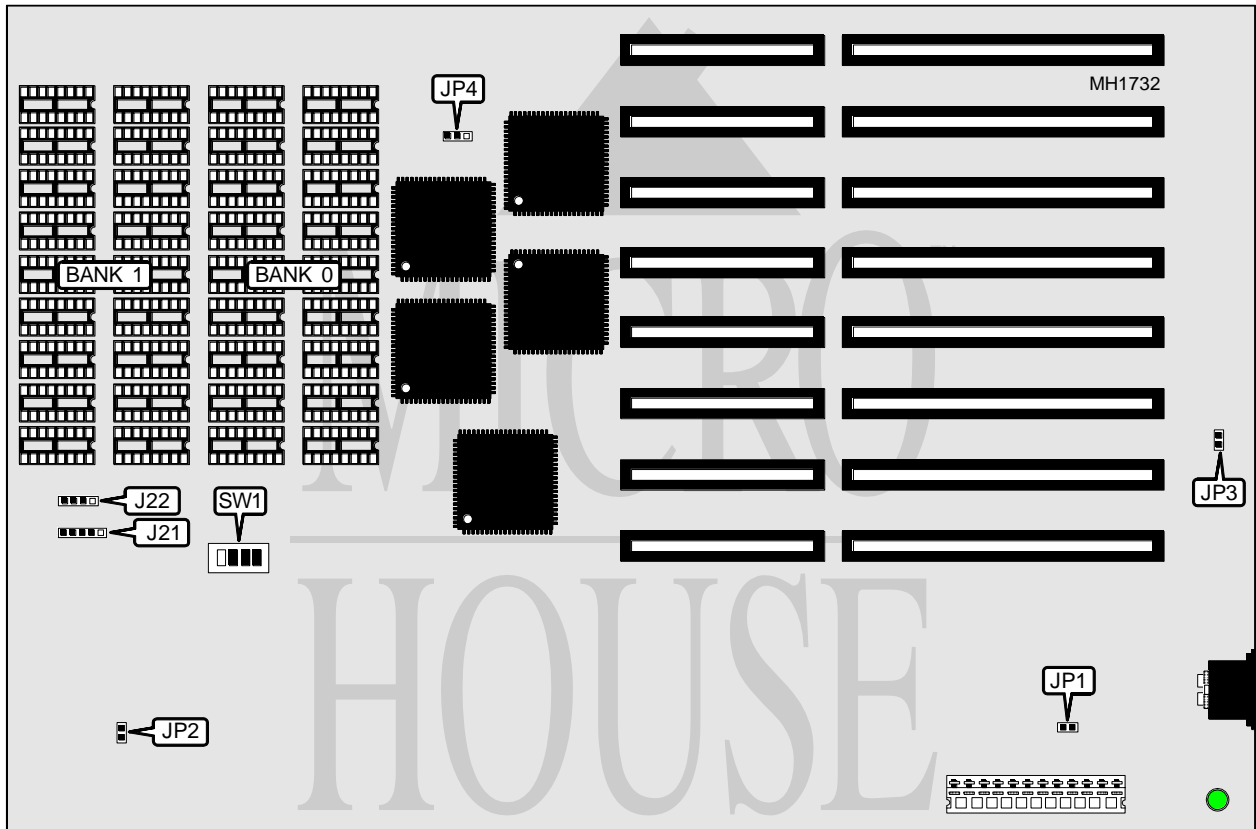


UNIDENTIFIED 10MHZ BABY AT

Processor	80286 (Exact location unknown)
Processor Speed	6/10MHz
Chip Set	C & T
Max. Onboard DRAM	1MB
Cache	None
BIOS	Unknown
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J21	Reset switch	JP1
Speaker	J22	Turbo switch	JP3

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	JP2	N/A
í BIOS type select 27128	JP4	pins 2 & 3 closed
BIOS type select 27256	JP4	pins 1 & 2 closed
í Factory configured - do not alter	SW1/1	N/A
í Monitor type select color	SW1/2	On
Monitor type select monochrome	SW1/2	Off

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
256KB	(9) 4164, 41256	(9) 4164, 41256
512KB	(9) 41256	NONE
640KB	(9) 41256	(9) 4164, 41256
1MB	(9) 41256	(9) 41256

DRAM JUMPER CONFIGURATION		
Size	SW1/3	SW1/4
256KB	On	On
512KB	Off	On
640KB	On	Off
1MB	Off	Off