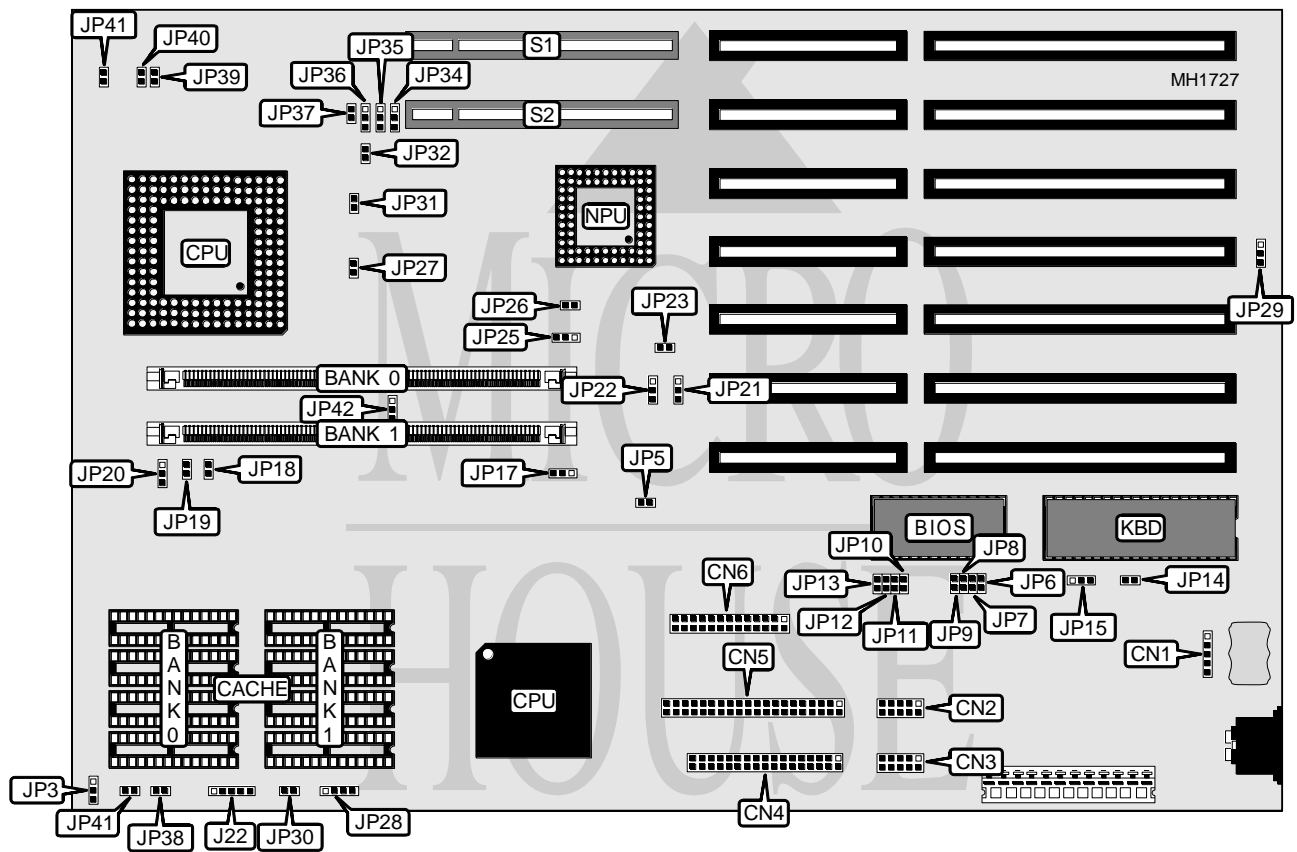


DATAEXPERT CORPORATION TERMINATOR 5-VL

Processor	80386DX/CX486DLC/CX486S/CX487S/80486SX/ 80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	8/16/20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	OPTi
Max. Onboard DRAM	32MB
Cache	128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2), floppy drive interface, IDE interface, parallel port, PS/2 style mouse port, serial ports (2)
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 style mouse port	CN1	Power LED & keylock	J22
Serial port 1	CN2	Speaker/IDE interface LED	JP28
Serial port 2	CN3	Reset/sleep switch	JP30
Floppy drive interface	CN4	Turbo LED	JP38
IDE interface	CN5	32-bit VESA Local bus slot	S1
Parallel port	CN6	32-bit VESA Local bus slot	S2

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USER CONFIGURABLE SETTINGS

Function	Jumper	Position
On board PS/2 style mouse enabled	JP14	Closed
On board PS/2 style mouse disabled	JP14	Open
í Power down mode enabled	JP15	pins 2 & 3 closed
Power down mode disabled	JP15	pins 1 & 2 closed
í CMOS memory normal operation	JP29	pins 2 & 3 closed
CMOS memory clear	JP29	pins 1 & 2 closed
í Factory configured - do not alter	JP37	Open
í Single sided memory type installed	JP42	pins 1 & 2 closed
Double sided memory type installed	JP42	pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(1) 1M x 36	(1) 1M x 36
8MB	(1) 2M x 36	NONE
16MB	(1) 2M x 36	(1) 2M x 36
16MB	(1) 4M x 36	NONE
32MB	(1) 8M x 36	NONE

CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

Note: The Exact location of TAG is unknown.

CACHE JUMPER CONFIGURATION

Size	JP3	JP18	JP19	JP20
128KB	pins 1 & 2 closed	Open	Closed	pins 1 & 2 closed
256KB	pins 2 & 3 closed	Closed	Closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION

Type	JP17	JP21	JP23	JP25	JP26	JP27	JP31	JP32	JP36
80386DX(PGA)	Open	2 & 3	Open	1 & 2	Open	Open	Closed	Open	Open
CX486DLC	Open	2 & 3	Open	1 & 2	Open	Open	Closed	Open	Open
80386DX(PQFP)	Open	2 & 3	Open	1 & 2	Open	Open	Open	Open	Open
80486SX(PQFP)	2 & 3	2 & 3	Open	1 & 2	Open	Open	Open	Open	Open
80487SX(PGA)	1 & 2	2 & 3	Open	1 & 2	Open	Open	Closed	Closed	2 & 3
80486DX 25/33	1 & 2	2 & 3	Open	1 & 2	Open	Open	Closed	Closed	Open
80486DX/50	1 & 2	2 & 3	Open	1 & 2	Open	Open	Closed	Closed	Open
80486DX2 25/33	1 & 2	2 & 3	Open	2 & 3	Open	Open	Closed	Closed	Open
CX486S	1 & 2	2 & 3	Closed	2 & 3	Open	Open	Closed	Closed	Open
CX487S	1 & 2	2 & 3	Closed	2 & 3	Closed	Open	Closed	Closed	Open
ODP486SX (W/B)	1 & 2	1 & 2	Closed	2 & 3	Open	Open	Closed	Closed	Open
ODP486SX (W/T)	1 & 2	1 & 2	Closed	2 & 3	Open	Closed	Closed	Closed	Open

Note: Pins designated should be in the closed position.

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CPU CLASS SELECTION			
Class	JP5	JP22	JP35
í 80486	Closed	pins 1 & 2 closed	pins 2 & 3 closed
80386	Open	pins 2 & 3 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION			
Speed	JP39	JP40	JP41
8MHz	Open	Open	Open
16MHz	Open	Closed	Open
20MHz	Open	Closed	Closed
25MHz	Closed	Open	Open
33MHz	Closed	Open	Closed
40MHz	Closed	Closed	Open
50i MHz	Closed	Open	Open
50MHz	Closed	Closed	Closed
66i MHz	Closed	Open	Closed

DMA CHANNEL INTERRUPT SELECT									
DRQ	DACK	JP6	JP7	JP8	JP9	JP10	JP11	JP12	JP13
DRQ3	DACK3	Closed	Open	Open	Open	Closed	Open	Open	Open
DRQ5	DACK5	Open	Closed	Open	Open	Open	Closed	Open	Open
DRQ6	DACK6	Open	Open	Closed	Open	Open	Open	Closed	Open
DRQ7	DACK7	Open	Open	Open	Closed	Open	Open	Open	Closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP34
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed