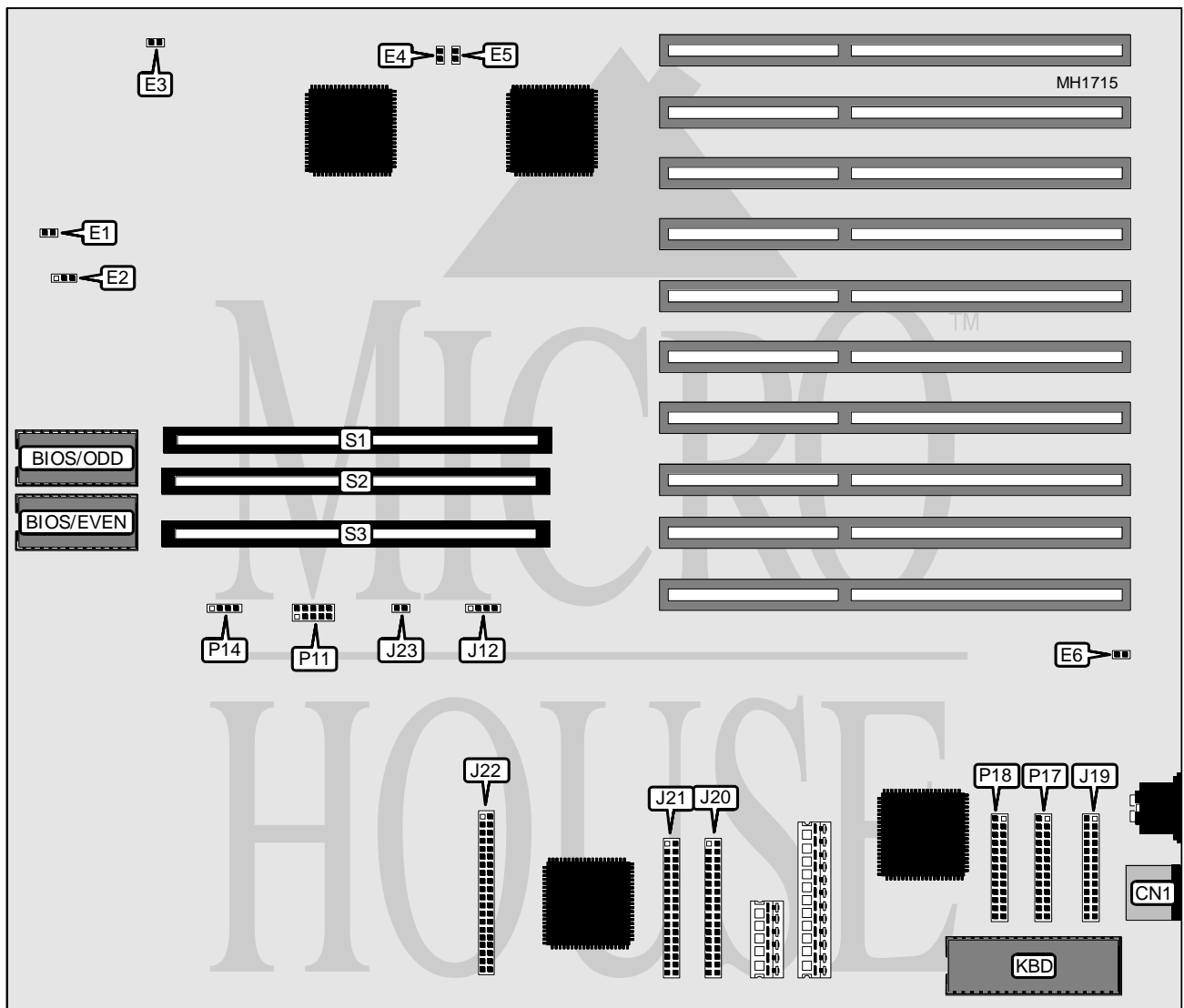


AST RESEARCH, INC. PREMIUM SE (P/N 202377-002)

Processor	Dependent upon the Cupid CPU board installed
Processor Speed	Dependent upon the Cupid CPU board installed
Chip Set	Intel
Max. Onboard DRAM	Located on the Cupid CPU and memory boards
Cache	None
BIOS	AST
Dimensions	355 x 304mm
I/O Options	32-bit Cupid bus slots (3), floppy drive interface (3), IDE interface, parallel port, PS/2 mouse port, serial ports (2)
NPU Options	Dependent upon the Cupid CPU board installed



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CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port	CN1	Front panel connector	P11
Speaker	J12	External battery	P14
Parallel port	J19	Serial port 1	P17
Floppy drive interface (1 & 2)	J20	Serial port 2	P18
Floppy drive interface (3)	J21	32-bit Cupid local bus slot	S1
IDE interface	J22	32-bit Cupid local bus slot	S2
Hard drive LED	J23	32-bit Cupid local bus slot	S3

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	E1	Open
í Password enabled	E2	pins 2 & 3 closed
Password disabled	E2	pins 1 & 2 closed
í Factory configured - do not alter	E3	Open
í Factory configured - do not alter	E4	Closed
í Factory configured - do not alter	E5	Closed
í Factory configured - do not alter	E6	Open

CPU CONFIGURATION
Note: Dependent upon Cupid board installed. See Cupid boards.