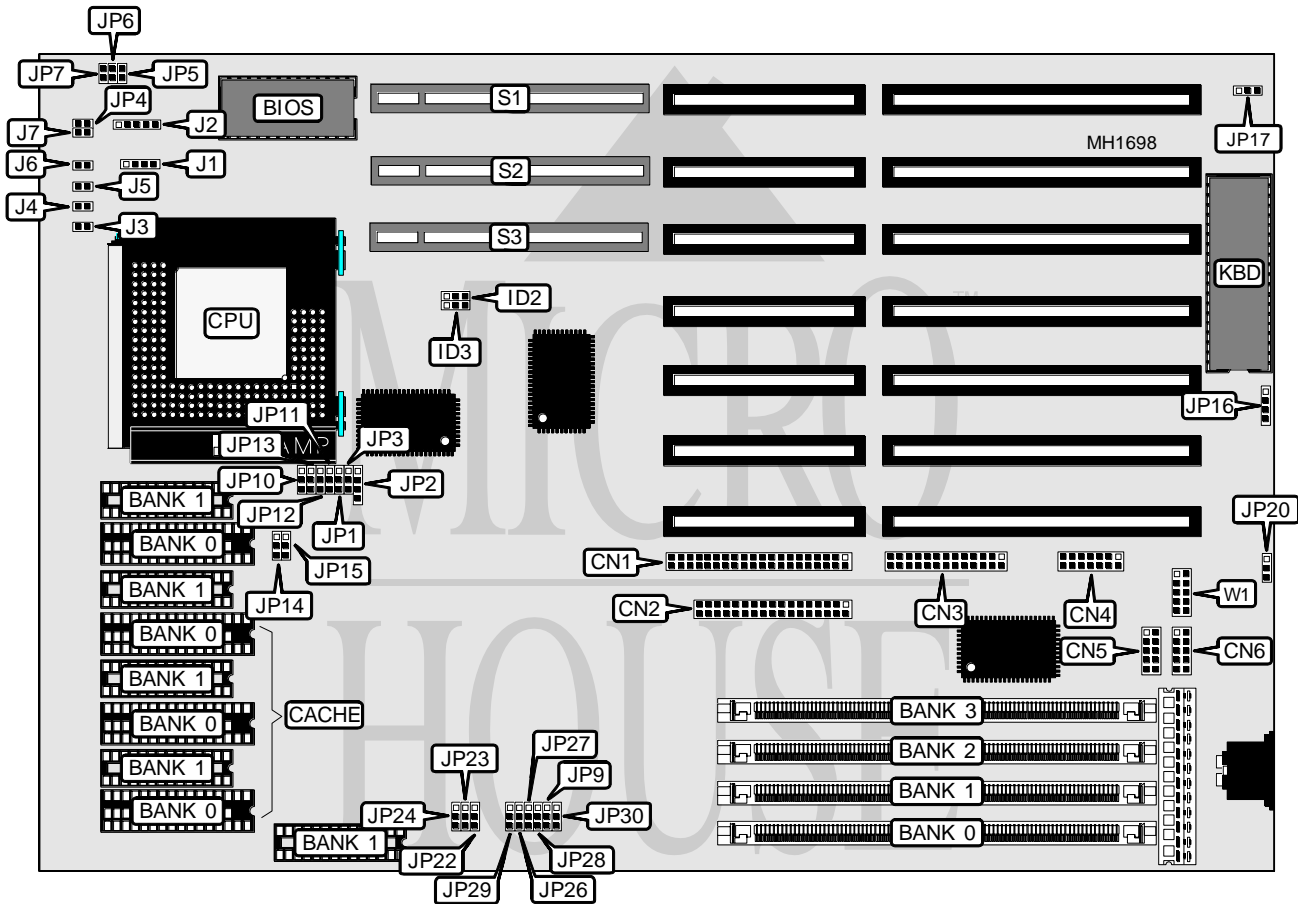


ADVANCED INTEGRATION RESEARCH, INC.

486 MI REV. 1.11

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	64MB
Cache	64/128/256/512KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (3), floppy drive interface, game port, IDE interface, parallel port, serial ports (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE port	CN1	Green PC LED	J3
Floppy port	CN2	Reset switch	J4
Parallel port	CN3	Turbo switch	J5
Game port	CN4	Turbo LED	J6
Serial port 1	CN5	IDE interface LED	J7
Serial port 2	CN6	32-bit VESA Local bus slots	S1, S2 & S3
Speaker	J1	Green PC connector	W1
Power LED & keylock	J2		

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486 MI REV. 1.11

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP3	N/A
í Factory configured - do not alter	JP4	N/A
í Factory configured - do not alter	JP9	N/A
í Monitor type select monochrome, VGA or EGA	JP16	Open
Monitor type select CGA	JP16	Closed
í Flash BIOS write protect enabled	JP17	pins 1 & 2 closed
Flash BIOS write protect disabled	JP17	pins 2 & 3 closed
í CMOS memory normal operation	JP20	pins 1 & 2 closed
CMOS memory clear	JP20	pins 2 & 3 closed
í IDE interface enabled	JP29	pins 1 & 2 closed
IDE interface disabled	JP29	pins 2 & 3 closed
í Parallel port select interrupt IRQ7	JP30	pins 1 & 2 closed
Parallel port select interrupt IRQ5	JP30	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(1) 512K x 36	None	None	None
4MB	(1) 512K x 36	None	(1) 512K x 36	None
4MB	(1) 1M x 36	None	None	None
6MB	(1) 512K x 36	None	(1) 1M x 36	None
8MB	(1) 1M x 36	(1) 1M x 36	None	None
8MB	(1) 2M x 36	None	None	None
10MB	(1) 512K x 36	None	(1) 1M x 36	(1) 1M x 36
10MB	(1) 512K x 36	None	(1) 2M x 36	None
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	None
12MB	(1) 2M x 36	None	(1) 1M x 36	None
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	None	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	None	(1) 2M x 36	None
16MB	(1) 4M x 36	None	None	None
20MB	(1) 1M x 36	(1) 4M x 36	None	None
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	None
24MB	(1) 2M x 36	None	(1) 4M x 36	None
32MB	(1) 4M x 36	(1) 4M x 36	None	None
32MB	(1) 8M x 36	None	None	None
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	None
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 2M x 36	None	(1) 4M x 36	(1) 4M x 36
40MB	(1) 2M x 36	None	(1) 8M x 36	None
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	None
48MB	(1) 8M x 36	None	(1) 4M x 36	None
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	None	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	None	(1) 8M x 36	None

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ADVANCED INTEGRATION RESEARCH, INC.
486MI REV. 1.11

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CACHE JUMPER CONFIGURATION						
Size	JP10	JP11	JP12	JP13	JP14	JP15
64KB	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 1 & 2
128KB	pins 2 & 3	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 2 & 3
256KB	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 2 & 3	pins 2 & 3	pins 2 & 3
256KB	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 1 & 2	pins 2 & 3

Note: Pins designated should be in the closed position.

CACHE SIZE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CPU TYPE CONFIGURATION		
Type	JP1	JP2
80486SX	Open	pins 2 & 3 closed
80486DX	pins 1 & 2 closed	pins 1 & 2 and 3 & 4 closed
80487SX	pins 2 & 3 closed	pins 1 & 2 and 3 & 4 closed
ODP486SX	pins 2 & 3 closed	pins 1 & 2 and 3 & 4 closed
80486DX2	pins 1 & 2 closed	pins 1 & 2 and 3 & 4 closed

CPU SPEED CONFIGURATION						
Speed	JP5	JP6	JP7	JP26	JP27	JP28
20MHz	Closed	Open	Open	pins 2 & 3	pins 2 & 3	pins 1 & 2
25MHz	Open	Closed	Closed	pins 2 & 3	pins 2 & 3	pins 1 & 2
33MHz	Open	Closed	Open	pins 2 & 3	pins 2 & 3	pins 1 & 2
40MHz	Open	Open	Closed	pins 1 & 2	pins 1 & 2	pins 2 & 3
50i MHz	Open	Closed	Closed	pins 2 & 3	pins 2 & 3	pins 1 & 2
50MHz	Open	Open	Open	pins 1 & 2	pins 1 & 2	pins 2 & 3
66i MHz	Open	Closed	Open	pins 2 & 3	pins 2 & 3	pins 1 & 2

Note: Pins designated should be in the closed position.

IDE RECOVERY TIME				
Speed	Recovery Time	JP22	JP23	JP24
<33mhz	9T, low speed	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
<33mhz	7T, middle speed	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
<33mhz	5T, high speed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
>40mhz	13T, low speed	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed
>40mhz	11T middle speed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
>40mhz	9T high speed	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed

VESA WAIT STATE/BUS SPEED (ID2 & ID3) CONFIGURATION			
Speed	Setting	ID2	ID3
≤33MHz	0 wait states	pins 1 & 2 closed	pins 1 & 2 closed
>33MHz	1 wait state	pins 2 & 3 closed	pins 2 & 3 closed