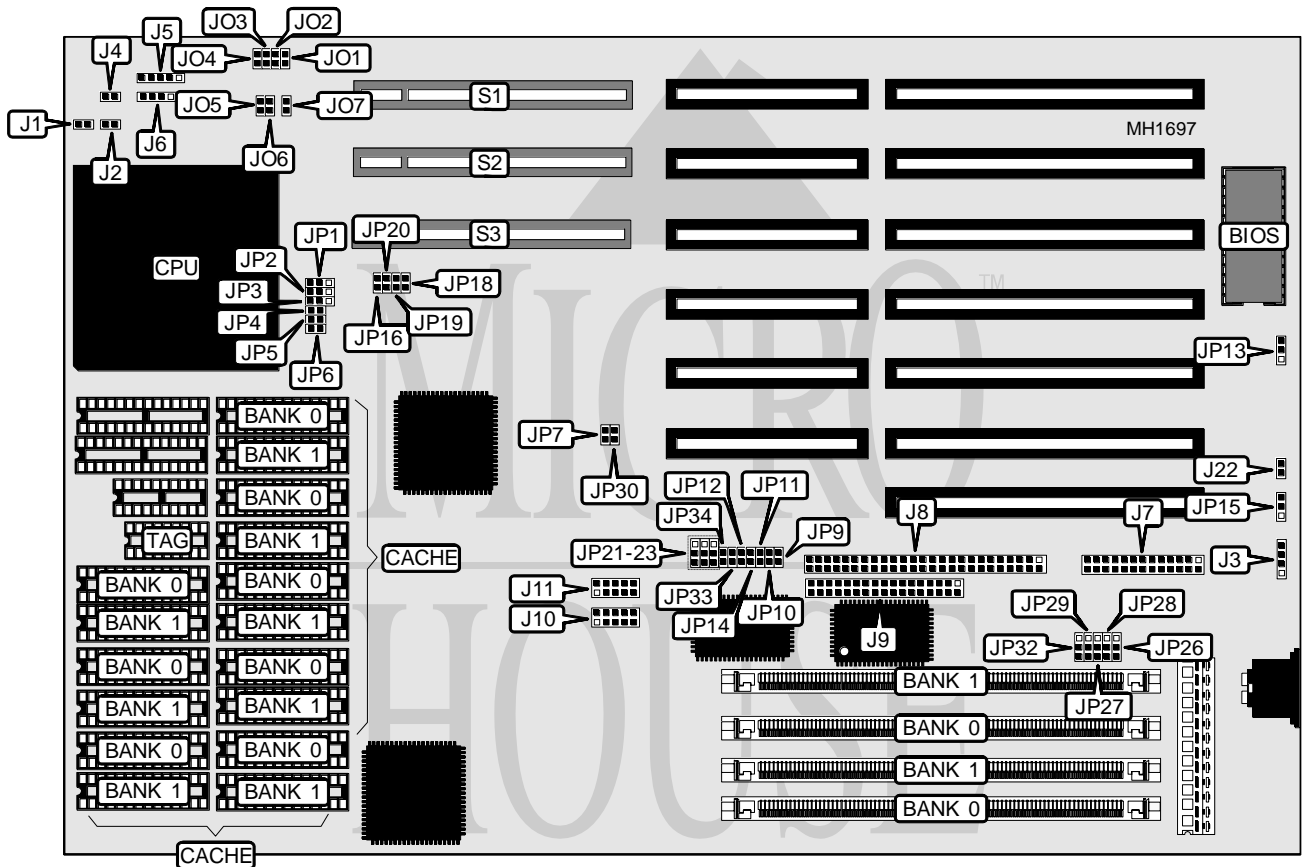


ADVANCED INTEGRATION RESEARCH, INC.

586MI

Processor	Pentium Overdrive/Pentium
Processor Speed	60/66MHz
Chip Set	SIS
Max. Onboard DRAM	128MB
Cache	256/512KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (3), floppy drive interface, IDE interface parallel port, serial ports (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	J1	IDE port	J8
Turbo LED	J2	Floppy drive interface	J9
External battery	J3	Serial port 1	J10
Reset switch	J4	Serial port 2	J11
Power LED & keylock	J5	IDE interface LED	J22
Speaker	J6	32-bit VESA Local bus slots	S1, S2 & S3
Parallel port	J7		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JO1	N/A
í Factory configured - do not alter	JO2	N/A
í Factory configured - do not alter	JO3	N/A
í Factory configured - do not alter	JO4	N/A
í Factory configured - do not alter	JP9	N/A
í Factory configured - do not alter	JP10	N/A
í Factory configured - do not alter	JP11	N/A
í Factory configured - do not alter	JP12	N/A
í Flash BIOS write protect disabled	JP13	Open
Flash BIOS write protect enabled	JP13	pins 2 & 3 closed
í Monitor type select monochrome VGA or EGA	JP14	Open
Monitor type select CGA	JP14	Closed
í CMOS memory normal operation	JP15	pins 2 & 3 closed
CMOS memory clear	JP15	pins 1 & 2 closed
í Factory configured - do not alter	JP17	N/A
í Factory configured - do not alter	JP34	N/A
í Factory configured - do not alter	JP26	N/A
í Factory configured - do not alter	JP27	N/A
í Factory configured - do not alter	JP28	N/A
í Factory configured - do not alter	JP29	N/A
í IDE interface enabled	JP30	Open
IDE interface disabled	JP30	Closed
í Parallel port select interrupt IRQ 7	JP32	pins 1 & 2 closed
Parallel port select interrupt IRQ 5	JP32	pins 2 & 3 closed
í Factory configured - do not alter	JP33	N/A

CACHE CONFIGURATION			
Size	Bank 0 (U3-U10)	Bank 1 (U18-U25)	TAG
64KB	(8) 8K x8	None	(1) 8K x8
128KB	(8) 8K x8	(8) 8K x8	(1) 8K x8
256KB	(8) 32K x8	None	(1) 32K x8
512KB	(8) 32K x8	(8) 32K x8	(1) 32K x8

CACHE JUMPER CONFIGURATION						
Size	JP1	JP2	JP3	JP4	JP5	JP6
64KB	pins 1 & 2	pins 1 & 2	pins 1 & 2	Open	Open	Open
128KB	pins 2 & 3	pins 2 & 3	pins 2 & 3	Open	Open	Closed
256KB	pins 1 & 2	pins 1 & 2	pins 1 & 2	Open	Closed	Closed
512KB	pins 2 & 3	pins 2 & 3	pins 2 & 3	Closed	Closed	Closed

Note: Pins designated should be in the closed position.

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DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(1) 256K x 36	None
4MB	(1) 256K x 36	(1) 256K x 36
4MB	(1) 512K x 36	None
6MB	(1) 256K x 36	(1) 512K x 36
8MB	(1) 512K x 36	(1) 512K x 36
8MB	(1) 1M x 36	None
10MB	(1) 256K x 36	(1) 1M x 36
12MB	(1) 512K x 36	(1) 1M x 36
16MB	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	None
18MB	(1) 256K x 36	(1) 2M x 36
20MB	(1) 512K x 36	(1) 2M x 36
24MB	(1) 1M x 36	(1) 2M x 36
32MB	(1) 2M x 36	(1) 2M x 36
32MB	(1) 4M x 36	None
34MB	(1) 256K x 36	(1) 4M x 36
36MB	(1) 512K x 36	(1) 4M x 36
40MB	(1) 1M x 36	(1) 4M x 36
48MB	(1) 2M x 36	(1) 4M x 36
64MB	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	None
66MB	(1) 256K x 36	(1) 4M x 36
68MB	(1) 512K x 36	(1) 8M x 36
72MB	(1) 1M x 36	(1) 8M x 36
80MB	(1) 2M x 36	(1) 8M x 36
96MB	(1) 4M x 36	(1) 8M x 36
128MB	(1) 8M x 36	(1) 8M x 36

IDE RECOVERY TIME			
Speed	JP21	JP22	JP23
High	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
Medium	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
Slow	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed

VESA WAIT STATE/BUS SPEED CONFIGURATION						
Speed	Wait states	JP18	JO5	JO6	JO7	JO8
33MHz	0 wait states	Closed	Closed	Open	Open	Open
40MHz	1 wait state	Open	Open	Open	Open	Closed