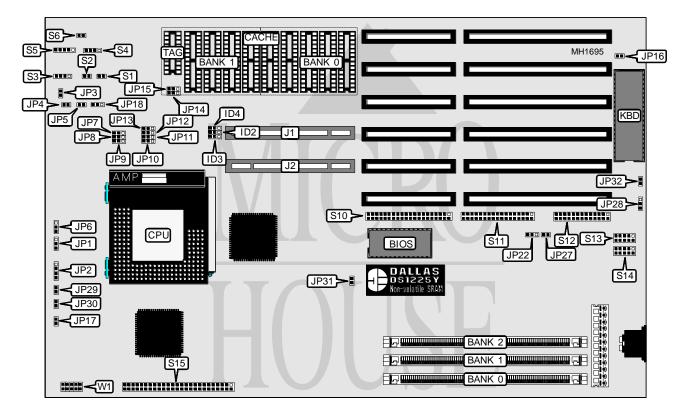
ADVANCED INTEGRATION RESEARCH, INC. 486MIS REV. 1.11

Processor	804865X/804875X/80486DX/80486DX2/ODP4865X/Pentium Overdrive
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	64MB
Cache	64/128/256/512KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2), floppy drive interface,
	IDE interface, parallel port, serial ports (2)
NPU Options	None



CONNECTIONS				
Purpose	Location	Purpose	Location	
32-bit VESA Local bus slots	J1 & J2	Turbo switch	S6	
Turbo LED	S1	IDE interface	S10	
Reset switch	S2	Floppy drive interface	S11	
Hard drive LED	S3	Parallel port	S12	
Speaker	S4	Serial port 1	S13	
Power LED & keylock	S5	Serial port 2	S14	

Continued on next page . . .

ADVANCED INTEGRATION RESEARCH, INC. 486 MIS REV. 1.11

... continued from previous page

USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í Factory configured - do not alter	JP6	Closed		
í Monitor type select monochrome, VGA or EGA	JP16	Open		
Monitor type select CGA	JP16	Closed		
í IDE interface enabled	JP17	Open		
IDE interface disabled	JP17	Closed		
í Factory configured - do not alter	JP18	Closed		
í Parallel port select interrupt IRQ7	JP22	pins 1 & 2 closed		
Parallel port select interrupt IRQ5	JP22	pins 2 & 3 closed		
í Factory configured - do not alter	JP27	Closed		
í Flash BIOS write protect disabled	JP28	pins 1 & 2 closed		
Flash BIOS write protect enabled	JP28	pins 2 & 3 closed		
í Factory configured - do not alter	JP31	Closed		
í Normal password operation	JP32	Open		
BIOS password clear	JP32	Closed		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	
2MB	(1) 512K x 36	None	None	
4MB	(1) 512K x 36	None	(1) 512K x 36	
4MB	(1) 1M x 36	None	None	
6MB	(1) 512K x 36	None	(1) 1M x 36	
8MB	(1) 1M x 36	(1) 1M x 36	None	
8MB	(1) 2M x 36	None	None	
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	
12MB	(1) 2M x 36	None	(1) 1M x 36	
16MB	(1) 2M x 36	None	(1) 2M x 36	
16MB	(1) 4M x 36	None	None	
20MB	(1) 1M x 36	(1) 4M x 36	None	
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	
24MB	(1) 2M x 36	None	(1) 4M x 36	
32MB	(1) 4M x 36	(1) 4M x 36	None	
40MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	
40MB	(1) 2M x 36	None	(1) 8M x 36	
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	
48MB	(1) 8M x 36	None	(1) 4M x 36	
64MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	
64MB	(1) 8M x 36	None	(1) 8M x 36	

Continued on next page . . .

ADVANCED INTEGRATION RESEARCH, INC. 486 MIS REV. 1.11

... continued from previous page

	CACHE SIZE JUMPER CONFIGURATION								
Size	JP7	JP8	JP9	JP10	JP11	JP12	JP13	JP14	JP15
64KB	1&2	1&2	1&2	1&2	1&2	1&2	1&2	1&2	2&3
128KB	1&2	1&2	2&3	2&3	2&3	1&2	1&2	1&2	1&2
256KB	2&3	1&2	2&3	2&3	2&3	2&3	2&3	1&2	2&3
256KB	2&3	2&3	2&3	2&3	2&3	1&2	2&3	2&3	1&2
Note: Pir	Note: Pins designated should be in the closed position.								

CACHE SIZE AND SRAM LOCATIONS					
Cache Size Bank 0 Bank 1 TAG Dirty B					
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 16K x 1	
128KB	(4) 32K x 8	None	(1) 32K x 8	(1) 16K x 1	
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 16K x 1	
512KB	(4) 128K x 8	None	(1) 32K x 8	(1) 16K x 4	

	CPU EXTERNALCLOCK SPEED				
Speed	JP3	JP4	JP5		
20MHz	Open	Closed	Closed		
25MHz	Closed	Open	Closed		
33MHz	Closed	Closed	Open		
40MHz	Open	Open	Closed		
50i MHz	Closed	Open	Closed		
50MHz	Open	Closed	Open		
66i MHz	Closed	Closed	Open		

CPU TYPE			
Туре	JP1	JP2	
80486SX	Open	pins 2 & 3 closed	
80487SX	pins 2 & 3 closed	pins 1 & 2 and 3 & 4 closed	
80486DX	pins 1 & 2 closed	pins 1 & 2 and 3 & 4 closed	
ODP486SX	pins 2 & 3 closed	pins 1 & 2 and 3 & 4 closed	
80486DX2	pins 1 & 2 closed	pins 1 & 2 and 3 & 4 closed	
Pentium Overdrive	pins 1 & 2 closed	pins 1 & 2 and 3 & 4 closed	

IDE RECOVERY TIME			
Time	JP29	JP30	
í 300ns	Closed	Closed	
240ns	Open	Closed	
180ns	Closed	Open	
120ns	Open	Open	

Continued on next page . . .

ADVANCED INTEGRATION RESEARCH, INC. 486 MIS REV. 1.11

... continued from previous page

	SCSI ADDRESS	
Address	W1/pins 3 & 4	W1/pins 5 & 6
DC000h enabled	Closed	Closed
DC000h disabled	Closed	N/A
DC8000h enabled	N/A	Closed
DC8000h disabled	Closed	Closed

SCSI PORT CONFIGURATION					
Setting	IRQ	W1/1 & 2	W1/7 & 8	W1/9 & 10	
í Enabled	í 11	Closed	Closed	Open	
Disabled	10	Open	Open	Closed	