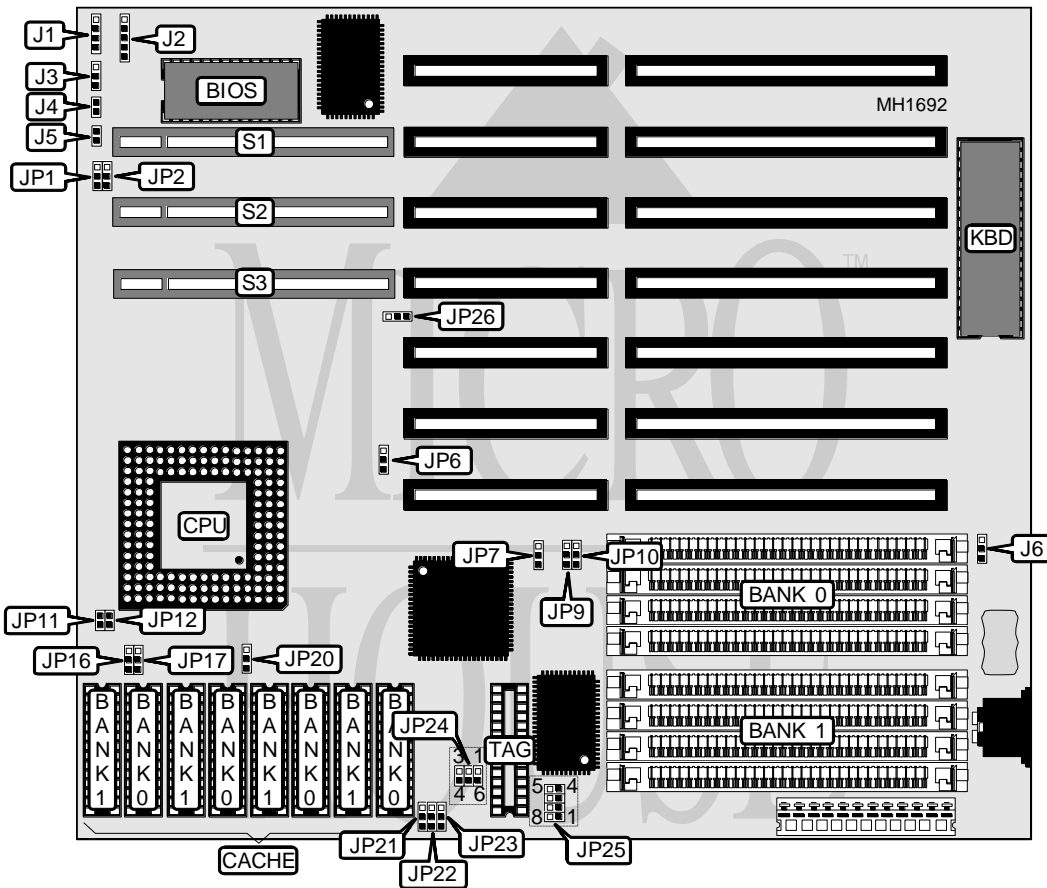


MICROMEDIA TECHNOLOGIES, INC.

486 - V L A

Processor	80486SX/80486DX/80486DX2
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	ALI
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	230mm x 218mm
I/O Options	32-bit VESA local bus slots (3)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	External battery	J6
Power LED & keylock	J2	32-bit VESA Local bus slot	S1
Reset switch	J3	32-bit VESA Local bus slot	S2
Turbo LED	J4	32-bit VESA Local bus slot	S3
Turbo switch	J5		

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486 - V L A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE JUMPER CONFIGURATION				
Size	JP21	JP22	JP23	JP24
64KB	pins 1 & 2	pins 1 & 2	Open	pins 3 & 4
128KB	pins 2 & 3	pins 1 & 2	Open	pins 1 & 2, 3 & 4 and 5 & 6
256KB	pins 1 & 2	pins 1 & 2	pins 1 & 2 and 2 & 3	pins 1 & 2, 3 & 4 and 5 & 6

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION						
Speed	JP6	JP10	JP25/pins 1&2	JP25/pins 3&4	JP25/pins 5&6	JP26
25MHz	pins 1 & 2	pins 1 & 2	Open	Closed	Open	pins 1 & 2
33MHz	pins 1 & 2	pins 1 & 2	Closed	Open	Closed	pins 1 & 2
40MHz	pins 2 & 3	pins 2 & 3	Closed	Closed	Open	pins 2 & 3
50i MHz	pins 1 & 2	pins 1 & 2	Open	Closed	Open	pins 1 & 2
50MHz	pins 2 & 3	pins 2 & 3	Open	Closed	Open	pins 2 & 3
66i MHz	pins 1 & 2	pins 1 & 2	Closed	Open	Closed	pins 1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION							
Type	JP7	JP9	JP11	JP12	JP16	JP17	JP20
80486SX	pins 1 & 2	pins 1 & 2	Closed	Closed	pins 1 & 2	N/A	pins 1 & 2
80487SX	pins 1 & 2	pins 1 & 2	Closed	Closed	pins 2 & 3	pins 1 & 2	pins 1 & 2
80487DX	pins 1 & 2	pins 1 & 2	Closed	Closed	pins 2 & 3	pins 2 & 3	pins 1 & 2
80486DX2	pins 2 & 3	pins 2 & 3	Open	Open	pins 1 & 2	N/A	pins 1 & 2

Note: Pins designated should be in the closed position.

VESA WAIT STATE/BUS SPEED CONFIGURATION			
CPU speed	Wait states	JP1	JP2
≤ 33MHz	0 wait states	pins 1 & 2 closed	pins 1 & 2 closed
> 33MHz	1 wait state	pins 2 & 3 closed	pins 2 & 3 closed