MICRO EXPRESS, INC. FOREX 486 CACHE

Processor 80486SX/80487SX/80486DX/80486DX2 20/25/33/50 (internal)/50/66 (internal)MHz **Processor Speed**

Chip Set FOREX Max. onboard DRAM 32MB

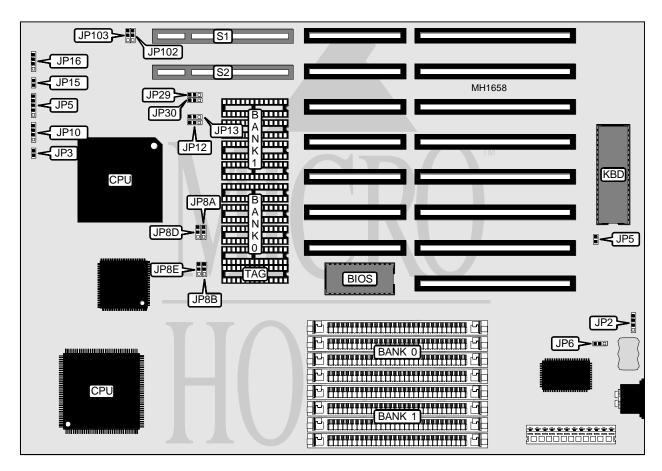
Cache 64/128/256KB

BIOS AMI

Dimensions 330mm x 218mm

I/O Options 32-bit VESA local bus slots (2)

NPU Options None



CONNECTIONS				
Purpose	Location			
External battery	JP2	Turbo switch	JP15	
Reset switch	JP3 Turbo LED		JP16	
Power LED & keylock	JP9	32-bit VESA local bus slot	S1	
Speaker	JP10	32-bit VESA local bus slot	S2	

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USER CONFIGURABLE SETTINGS			
Function	Jumper/Switch	Position	
í Factory configured - do not alter	JP5	N/A	
í CMOS memory normal operation	JP6	Open	
CMOS memory clear	JP6	Closed	

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size Bank 0 Bank 1 TAG				
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 4	
128KB	(4) 32K x 8	NONE	(1) 8K x 4	
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 4	

CACHE JUMPER CONFIGURATION					
Size	JP8A	JP8B	JP8D	JP8E	
64KB	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed	
128KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed	
256KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed	

VESA WAIT STATE/BUS SPEED (ID2 & ID3) CONFIGURATION				
CPU speed Wait states JP102 (ID2) JP103 (I				
≤ 33MHz	0 wait states	pins 1 & 2 closed	pins 1 & 2 closed	
> 33MHz	1 wait state	pins 2 & 3 closed	pins 2 & 3 closed	

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CPU SPEED CONFIGURATION				
Speed	JP7A	JP7B	JP7C	
20MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	
25MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed	
33MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed	
50i MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed	
50MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	
66i MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed	

CPU TYPE CONFIGURATION					
Туре	JP12	JP13	JP29	JP30	
80486SX/PQFP	pins 2 & 3 closed				
80486SX/PGA	pins 2 & 3 closed				
80487SX/PGA	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed	
80486DX/PQFP	pins 2 & 3 closed				
80486DX/PGA	pins 1 & 2 closed				
80486DX2/PQFP	pins 2 & 3 closed				
80486DX2/PGA	pins 1 & 2 closed				