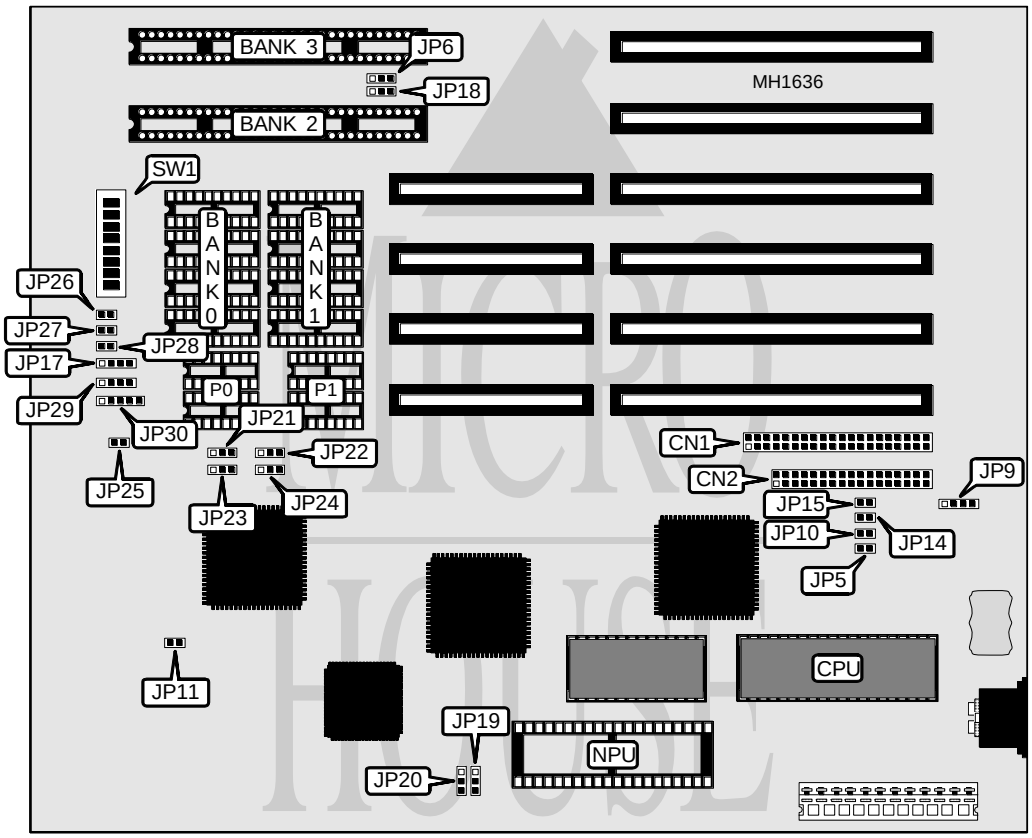
AMERICAN DIGICOM CORPORATION

2 8 6 H

Processor	80286
Processor Speed	12/16/20MHz
Chip Set	Suntac
Max. onboard DRAM	5MB
Cache	None
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	IDE interface, floppy drive interface (optional)
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface (optional)	CN1	Turbo Switch	JP27
Floppy drive interface (optional)	CN2	Turbo LED	JP28
External battery	JP9	Speaker	JP29
IDE interface LED	JP17	Power LED & keylock	JP30
Reset switch	JP26		

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
íFloppy drive address select 3F4-3F5h	JP5	Closed
Floppy drive address select 374-375h	JP5	Open
í BIOS type select 27256	JP6 & JP18	pins 1 & 2 closed
BIOS type select 27512	JP6 & JP18	pins 2 & 3 closed
í Floppy drive enabled	JP10	Closed
Floppy drive disabled	JP10	Open
í Floppy drive speed select 12MHz	JP11	Closed
Floppy drive speed select low 16/20MHz	JP11	Open
í NPU speed select low (12MHz CPU)	JP19 & JP20	pins 1 & 2 closed
NPU speed select high (16/20MHz CPU)	JP19 & JP20	pins 2 & 3 closed
í I/O bus speed select OSC1/4 for 12/16MHz	JP25	Closed
I/O bus speed select OSC2/2 for 12/16MHz	JP25	Open
í Monitor type select color	SW1/switch 1	On
Monitor type select monochrome	SW1/switch 1	Off
í Parity enabled	SW1/switch 2	On
Parity disabled	SW1/switch 2	Off
Note: For a 20Mhz CPU, JP25 should remain open.		

DRAM CONFIGURATION						
Size	Bank 0	Parity 0	Bank 1	Parity 1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 256K x 9	(2) 256K x 9
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
3MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9
5MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 1M x 9	(2) 1M x 9
Note: You may use SIPP modules as bank 0 and bank 1.						

DRAM SWITCH CONFIGURATION			
Size	SW1/switch 3	SW1/switch 4	SW1/switch 5
512KB	On	On	On
1MB	On	On	Off
2MB	On	Off	Off
2MB	Off	On	On
3MB	Off	Off	On
4MB	Off	On	Off
5MB	On	Off	On

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MODULE RAM BANK CONFIGURATION				
Bank	JP21	JP22	JP23	JP24
SIP RAM as banks 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3
SIP RAM as banks 0 & 1	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2
Note: Pins designated should be in the closed position.				

MEMORY MAP CONFIGURATION					
Base Memory	Shadow	Extended	SW1/6	SW1/7	SW1/8
640KB	0KB	remaining memory	On	On	On
640KB	128KB	remaining memory	On	On	Off
512KB	0KB	remaining memory	On	Off	On
512KB	128KB	remaining memory	Off	On	On
640KB	EMS	64KB	Off	Off	On
640KB	EMS	0KB	Off	On	Off
512KB	EMS	64KB	Off	Off	On
512KB	EMS	0KB	Off	Off	Off

IDE INTERFACE CONFIGURATION		
Function	JP14	JP15
Enabled	pins 2 & 3	pins 2 & 3
Disabled	pins 1 & 2	pins 1 & 2