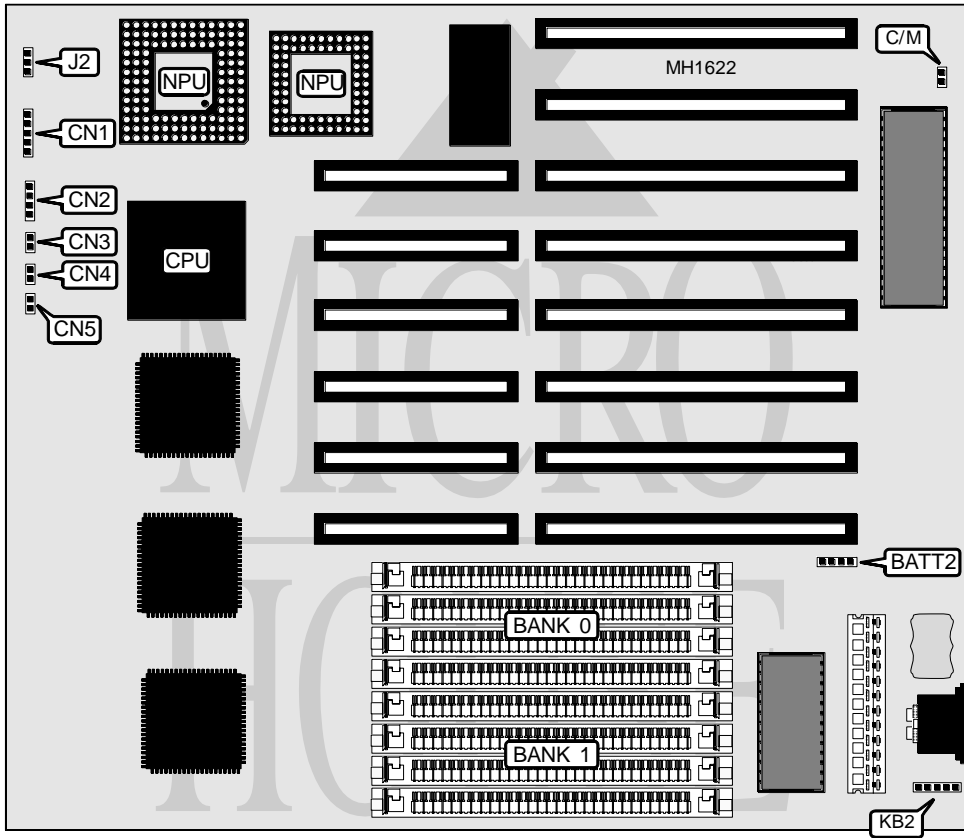


CACHING TECH CORPORATION

C 3 8 6 M X

Processor 80386DX/CX486DLC
Processor Speed 33/40MHz
Chip Set MXIC
Max. onboard DRAM 32MB
Cache None
BIOS AMI
Dimensions 254mm x 218mm
I/O Options None
NPU Options 80387DX/CX487DLC/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	BATT2	Turbo switch	CN4
Power LED & keylock	CN1	Turbo LED	CN5
Speaker	CN2	External Keyboard	KB2
Reset switch	CN3		

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CACHING TECH CORPORATION

C 3 8 6 M X

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í CMOS memory normal operation (Internal Battery)	BATT2	pins 2 & 3 closed
CMOS memory normal operation (External Battery)	BATT2	Connect to 1 & 4
CMOS memory clear	BATT2	pins 1 & 2 closed
í Monitor type select color	C/M	Open
Monitor type select monochrome	C/M	Closed

CPU SPEED CONFIGURATION		
Speed	JF0	JF1
33MHz	Closed	Open
40MHz	Open	Closed

Note: The locations of JF0 and JF1 are unknown.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

Note: The locations of bank 0 and bank 1 may be different on the system board.

CPU TYPE CONFIGURATION	
Type	J2
80386DX	pins 1 & 2 closed
CX486DLC	pins 2 & 3 closed