ACER, INC. ACER 710

Processor 8088

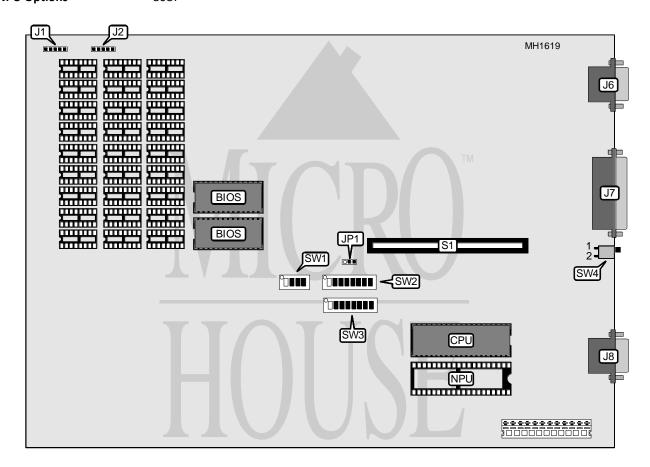
4.77/10MHz **Processor Speed**

Chip Set None Max. onboard DRAM 1MB **SRAM Cache** None **BIOS** Phoenix

325mm x 308mm **Dimensions**

Floppy drive interface, parallel port, serial port, video I/O Options

NPU Options 8087



CONNECTIONS				
Purpose	Location	Purpose	Location	
Floppy drive interface	CN1	Video port	J6	
Keyboard connector	J1	Parallel port	J7	
Front panel connector	J2	Serial port	J8	
Note: The exact location of J1 an The location of CN1 is unki				

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ACER, INC. ACER 710

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USER CONFIGURABLE SETTINGS			
Function	Jumper/Switch	Position	
í Video controller enabled	JP1	pins 2 & 3 closed	
Video controller disabled	JP1	pins 1 & 2 closed	
í COM1 enabled	SW1/switch 1	On	
COM1 disabled	SW1/switch 1	Off	
í Real time clock enabled	SW1/switch 3	Off	
Real time clock disabled	SW1/switch 3	On	
í Floppy drive interface enabled	SW2/switch 1	Off	
Floppy drive interface disabled	SW2/switch 1	On	
í NPU enabled	SW2/switch 2	Off	
NPU disabled	SW2/switch 2	On	
í Factory configured - do not alter	SW2/switch 5	N/A	
í Onboard display enabled	SW2/switch 6	Off	
Onboard display disabled	SW2/switch 6	On	
í CPU speed high	SW3/switch 8	On	
CPU speed low	SW3/switch 8	Off	
í Monitor type select color	SW4	position 1	
Monitor type select monochrome	SW4	position 2	
Note: Disabling onboard video might also disable onboard parallel port.			

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	
256KB	(9) 41256	NONE	NONE	
512KB	(9) 41256	(9) 41256	NONE	
640KB	(9) 41256	(9) 41256	(9) 4164	
768KB	(9) 41256	(9) 41256	(9) 41256	

DRAM JUMPER CONFIGURATION				
Size	SW2/3	SW2/4	SW3/5	SW3/6
256KB	Off	Off	Off	Off
512KB	On	Off	Off	Off
640KB	Off	On	Off	Off
768KB	Off	On	Off	On

FLOPPY DRIVE CONFIGURATION				
Drives installed	SW2/switch 1 SW2/switch 7 SW2/switch			
0	On	Off	Off	
1	Off	On	On	
2	Off	Off	On	