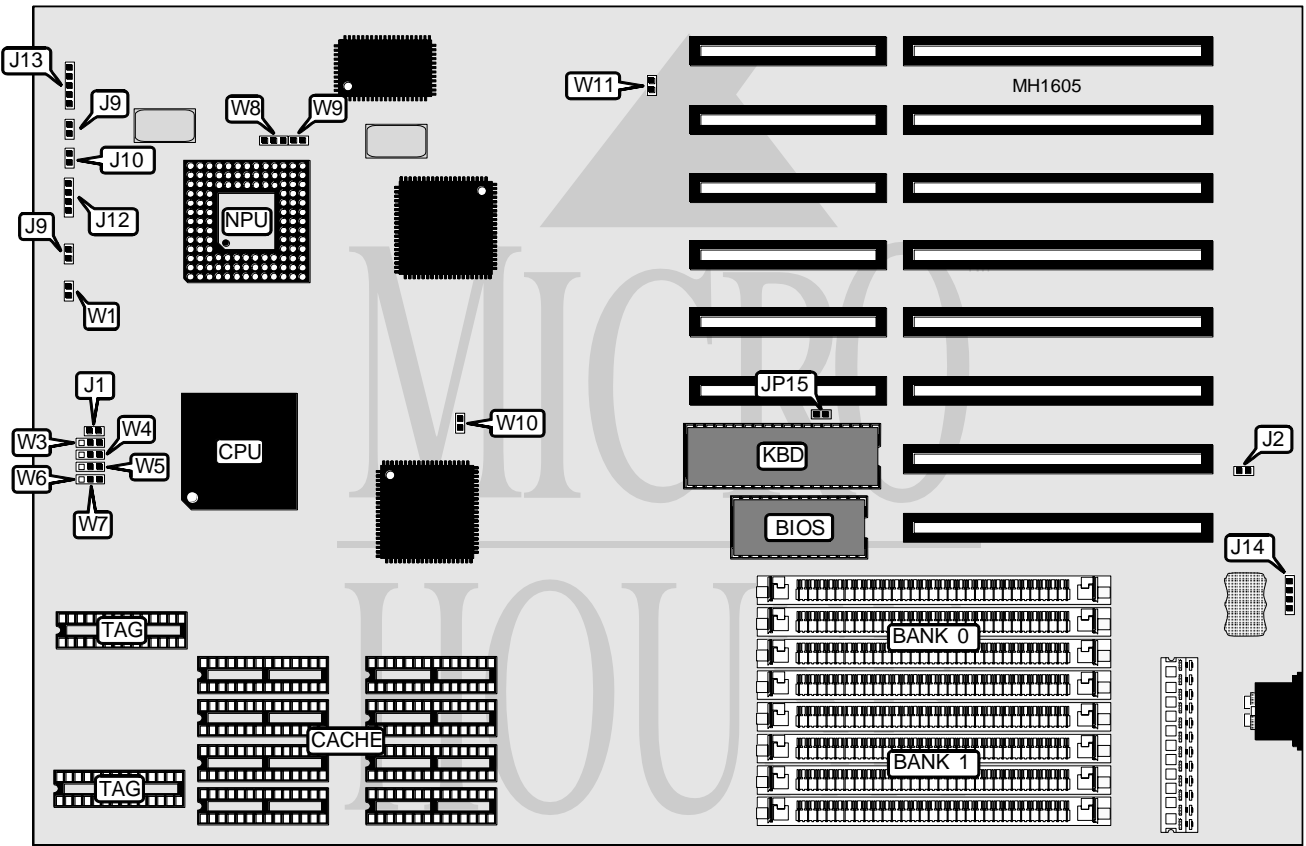


JOINDATA SYSTEMS, INC.

G 3 8 6 U

Processor 80386DX
Processor Speed 33/40MHz
Chip Set UMC
Max. onboard DRAM 32MB
Cache 32/64/128/256KB
BIOS AMI
Dimensions 330mm x 218mm
I/O Options None
NPU Options 80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J9	Speaker	J12
Turbo LED	J10	Power LED & keylock	J13
Turbo switch	J11	External battery	J14

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	J1	Unknown
í Factory configured - do not alter	J2	Unknown
í NPU enabled	W1 & W10	Closed
NPU disabled	W1 & W10	Open
í Monitor type select color	W11	Closed
Monitor type select monochrome	W11	Open
í CMOS memory normal operation (Internal Battery)	W9	Closed
CMOS memory normal operation (External Battery)	W9	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	U21, U22, U23, & U24	U15, U16, U17, & U18	TAG
32KB	(4) 8K x 8	NONE	(2) 16K x 4
64KB	(4) 8K x 8	(4) 8K x 8	(2) 16K x 4
128KB	(4) 32K x 8	NONE	(2) 64K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(2) 64K x 4

Note: U21 - U24 and U15 - U18 may be located on the system board.

CACHE JUMPER CONFIGURATION					
Size	W3	W4	W5	W6	W7
32KB	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 2 & 3	Open
64KB	pins 2 & 3	pins 1 & 2	pins 1 & 2	pins 1 & 2	Open
128KB	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 2 & 3	pins 1 & 2
256KB	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 2 & 3

Note: Pins designated should be in the closed position.

NPU CLOCK CONFIGURATION			
Mode	OSC2	W8	W9
Synchronous	Not installed	pins 1 & 2 closed	Open
Asynchronous 25MHz	50MHz	pins 2 & 3 closed	Closed
Asynchronous 33MHz	66MHz	pins 2 & 3 closed	Closed