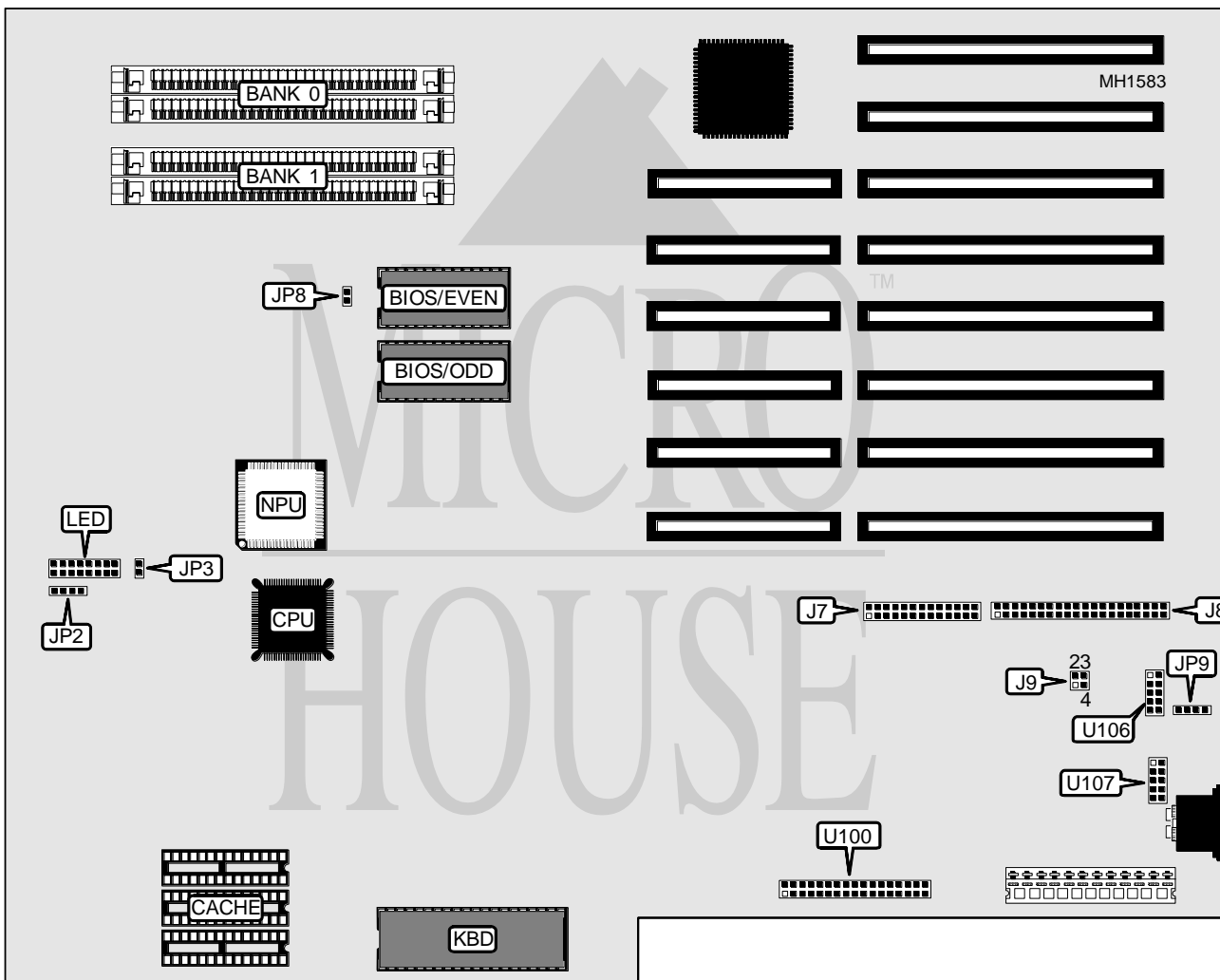


# EVEREX SYSTEMS, INC. STEP 386SX20 GATE ARRAY EV-18107C

<b>Processor</b>	80386SX
<b>Processor Speed</b>	20MHz
<b>Chip Set</b>	C & T
<b>Max. onboard DRAM</b>	16MB
<b>Cache</b>	64KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	355mm x 304mm
<b>I/O Options</b>	Floppy drive interface, IDE interface, parallel port, serial ports (2)
<b>NPU Options</b>	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Parallel port	J7	Floppy drive interface	U100
IDE interface	J8	Serial port 2	U106
Speaker	JP2	Serial port 1	U107
External battery	JP9	LED connector	LED

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## EVEREX SYSTEMS, INC.

## STEP 386SX20 GATE ARRAY EV-18107C

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Cache LED disabled	J3	Closed
Cache LED enabled	J3	Open
í BIOS type select 27256	JP8	Open
BIOS type select 27512	JP8	Closed
í Floppy drive interface enabled	JP9	pins 1 & 2 closed
Floppy drive interface disabled	JP9	pins 3 & 4 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
2.5MB	(2) 256K x 9	(2) 1M x 9
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
8.5MB	(2) 256K x 9	(2) 4M x 9
10MB	(2) 1M x 9	(2) 4M x 9
16MB	(2) 4M x 9	(2)4M x 9

ONBOARD SERIAL/PARALLEL CONFIGURATION			
Port	Connector	IRQ	Address
COM1	U107	IRQ4	3F8h
COM2	U106	IRQ3	2F8h
LPT2	J7	IRQ7	378h

Note: The onboard serial & parallel port configurations are fixed and cannot be altered or disabled.