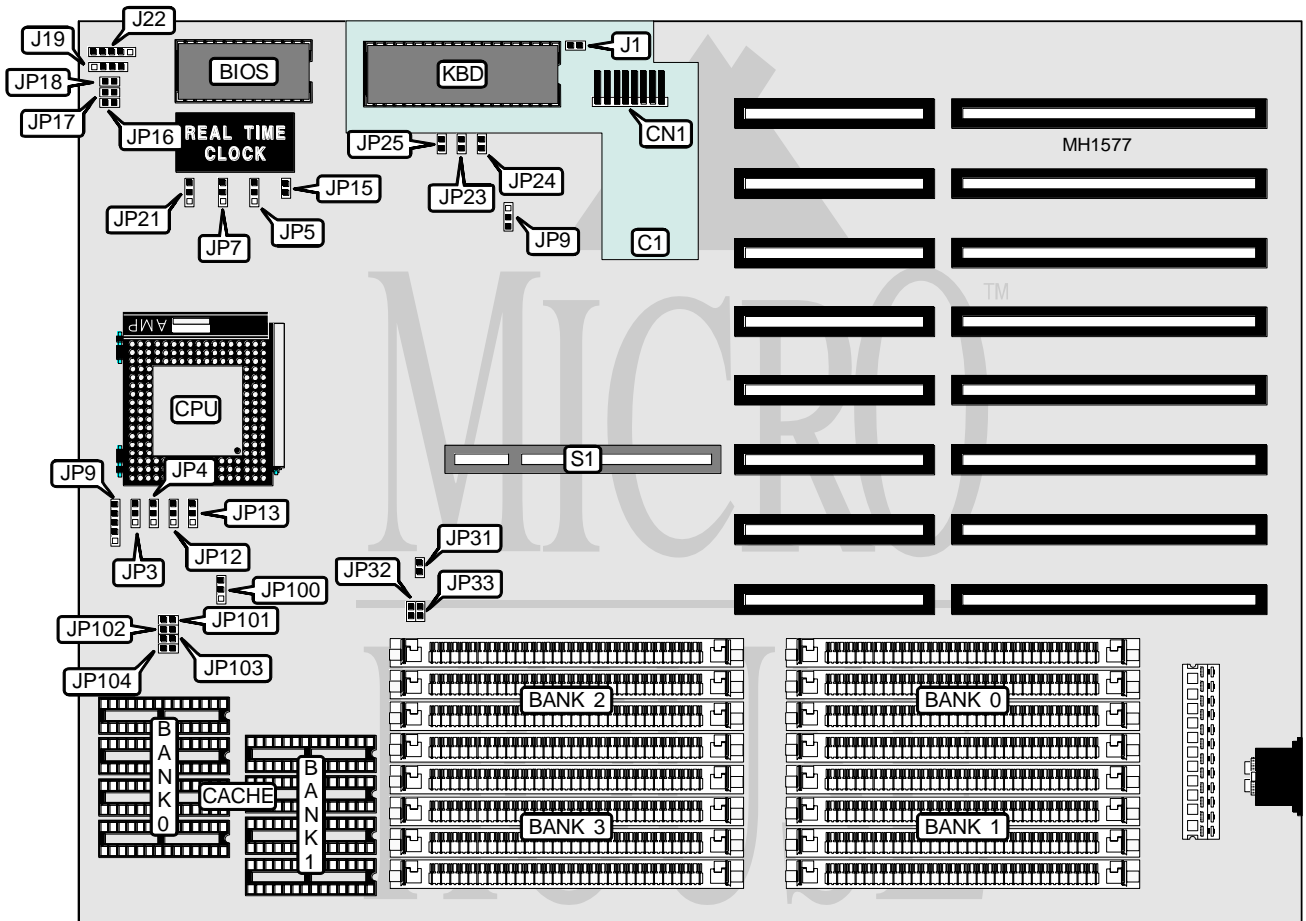


EVEREX SYSTEMS, INC.
STEP 486 EISA VL (EV-3605)

Processor 80486SX/80487SX/80486DX
Processor Speed 20/25/33MHZ
Chip Set C & T
Max. Onboard DRAM 64MB
SRAM Cache 256/512/1024KB
BIOS AMI
Dimensions 330mm x 218mm
I/O Options 32-bit VESA local bus slot, keyboard controller/daughter card
NPU Options None



CONNECTIONS			
Purpose	Location	Purpose	Location
Keyboard controller/daughter card	C1	Turbo LED	JP16
LED front panel connector	CN1	Turbo switch	JP17
Speaker	J19	Reset switch	JP18
Power LED & keylock	J22	32-bit VESA Local bus slot	S1

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	J1	Closed
í Factory configured - do not alter	JP9	pins 2 & 3 closed
í Factory configured - do not alter	JP21	pins 2 & 3 closed
í Factory configured - do not alter	JP23	Closed
í Monitor type select color	JP24	Open
Monitor type select monochrome	JP24	Closed
í CMOS memory normal operation	JP25	Open
CMOS memory clear	JP25	Closed
í Factory configured - do not alter	JP31	Closed
í Factory configured - do not alter	JP32	Closed
í Factory configured - do not alter	JP33	Closed
í Factory configured - do not alter	JP100	pins 2 & 3 closed
í Factory configured - do not alter	JP101	Closed
í Factory configured - do not alter	JP102	Closed
í Factory configured - do not alter	JP103	Closed
í Factory configured - do not alter	JP104	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE	NONE	NONE
18MB	(4) 256K x 9	(4) 256K x 9	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE	NONE	NONE
128MB	(4) 16M x 9	(4) 16M x 9	NONE	NONE
192MB	(4) 16M x 9	(4) 16M x 9	(4) 16M x 9	NONE
256MB	(4) 16M x 9	(4) 16M x 9	(4) 16M x 9	(4) 16M x 9

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STEP 486 EISA VL (EV-3605)

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CACHE JUMPER CONFIGURATION					
Size	JP1	JP3	JP4	JP12	JP13
256KB	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2
512KB	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 1 & 2	pins 2 & 3
1024KB	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3

Note: pins designated should be in the closed position.

CACHE CONFIGURATION			
Size	Cache	Location	TAG
64KB	(8) 8K x 8	Banks 0 & 1	(2) 16K x 4
128KB	(4) 32K x 8	Bank 0	(2) 16K x 4
256KB	(8) 32K x 8	Banks 0 & 1	(2) 16K x 4

CPU TYPE CONFIGURATION			
Type	JP15	JP5	JP7
80486SX	Open	Open	pins 1 & 2 closed
80487SX	Closed	pins 1 & 2 closed	pins 2 & 3 closed
80486DX	Closed	pins 2 & 3 closed	pins 2 & 3 closed