PACKARD BELL
P B 8 0 0 / 9 0 0   (R E V I S I O N   C)

Processor	80286
Processor Speed	8/10MHz
Chip Set	C & T
Max. onboard DRAM	1MB
SRAM Cache	None
BIOS	Phoenix
Dimensions	330mm x 240mm
I/O Options	32-bit memory expansion slot
NPU Options	80287
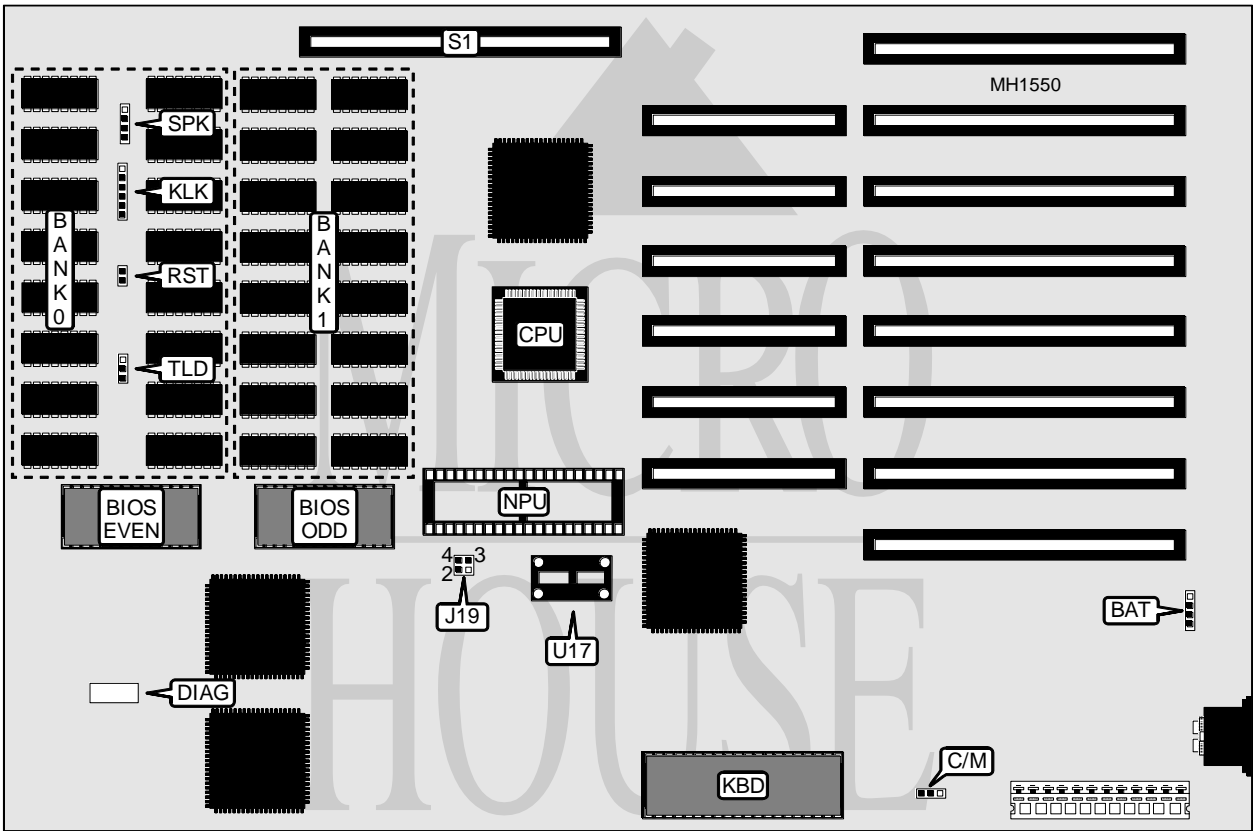
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	BAT	32-bit memory expansion slot	S1
Diagnostics header	DIAG	Speaker	SPK
Power LED & keylock	KLK	Turbo LED	TLD
Reset	RST		
Note: The diagnostics header is used in preproduction, and no aftermarket technical information is available on its usage.			

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Monitor type select color	C/M	pins 1 & 2 closed
Monitor type select monochrome	C/M	pins 2 & 3 closed

SPEED CONFIGURATION		
Speed	U17	J19
8MHz	Empty	pins 1 & 2 closed
10MHz	30-32MHz crystal	pins 3 & 4 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(18) 41256	(18) 41256
Note: Banks are soldered to the board.		