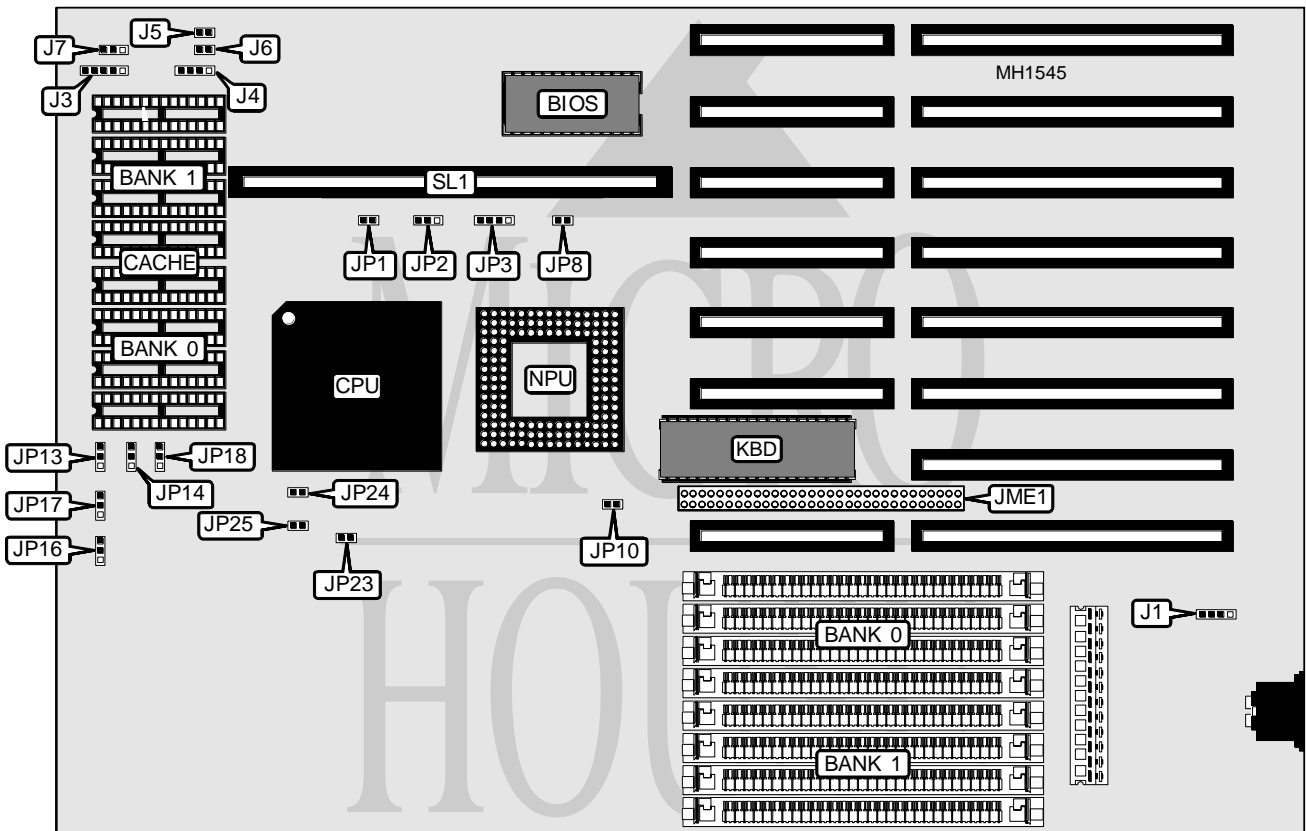


BUFFALO PRODUCTS, INC.

486UV AT

| | |
|--------------------------|--|
| Processor | 80486SX/80487SX/80486DX |
| Processor Speed | 20/25/33/50(internal)/50MHz |
| Chip Set | UMC |
| Max. Onboard DRAM | 32MB |
| Cache | 64/128/256KB |
| BIOS | AMI/Phoenix |
| Dimensions | 330mm x 218mm |
| I/O Options | 32-bit system memory card, 32-bit local bus slot |
| NPU Options | 4167 |



| CONNECTIONS | | | |
|---------------------|----------|----------------------------|----------|
| Purpose | Location | Purpose | Location |
| External battery | J1 | Turbo switch | J6 |
| Power LED & keylock | J3 | Turbo LED | J7 |
| Speaker | J4 | 32-bit system memory card | JME1 |
| Reset switch | J5 | 32-bit VESA Local bus slot | SL1 |

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| USER CONFIGURABLE SETTINGS | | |
|--------------------------------|--------|----------|
| Function | Jumper | Position |
| í Monitor type select color | JP10 | Open |
| Monitor type select monochrome | JP10 | Closed |

| DRAM CONFIGURATION | | |
|--------------------|--------------|--------------|
| Size | Bank 0 | Bank 1 |
| 1MB | (4) 256K x 9 | NONE |
| 2MB | (4) 256K x 9 | (4) 256K x 9 |
| 4MB | (4) 1M x 9 | NONE |
| 5MB | (4) 256K x 9 | (4) 1M x 9 |
| 8MB | (4) 1M x 9 | (4) 1M x 9 |
| 16MB | (4) 4M x 9 | NONE |
| 20MB | (4) 1M x 9 | (4) 4M x 9 |
| 32MB | (4) 4M x 9 | (4) 4M x 9 |

| DRAM CONFIGURATION (WITH MEMORY CARD INSTALLED) | | | | |
|---|--------------|--------------|--------------|--------------|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 1MB | (4) 256K x 9 | NONE | NONE | NONE |
| 2MB | (4) 256K x 9 | (4) 256K x 9 | NONE | NONE |
| 3MB | (4) 256K x 9 | (4) 256K x 9 | (4) 256K x 9 | NONE |
| 4MB | (4) 256K x 9 | (4) 256K x 9 | (4) 256K x 9 | (4) 256K x 9 |
| 4MB | (4) 1M x 9 | NONE | NONE | NONE |
| 5MB | (4) 256K x 9 | (4) 1M x 9 | NONE | NONE |
| 6MB | (4) 256K x 9 | (4) 256K x 9 | (4) 1M x 9 | NONE |
| 8MB | (4) 1M x 9 | (4) 1M x 9 | NONE | NONE |
| 9MB | (4) 256K x 9 | (4) 1M x 9 | (4) 1M x 9 | (4) 1M x 9 |
| 10MB | (4) 256K x 9 | (4) 256K x 9 | (4) 1M x 9 | (4) 1M x 9 |
| 12MB | (4) 1M x 9 | (4) 1M x 9 | (4) 1M x 9 | NONE |
| 16MB | (4) 1M x 9 | (4) 1M x 9 | (4) 1M x 9 | (4) 1M x 9 |
| 16MB | (4) 4M x 9 | NONE | NONE | NONE |
| 20MB | (4) 1M x 9 | (4) 4M x 9 | NONE | NONE |
| 24MB | (4) 1M x 9 | (4) 1M x 9 | (4) 4M x 9 | NONE |
| 32MB | (4) 4M x 9 | (4) 4M x 9 | NONE | NONE |
| 36MB | NONE | (4) 4M x 9 | (4) 4M x 9 | NONE |
| 40MB | NONE | NONE | (4) 4M x 9 | (4) 4M x 9 |
| 48MB | (4) 4M x 9 | (4) 4M x 9 | (4) 4M x 9 | NONE |
| 64MB | (4) 4M x 9 | (4) 4M x 9 | (4) 4M x 9 | (4) 4M x 9 |

| CACHE CONFIGURATION | | |
|---------------------|-------------|-------------|
| Size | Bank 0 | Bank 1 |
| 64KB | (4) 8K x 8 | (4) 8K x 8 |
| 128KB | (4) 32K x 8 | NONE |
| 256KB | (4) 32K x 8 | (4) 32K x 8 |

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| CACHE JUMPER CONFIGURATION | | | | | |
|----------------------------|-------|-------|-------|-------|-------|
| Size | JP13 | JP14 | JP16 | JP17 | JP18 |
| 64KB | 1 & 2 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 |
| 128KB | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 |
| 256KB | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |

Note: Pins designated should be in the closed position.

| CPU SPEED CONFIGURATION | | | | |
|-------------------------|--------|-------------------|-------------------|----------|
| Speed | JP1 | JP2 | JP3 | JP8 |
| 20MHz | Open | pins 2 & 3 closed | pins 3 & 4 closed | See note |
| 25MHz | Open | pins 2 & 3 closed | pins 3 & 4 closed | See note |
| 33MHz | Closed | pins 1 & 2 closed | pins 1 & 2 closed | Open |
| 50MHz | Closed | pins 1 & 2 closed | pins 1 & 2 closed | Open |

Note: For 20MHz 486SX/487SX CPU, U14 oscillator frequency can be 20MHz or 40mhz. If U14 oscillator frequency is 20mhz, open JP8. If U14 oscillator frequency is 40 Mhz, close JP8

| VESA CONFIGURATION | | | |
|------------------------------|--------|--------|--------|
| Type | JP23 | JP24 | JP25 |
| Weitek 4167 installed | Open | Closed | Open |
| Local bus installed | Closed | Open | Open |
| Weitek & Local bus installed | Open | Open | Closed |