DD & TT ENTERPRISE USA COMPANY
DT ESCORT SYSTEM BOARD B

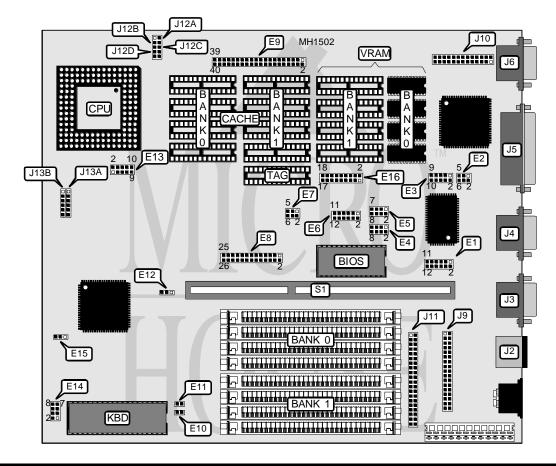|	
Processor	80486SX/80486DX/ODP486SX/80486DX2
Processor Speed	16/20/25/33/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64KB/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	Floppy drive interface, IDE interface parallel port, Proprietary EISA riser card slot, PS/2 mouse port, Serial port (2), VGA port, video feature connector
NPU Options	None

CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port	J2	Reset switch	J12A
Serial port (COM 1)	J3	Turbo switch	J12B
Serial port (COM 2)	J4	Turbo LED	J12C
Parallel port	J5	IDE interface LED	J12D
VGA port	J6	Not used	J12E
Floppy drive interface	J9	Speaker	J13A
Video feature connector	J10	Power LED & keylock	J13B
IDE interface	J11	Proprietary EISA riser card slot	S1

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Floppy drive disabled	E1	pins 1 & 3 closed
Floppy drive enabled	E1	pins 3 & 5 closed
Floppy drive address select 3FO-3F7	E1	pins 7 & 9 closed
Floppy drive address select 370-377	E1	pins 9 & 11 closed
í IDE interface disabled	E1	pins 2 & 4 closed
IDE interface enabled	E1	pins 4 & 6 closed
IDE interface address select 3FO-3F7, 1FO-1F7	E1	pins 8 & 10 closed
IDE interface address select 370-377, 170-177	E1	pins 10 & 12 closed
í Onboard video enabled	E2	pins 1 & 2, 3 & 4, 5 & 6 closed
Onboard video disabled	E2	pins 1 & 2, 3 & 4, 5 & 6 open
í BIOS device select 1MB	E6	pins 1 & 2 closed
BIOS device select 2MB	E6	pins 3 & 5 closed
BIOS device select read only	E6	pins 2 & 4, 7 & 9, 8 & 10 closed
BIOS device select read/write	E6	pins 4 & 6, 9 & 11, 10 & 12
í Chipset mode select 2x for 40MHz or less	E7	pins 3 & 5 closed
Chipset mode select 1x for true 50MHz only	E7	pins 1 & 3 closed
Chipset mode RDY1 set for 1X mode	E7	pins 4 & 6 closed
Chipset mode RDY2 set for 2X mode	E7	pins 2 & 4 closed
í Chipset mode burst rate select 3-2-2-2	E8	pins 23 & 24 closed
Chipset mode burst rate select 2-1-1-1	E8	pins 23 & 24 open
í Chipset mode write states select 0 wait state	E8	pins 25 & 26 closed
Chipset mode write states select 1 wait state	E8	pins 25 & 26 open
í Factory configured - do not alter	E9	pins 39 & 40 closed
í PS/2 mouse enable at IRQ12	E10	Closed
PS/2 mouse disable	E10	Open
í Factory configured do not alter	E11	Unknown
í CPU clock select mode 2X	E12	pins 2 & 3 closed
CPU clock select mode 1 x for 486DX 50MHz	E12	pins 1 & 2 closed
í Factory configured - do not alter	E15	pins 1 & 2 closed
Note: If there is no monochrome hercules card present then the system will automatically assume that address 378h is LPT1 and address 278h is LPT2. pins designated should be in the closed position.		

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DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 4M x 9	None
16MB	(4) 4M x 9	(4) 4M x 9
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

SRAM CONFIGURATION			
Size	Cache	Location	TAG
64KB	(8) 8K x 8	Bank 0 & 1	(1) 8K x 4
256KB	(8) 32K x 8	Bank 0 & 1	(1) 32K x 4

SRAM JUMPER CONFIGURATION		
E9	64KB	256KB
pins 1, 3, 5	pins 1 & 3 closed	pins 3 & 5 closed
pins 2, 4, 6	pins 2 & 4 closed	pins 2 & 4 closed
pins 7, 9, 11	pins 7 & 9 closed	pins 9 & 11 closed
pins 8, 10, 12	Open	pins 8 & 10 closed
pins 13, 15, 17	pins 15 & 17 closed	pins 15 & 17 closed
pins 14, 16, 18	pins 14 & 16 closed	pins 14 & 16 closed
pins 19, 21, 23	pins 21 & 23 closed	pins 21 & 23 closed
pins 20, 22, 24	Open	pins 20 & 22 closed
pins 26, 28, 30	pins 26 & 28 closed	pins 26 & 28 closed
pins 31, 33, 35	pins 33 & 35 closed	pins 33 & 35 closed
pins 32, 34, 36	pins 34 & 36 closed	pins 34 & 36 closed
pins 37 & 38	Open	Closed
Note: For non-cache configuration pins 25, 26, & 27 should be in the closed position.		

NON CACHE CONFIGURATION E16					
pins 1-3-5	pins 2-4-6	pins 7-9-11	8 & 10, 11 & 12	pins 13 & 14	15 & 16, 17 & 18
3 & 5 closed	2 & 4 closed	7 & 9 closed	Closed	Closed	Open

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D T E S C O R T S Y S T E M B O A R D B

CPU TYPE CONFIGURATION		
Jumper	80486DX/80486DX2	80386SX/80486SLC
E8/pins 1, 3, 5	pins 1 & 3 closed	pins 3 & 5 closed
E8/pins 2, 4, 6	pins 2 & 4 closed	pins 4 & 6 closed
E8/pins 7, 9, 11	pins 7 & 9 closed	pins 9 & 11 closed
E8/pins 8, 10, 12	pins 8 & 10 closed	pins 10 & 12 closed
E8/pins 13 & 14	Closed	Open
E8/pins 15 & 16	Open	Closed
E8/pins 17 & 18	Closed	Open
E8/pins 19 & 20	Closed	Open
E8/pins 21 & 22	Open	Closed
E13/pins 1 & 2, 3 & 4, 5 & 6	pins 1 & 2, 3 & 4, 5 & 6 closed	pins 2 & 3 closed
Note: settings for the 80386SX and 80486SLC are factory configured and should not be altered.		

CPU SPEED CONFIGURATION						
Speed	U51	U52	pins 1 & 2	pins 3 & 4	pins 5 & 6	pins 7 & 8
16MHz	installed	empty	Closed	Open	Closed	N/A
16MHz	empty	installed	Closed	Open	Closed	Closed
20MHz	installed	empty	Closed	Open	Open	N/A
20MHz	empty	installed	Open	Open	Closed	Closed
25MHz	installed	empty	Open	Closed	Closed	N/A
25MHz	empty	installed	Closed	Closed	Open	Closed
33MHz	installed	empty	Open	Closed	Open	N/A
33MHz	empty	installed	Open	Closed	Open	Closed
40MHz	installed	empty	Open	Open	Closed	N/A
40MHz	empty	installed	Closed	Open	Open	Closed
50MHz	installed	empty	Open	Closed	Closed	N/A
50MHz	empty	installed	Closed	Closed	Open	Closed

SERIAL PORT A (J3) CONFIGURATION			
COM	I/O Address	E4	E5
í COM1	3F8h	pins 3 & 5, 2 & 4, 7 & 8	pins 3 & 5, 2 & 4, 7 & 8
COM3	3E8h	pins 1 & 3, 4 & 6, 7 & 8	pins 1 & 3, 4 & 6, 7 & 8
COM4	2E8h	pins 3 & 5, 4 & 6, 7 & 8	pins 3 & 5, 4 & 6
Disabled	N/A	pins 1 & 3, 2 & 4	pins 1 & 3, 2 & 4
Note: pins designated should be in the closed position.			

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SERIAL PORT B (J4) CONFIGURATION			
COM	I/O Address	E4	E5
í COM2	2F8h	pins 3 & 5, 2 & 4, 7 & 8	pins 3 & 5, 2 & 4, 7 & 8
COM3	3E8h	pins 1 & 3, 4 & 6, 7 & 8	pins 1 & 3, 4 & 6, 7 & 8
COM4	2E8h	pins 3 & 5, 4 & 6, 7 & 8	pins 3 & 5, 4 & 6
Disabled	N/A	pins 1 & 3, 2 & 4	pins 1 & 3, 2 & 4
Note: pins designated should be in the closed position.			

PARALLEL PORT (J5) CONFIGURATION			
COM	IRQ	I/O Address	E3
í LPT1	IRQ7	3BCh	pins 3 & 5, 4 & 6, 7 & 8
LPT1	IRQ5	3BCh	pins 3 & 5, 4 & 6, 9 & 10
LPT2	IRQ7	378h	pins 3 & 5, 2 & 6, 7 & 8
LPT2	IRQ5	378h	pins 3 & 5, 2 & 6, 9 & 10
LPT3	IRQ7	278h	pins 1 & 3, 4 & 6, 7 & 8
LPT3	IRQ5	278h	pins 1 & 3, 4 & 6, 9 & 10
Disabled	N/A	N/A	pins 1 & 3, 2 & 4
Note: pins designated should be in the closed position.			

VIDEO MEMORY CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(4) 44256	NONE
1024KB	(4) 44256	(4) 44256
Note: Bank 0 is permanently soldered onto the board.		