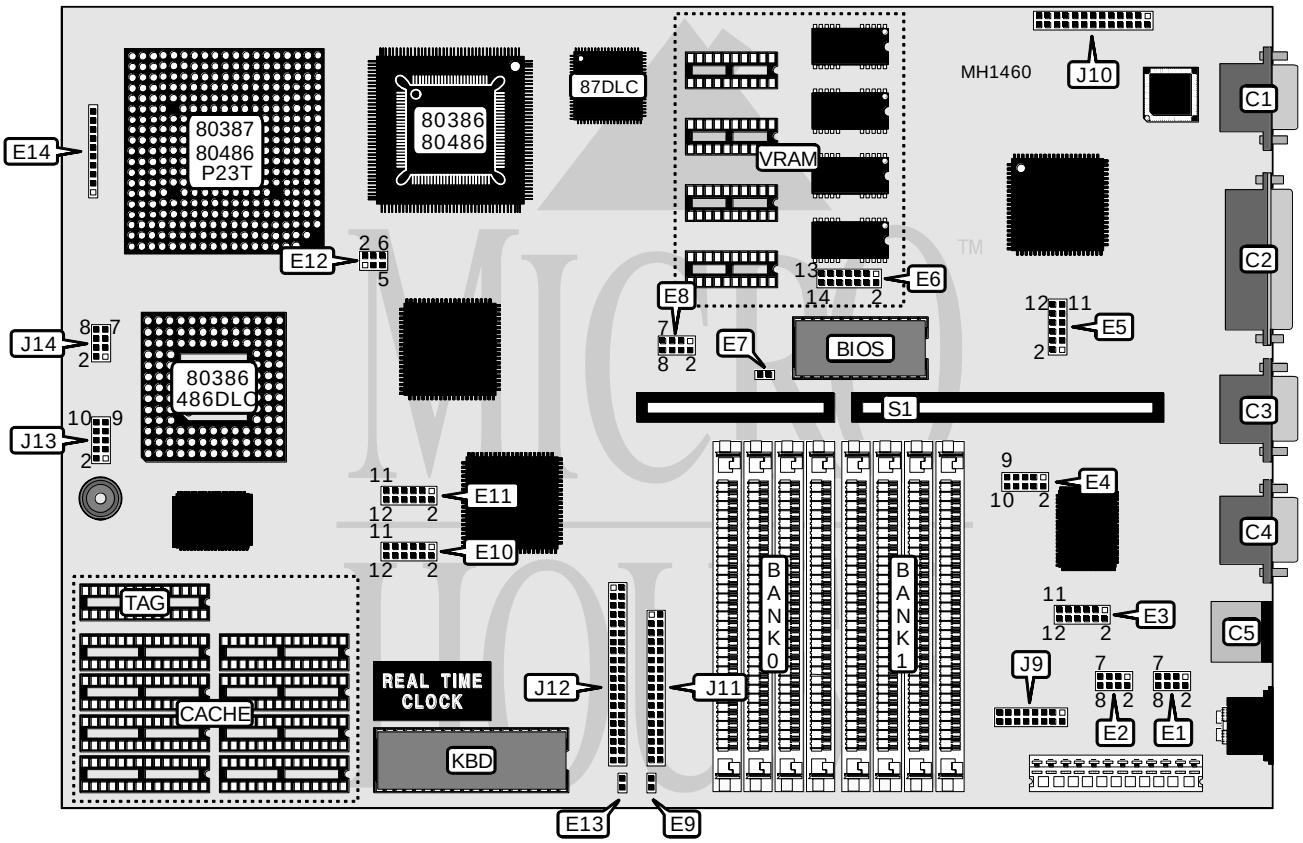
DD & TT ENTERPRISE USA COMPANY
D T - L B 2 A L

Processor	80386DX/CX486DLC/80486SX/80487SX/80486DX ODP486SX/80486DX2/ODP586SX
Processor Speed	20/25/33/40/50(internal)/50/66(internal) MHz
Chip Set	Symphony
Max. onboard DRAM	32MB
Cache	64/256KB
BIOS	AMI
Dimensions	330mm x 220mm
I/O Options	Floppy drive interface, game port, IDE interface, parallel port, PS/2 mouse port, serial ports (2), VGA feature connector, VGA port
NPU Options	80387DX/CX487DLC



CONNECTIONS			
Purpose	Location	Purpose	Location
VGA port	C1	IDE interface	J12
Parallel port	C2	Speaker	J13/pins 1, 3, 5, 7
Serial port B	C3	Power LED & keylock	J13/pins 2, 4, 6, 8, 10
Serial port A	C4	Reset switch	J14/pins 1 & 2
PS/2 mouse port	C5	Turbo switch	J14/pins 3 & 4
Game port	J9	Turbo LED	J14/pins 5 & 6
VGA feature connector	J10	IDE interface LED	J14/pins 7 & 8
Floppy drive interface	J11	Riser card	S1

Continued on next page . . .

DD & TT ENTERPRISE USA COMPANY
D T - L B 2 A L

. . . continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í PS/2 mouse port enabled	E9	Open
PS/2 mouse port disabled	E9	Closed
í CMOS memory normal operation	E13	Open
CMOS memory clear	E13	Closed
í Onboard speaker enabled	J13	pins 1 & 3 closed
Onboard speaker disabled	J13	pins 1 & 3 open

SERIAL PORT A (C4) CONFIGURATION					
COM	Interrupt	Address	E2/pins 1, 3, & 5	E2/pins 2, 4, & 6	E2/Pins 7 & 8
í COM1	IRQ4	3F8h	pins 3 & 5 closed	pins 2 & 4 closed	Closed
COM3	IRQ4	3E8h	pins 1 & 3 closed	pins 4 & 6 closed	Closed
COM4	IRQ3	2E8h	pins 3 & 5 closed	pins 4 & 6 closed	Open
Disabled	N/A	N/A	pins 1 & 3 closed	pins 2 & 4 closed	Open

SERIAL PORT B (C3) CONFIGURATION					
COM	Interrupt	Address	E1/pins 1, 3, & 5	E1/pins 2, 4, & 6	E1/Pins 7 & 8
í COM2	IRQ3	2F8h	pins 3 & 5 closed	pins 2 & 4 closed	Closed
COM3	IRQ4	3E8h	pins 1 & 3 closed	pins 4 & 6 closed	Closed
COM4	IRQ3	2E8h	pins 3 & 5 closed	pins 4 & 6 closed	Open
Disabled	N/A	N/A	pins 1 & 3 closed	pins 2 & 4 closed	Open

PARALLEL PORT (C2) CONFIGURATION						
LPT	Interrupt	Address	E4/1,3,&5	E4/2,4,&6	E4/7&8	E4/9&10
í LPT1	IRQ7	378h	pins 3 & 5	pins 2 & 4	Closed	Open
LPT1	IRQ5	378h	pins 3 & 5	pins 2 & 4	Open	Closed
LPT2	IRQ7	278h	pins 1 & 3	pins 2 & 4	Closed	Open
LPT2	IRQ5	278h	pins 1 & 3	pins 4 & 6	Open	Closed
LPT3	IRQ7	278h	pins 1 & 3	pins 4 & 6	Closed	Open
LPT3	IRQ5	278h	pins 1 & 3	pins 4 & 6	Open	Closed
Disabled	N/A	N/A	pins 1 & 3	pins 2 & 4	Open	Open

Notes: Pins designated should be in the closed position.
In order for LPT3 to be selected, a Hercules monographics card with an active parallel port must be present.

FLOPPY DRIVE INTERFACE CONFIGURATION	
I/O Address	E3/pins 1, 3, 5, 7, 9, 11
í 3F0-3F7	pins 3 & 5, 7 & 9 closed
370-377	pins 3 & 5, 9 & 11 closed
Disabled	pins 1 & 3 closed

Continued on next page . . .

DD & TT ENTERPRISE USA COMPANY
D T - L B 2 A L

. . . continued from previous page

IDE INTERFACE CONFIGURATION	
I/O Address	E3/pins 2, 4, 6, 8, 10, 12
í 3F0-3F7, 1F0-1F7	pins 4 & 6, 8 & 10 closed
370-377, 170-177	pins 4 & 6, 10 & 12 closed
Disabled	pins 2 & 4 closed

ONBOARD VGA PORT CONFIGURATION		
Function	Jumper	Position
í Onboard VGA enabled	E6	pins 1 & 2, 3 & 4 closed
Onboard VGA disabled	E6	pins 1 & 2, 3 & 4 open
í Onboard VGA memory 512KB	E6	pins 13 & 14 closed
Onboard VGA memory 1MB	E6	pins 13 & 14 open

ONBOARD VGA VIDEO MODE CONFIGURATION (800x600 RESOLUTION)			
Refresh Rate	Mode	E6/pins 9 & 10	E6/pins 11 & 12
56Hz	Non-Interlaced	Open	Open
56Hz	Non-Interlaced	Closed	Closed
60Hz	Non-Interlaced	Closed	Open
72Hz	Non-Interlaced	Open	Closed

ONBOARD VGA VIDEO MODE CONFIGURATION (1024 x 768 RESOLUTION)			
Refresh Rate	Mode	E6/pins 5 & 6	E6/pins 7 & 8
43.5Hz	Interlaced	Open	Open
43.5Hz	Interlaced	Closed	Closed
60Hz	Non-Interlaced	Closed	Open
70Hz	Non-Interlaced	Open	Closed

BIOS CONFIGURATION		
Function	Jumper	Position
í BIOS ROM 1MB	E5	pins 1 & 3 closed
BIOS ROM 2MB	E5	pins 3 & 5 closed
í Flash BIOS program mode enabled	E5	pins 2 & 4, 7 & 9, 8 & 10
Flash BIOS program mode disabled	E5	pins 4 & 5, 9 & 11, 10 & 12

CACHE CONFIGURATION			
Size	Max Cachable	Cache	TAG
64KB	8MB	(8) 8K x 8	(1) 32K x 8
256KB	32MB	(8) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION	
Size	E11
64KB	pins 3 & 5, 4 & 6 closed
256KB	pins 1 & 3, 2 & 4, 7 & 8, 9 & 10, 11 & 12 closed

Continued on next page . . .

DD & TT ENTERPRISE USA COMPANY
D T - L B 2 A L

. . . continued from previous page

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CPU TYPE CONFIGURATION		
CPU Type	E10	E14
80386DX PQFP/PGA	pins 3 & 4, 9 & 11, 10 & 12	N/A
80486SX PQFP	pins 1 & 2, 5 & 6, 7 & 9, 8 & 10	pins 1 & 2 closed
80486SX PGA	pins 1 & 2, 5 & 6, 7 & 9, 8 & 10	pins 2 & 3 closed
80487SX PGA	pins 1 & 2, 5 & 6, 7 & 9, 8 & 10	pins 1 & 2, 3 & 4, 7 & 8 closed
ODP486SX PGA	pins 1 & 2, 5 & 6, 7 & 9, 8 & 10	pins 1 & 2, 3 & 4, 7 & 8 closed
80486DX PQFP	pins 1 & 2, 5 & 6, 7 & 9, 8 & 10	pins 1 & 2, 3 & 4, 5 & 6 closed
80486DX PGA	pins 1 & 2, 5 & 6, 7 & 9, 8 & 10	pins 1 & 2, 3 & 4, 5 & 6 closed
80486DX2 PGA	pins 1 & 2, 5 & 6, 7 & 9, 8 & 10	pins 1 & 2, 3 & 4, 5 & 6 closed
ODP586SX PGA	pins 1 & 2, 5 & 6, 7 & 9, 8 & 10	pins 1 & 2, 3 & 4, 5 & 6 closed
Note: Pins designated should be in the closed position.		

CPU SPEED CONFIGURATION			
CPU Speed	E7	E8	E12
20MHz	pins 1 & 2	pins 1 & 2	pins 3 & 5, 4 & 6
25MHz	pins 1 & 2	pins 3 & 4, 5 & 6	pins 3 & 5, 4 & 6
33MHz	pins 1 & 2	pins 3 & 4	pins 3 & 5, 4 & 6
40MHz	pins 1 & 2	pins 5 & 6	pins 3 & 5, 4 & 6
50MHz	pins 2 & 3	pins 3 & 4, 5 & 6	pins 1 & 3, 2 & 4
Note: Pins designated should be in the closed position.			