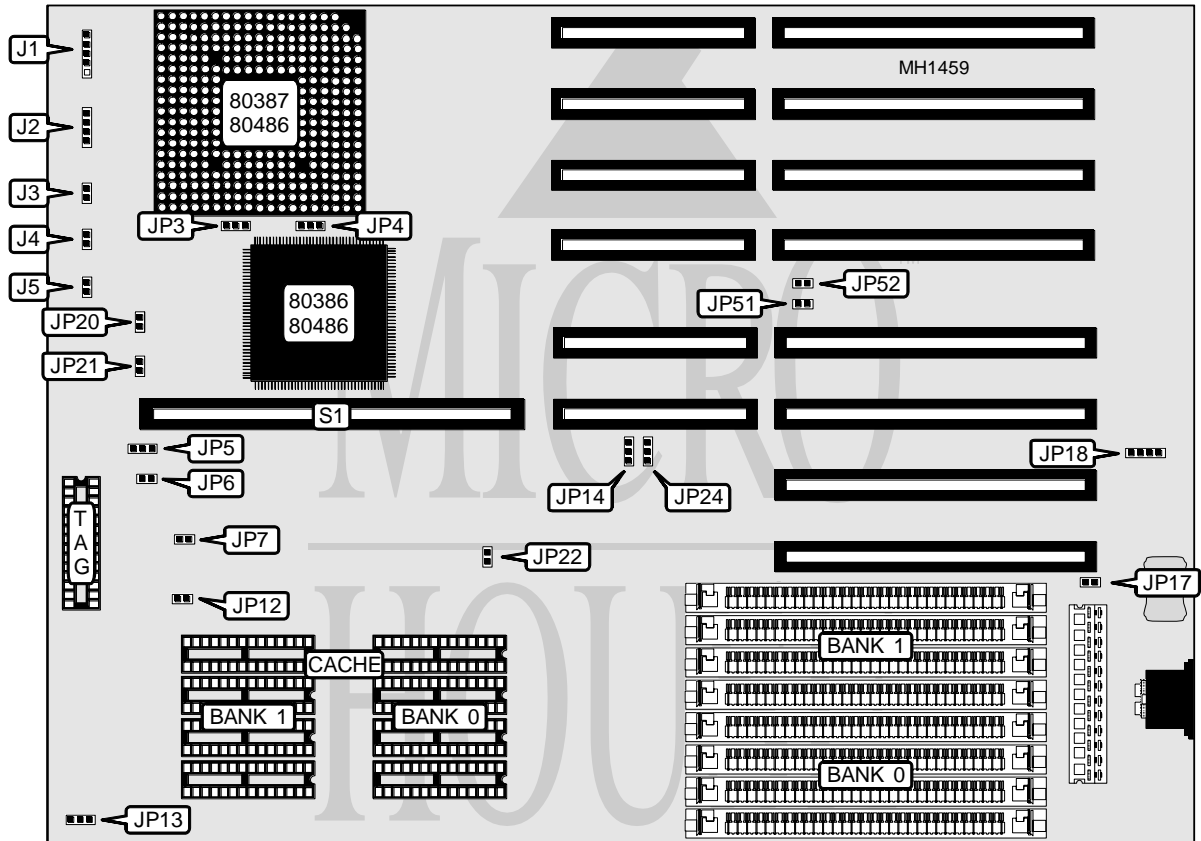


AUSTIN COMPUTER SYSTEMS 3486LE WINSTATION

Processor	80386DX/80486SX/80487SX/80486DX/80486DX2
Processor Speed	20/25/33/40/50(internal)/50/66(internal) MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
SRAM Cache	64/128/256KB
BIOS	AMI
Dimensions	310mm x 218mm
I/O Options	32-bit local bus slot
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	External battery	JP18
Speaker	J2	32-bit local bus	S1
Reset switch	J5		

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AUSTIN COMPUTER SYSTEMS

3486LE WINSTATION

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
Unused jumper pad	J3	Open
Unused jumper pad	J4	Open
í Monitor type select color	JP17	Closed
Monitor type select monochrome	JP17	Open
í CMOS memory normal operation (Internal Battery)	JP18	pins 2 & 3 closed
CMOS memory normal operation (External Battery)	JP18	pins 1 & 4 closed
CMOS memory clear	JP18	pins 3 & 4 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

SRAM JUMPER CONFIGURATION				
Size	JP6	JP7	JP12	JP13
64KB	Open	Open	Open	pins 2 & 3 closed
128KB	Open	Closed	Closed	pins 1 & 2 closed
256KB	Closed	Closed	Closed	pins 2 & 3 closed

SRAM CONFIGURATION			
Size	Cache	Location	TAG
64KB	(8) 8K x 8	Banks 0 & 1	(1) 8K x 8
128KB	(4) 32K x 8	Bank 0	(1) 8K x 8
256KB	(8) 32K x 8	Banks 0 & 1	(1) 32K x 8

CPU TYPE CONFIGURATION								
CPU	JP1	JP2	JP3	JP4	JP5	JP20	JP21	JP24
80386DX PQFP	N/A	N/A	2 & 3	N/A	1 & 2	Open	Closed	1 & 2
80486SX PQFP	N/A	N/A	1 & 2	N/A	2 & 3	Open	Open	2 & 3
80486SX PGA	Open	2 & 3	1 & 2	Open	2 & 3	Closed	Closed	2 & 3
80487SX PGA	Closed	1 & 2	1 & 2	2 & 3	2 & 3	Closed	Closed	2 & 3
80486DX PGA	Closed	1 & 2	1 & 2	1 & 2	2 & 3	Closed	Closed	2 & 3
ODP486SX PGA	Closed	1 & 2	1 & 2	2 & 3	2 & 3	Closed	Closed	2 & 3
80486DX2 PGA	Closed	1 & 2	1 & 2	1 & 2	2 & 3	Closed	Closed	2 & 3

Note: Pins designated should be in the closed position.
 The locations of jumpers JP1 and JP2 are unknown.

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CPU SPEED CONFIGURATION								
CPU	Speed	Type	JP14	JP15	JP16	JP22	JP51	JP52
80486SX	25MHz	PGA	pins 1 & 2	N/A	N/A	N/A	Closed	Open
80486DX	25MHz	PGA	pins 1 & 2	N/A	N/A	N/A	Closed	Open
80486SX	25MHz	PQFP	pins 1 & 2	Open	Closed	N/A	N/A	N/A
80486DX2	50i MHz	PGA	pins 1 & 2	Open	Closed	N/A	N/A	N/A
80386/DLC	33MHz	PQFP	pins 2 & 3	N/A	N/A	Closed	Closed	Open
80486SX	33MHz	PGA	pins 1 & 2	N/A	N/A	N/A	Open	Closed
80486DX	33MHz	PGA	pins 1 & 2	N/A	N/A	N/A	Open	Closed
80486SX	33MHz	PQFP	pins 1 & 2	Closed	Open	N/A	N/A	N/A
80486DX2	66i MHz	PGA	pins 1 & 2	Closed	Open	N/A	N/A	N/A
80386/DLC	40MHz	PQFP	pins 2 & 3	N/A	N/A	Open	Open	Open
80486DX	50MHz	PGA	pins 2 & 3	N/A	N/A	N/A	Closed	Open

Note: Pins designated should be in the closed position.
The locations of jumpers JP15 and JP16 are unknown.