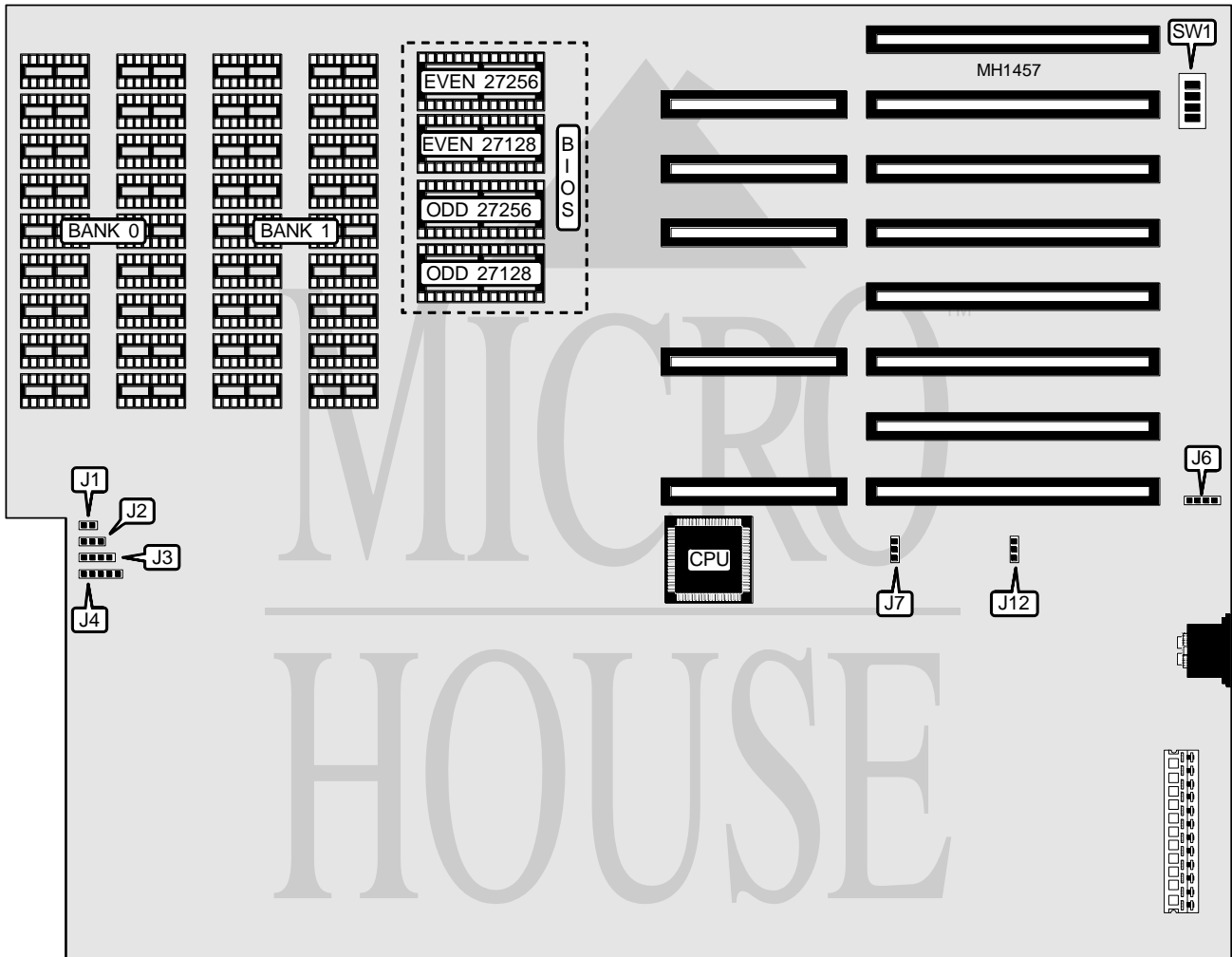


PACKARD BELL IS/VT286 (VERSION 1)

Processor	80286
Processor Speed	10MHz
Chip Set	Toshiba
Max. Onboard DRAM	1MB
SRAM Cache	None
BIOS	Phoenix
Dimensions	355mm x 305mm
I/O Options	None
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J1	Power LED & keylock	J4
Speaker	J3	External battery	J6

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PACKARD BELL

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USER CONFIGURABLE SETTINGS

Function	Jumper	Position
í Monitor type select color	J7	pins 1 & 2 closed
Monitor type select monochrome	J7	pins 2 & 3 closed
System speed select low	J12	pins 1 & 2 closed
System speed select high	J12	pins 2 & 3 closed

BIOS TYPE CONFIGURATION

Type	SW1/1	SW1/2	SW1/3	SW1/4
27128	On	On	Off	Off
27256	Off	Off	On	On

DRAM CONFIGURATION

Size	Bank 0	Bank 1	J2
512KB	(18) 41256	NONE	pins 2 & 3 closed
640KB	(18) 41256	(18) 4164	pins 2 & 3 closed
1024KB	(18) 41256	(18) 41256	pins 1 & 2 closed