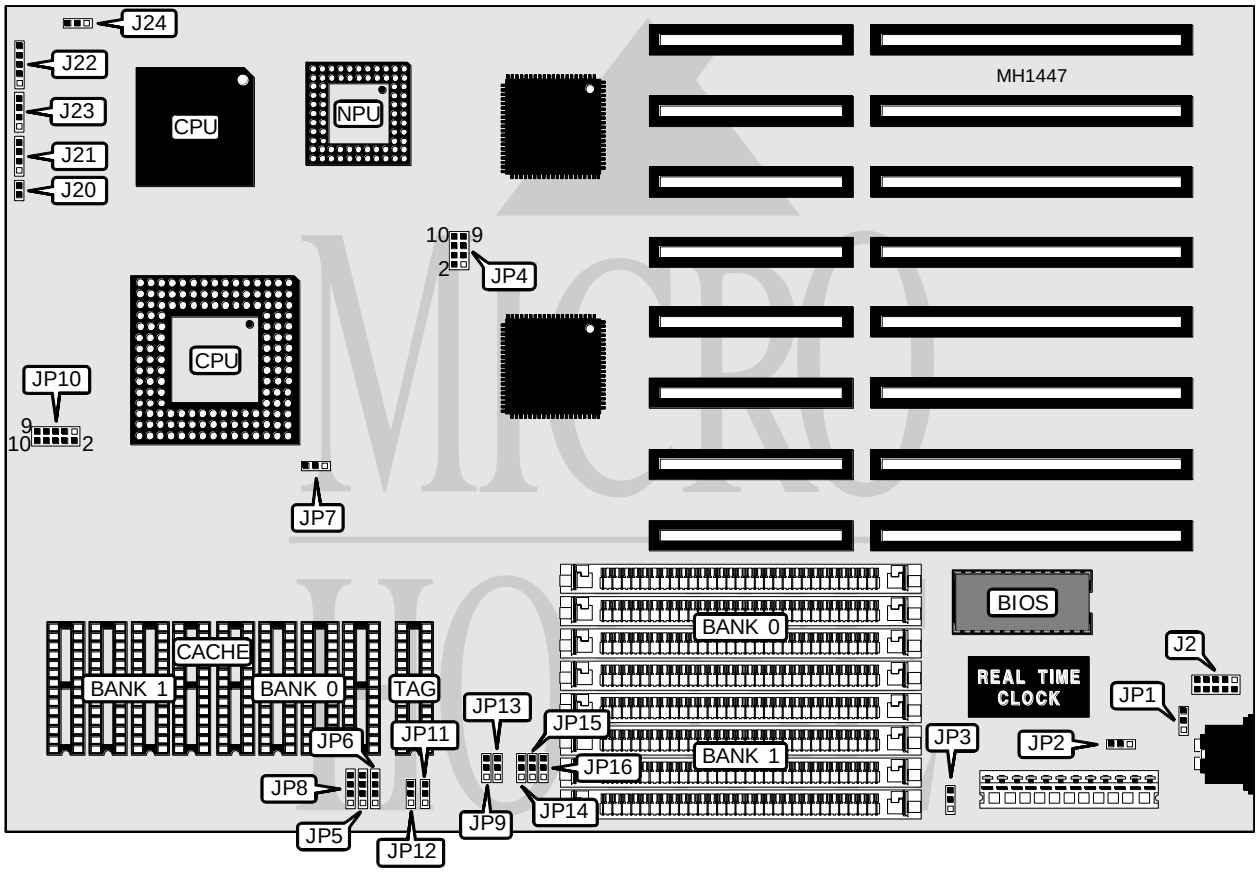
DEICO ELECTRONICS, INC.

P R E D A T O R - S

Processor	80386DX/80486SX/80487SX/ODP486SX/80486DX/80486DX2
Processor Speed	20/25/33/40/50(internal)/50/66(internal) MHz
Chip Set	Symphony
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	PS/2 mouse port
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port header	J2	Power LED & keylock	J22
Reset switch	J20	Speaker	J23
Turbo switch	J21/3 & 4	CPU speed indicator power (+5VDC)	J24
Turbo LED	J21/1 & 2		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í PS/2 mouse port enabled (IRQ12)	JP1	pins 1 & 2 closed
PS/2 mouse port disabled	JP1	pins 2 & 3 closed
í Monitor type select color	JP2	pins 1 & 2 closed
Monitor type select monochrome	JP2	pins 2 & 3 closed
í CMOS memory normal operation	JP3	pins 1 & 2 closed
CMOS memory clear	JP3	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE JUMPER CONFIGURATION				
Size	CPU	JP5, JP6, JP8	JP9, JP13, JP14, JP15, JP16	JP11 & JP12
64KB	80386DX	pins 1 & 2	pins 1 & 2 closed	pins 2 & 3
64KB	any 80486	pins 1 & 2	pins 1 & 2 closed	pins 1 & 2
128KB	80386DX	pins 2 & 3	pins 2 & 3 closed	pins 1 & 2
128KB	any 80486	pins 2 & 3	pins 2 & 3 closed	pins 1 & 2
256KB	80386DX	pins 3 & 4	pins 1 & 2 closed	pins 2 & 3
256KB	any 80486	pins 3 & 4	pins 1 & 2 closed	pins 1 & 2
Note: Pins designated should be in the closed position.				

SRAM CONFIGURATION			
Size	Cache	Location	TAG
64KB	(8) 8K x 8	Banks 0 & 1	(1) 8K x 8
128KB	(4) 32K x 8	Bank 0	(1) 32K x 8
256KB	(8) 32K x 8	Banks 0 & 1	(1) 32K x 8

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CPU SPEED CONFIGURATION				
CPU type	Speed	JP4	JP7	JP10
80386DX	25MHz	pins 7 & 8 closed	pins 1 & 2 closed	pins 3 & 4 closed
80386DX	33MHz	pins 7 & 8 closed	pins 1 & 2 closed	pins 5 & 6 closed
80386DX	40MHz	pins 7 & 8 closed	pins 1 & 2 closed	pins 7 & 8 closed
80486SX	20MHz	pins 5 & 6 closed	pins 1 & 2 closed	pins 1 & 2 closed
80486SX	25MHz	pins 5 & 6 closed	pins 1 & 2 closed	pins 3 & 4 closed
80487SX	20MHz	pins 3 & 4 closed	pins 1 & 2 closed	pins 1 & 2 closed
80487SX	25MHz	pins 3 & 4 closed	pins 1 & 2 closed	pins 3 & 4 closed
ODP486SX	25/50i MHz	pins 3 & 4 closed	pins 1 & 2 closed	pins 3 & 4 closed
ODP486SX	33/66i MHz	pins 3 & 4 closed	pins 1 & 2 closed	pins 5 & 6 closed
80486DX/DX2	25/50i MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 3 & 4 closed
80486DX/DX2	33/66i MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 5 & 6 closed
80486DX	50MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 9 & 10 closed