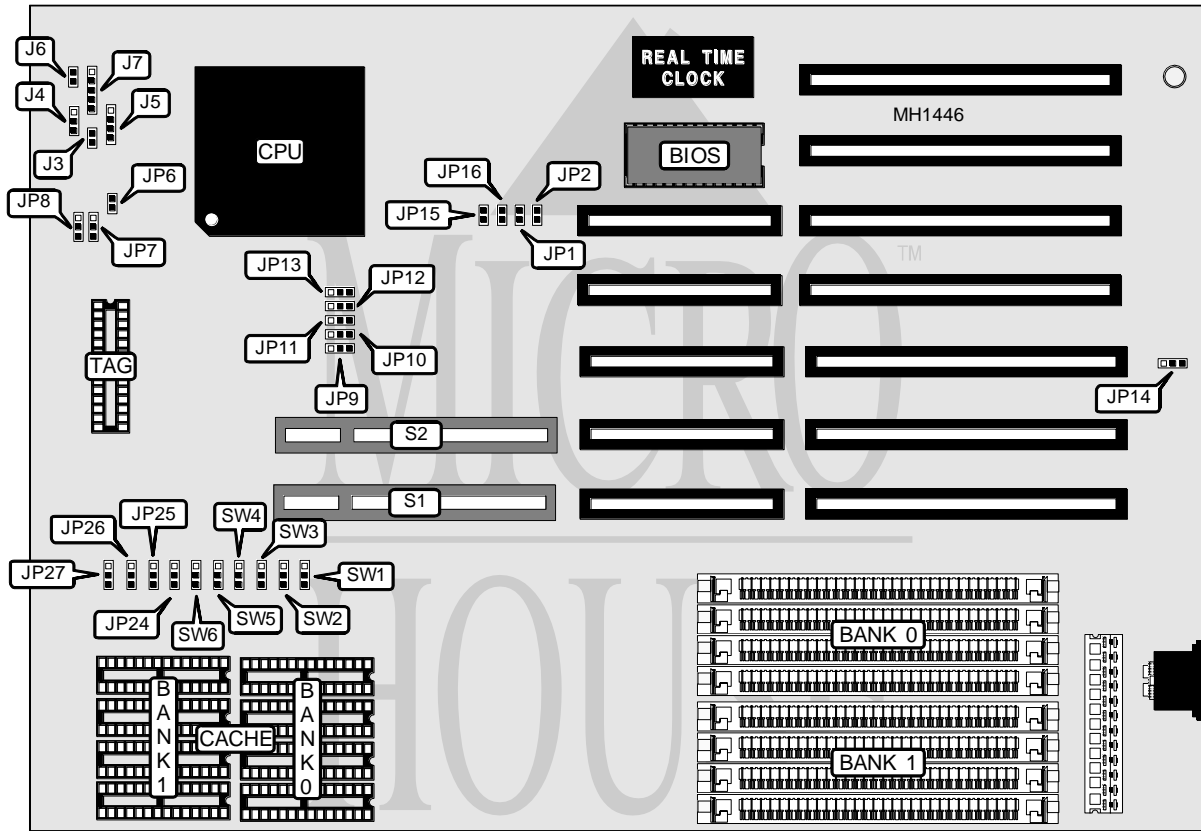


# AUSTIN COMPUTER SYSTEMS

## 486 LX VESA LOCAL BUS WINSTATION

<b>Processor</b>	80486SX/80487SX/80486DX/80486DX2
<b>Processor Speed</b>	25/33/50(Internal)/50/66(Internal)MHz
<b>Chip Set</b>	SIS
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	64/128/256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit VESA local bus slots (2)
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J3	Reset switch	J6
Turbo switch	J4	Power LED & keylock	J7
Speaker	J5	32-bit VESA local bus slots	S1 & S2

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# AUSTIN COMPUTER SYSTEMS

## 486 LX VESA LOCAL BUS WINSTATION

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured do not alter	JP1	open
í Monitor type select color	JP2	closed
Monitor type select monochrome	JP2	open
í Factory configured do not alter	JP9	pins 1 & 2 closed
í Factory configured do not alter	JP14	pins 1 & 2 closed
í Factory configured do not alter	JP15	open
í Factory configured do not alter	JP16	open
í Factory configured do not alter	JP24	Pins 2 & 3 open
í Factory configured do not alter	JP25	pins 1 & 2 open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Cache	Location	TAG
64KB	(8) 8K x 8	Banks 0 & 1	(1) 8K x 8
128KB	(4) 32K x 8	Bank 0	(1) 8K x 8
256KB	(8) 32K x 8	Banks 0 & 1	(1) 32K x 8

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## AUSTIN COMPUTER SYSTEMS

### 486 LX VESA LOCAL BUS WINSTATION

CACHE JUMPER CONFIGURATION						
Size	SR1	SR2	SR3	SR4	SR5	SR6
64KB	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 1 & 2
128KB	pins 1 & 2	pins 1 & 2	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 1 & 2
256KB	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION			
CPU Type	JP6	JP7	JP8
80486SX	open	open	pins 2 & 3 closed
80487SX	closed	pins 2 & 3 closed	pins 1 & 2 closed
80486DX	closed	pins 1 & 2 closed	pins 1 & 2 closed
80486DX2	closed	pins 1 & 2 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION				
Speed	JP10	JP11	JP12	JP13
25MHz	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 1 & 2
33MHz	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 1 & 2
50iMHz	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 1 & 2
50MHz	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2
66iMHz	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 1 & 2

Note Pins designated should be in the closed position.

VESA CPU TYPE (ID0 & ID1) CONFIGURATION		
CPU TYPE	JP24 (ID0)	JP25 (ID1)
80386	Pins 2 & 3 closed	pins 1 & 2 closed
80486	pins 2 & 3 closed	pins 1 & 2 closed

VESA WAIT STATE (ID2) CONFIGURATION		
Wait states	CPU Speed	JP26
0 wait states	≤ 33MHz	pins 1 & 2 closed
1 wait state	> 33MHz	pins 2 & 3 closed

VESA WAIT STATE (ID2) CONFIGURATION	
CPU Speed	JP27
≤ 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed