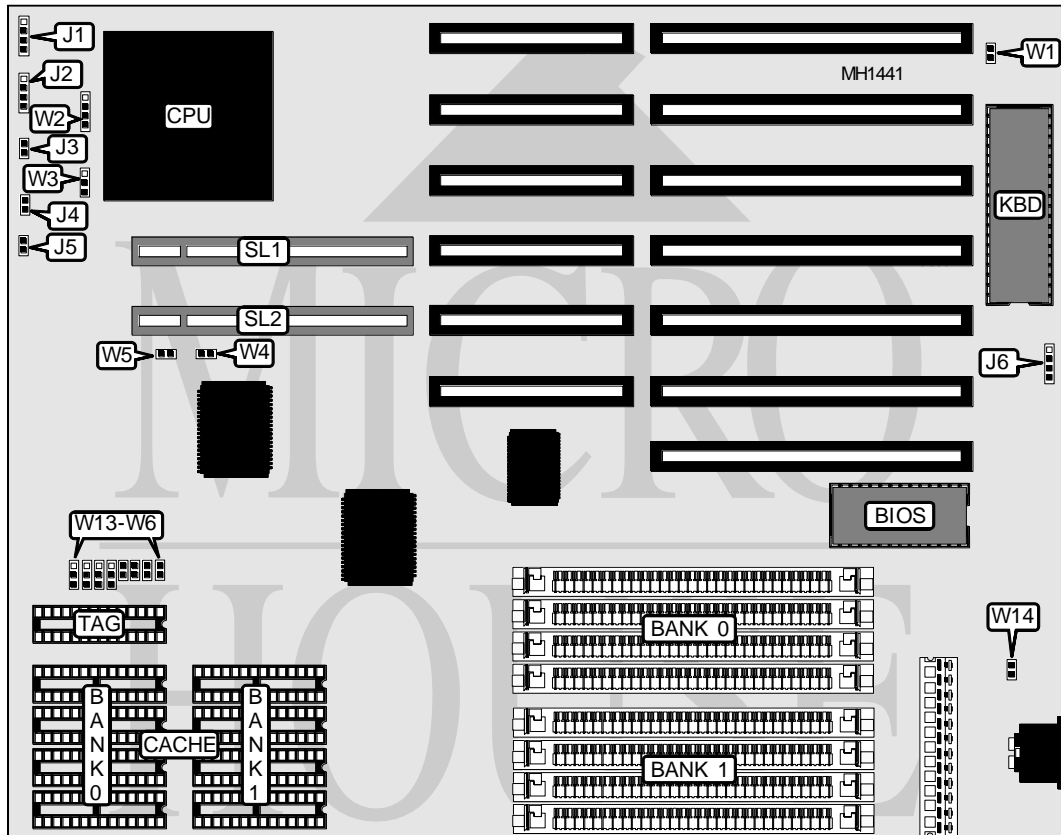


PINE TECHNOLOGY

PT-426V/PT-426I/PT-602A

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	220mm x 250mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Reset switch	J5
Speaker	J2	External battery	J6
Turbo switch	J3	32-bit VESA Local bus slots	SL1 & SL2
Turbo LED	J4		

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PINE TECHNOLOGY

PT-426V/PT-426I/PT-602A

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor select type monochrome	W1	Open
Monitor select type color	W1	Closed
í Factory configured do not alter	W14	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256 x 9	NONE
2MB	(4) 256 x 9	(4) 256 x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256 x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	W6	W7	W8	W9
64KB	Open	Open	Open	Open
128KB	Open	Closed	Open	Closed
256KB	Closed	Closed	Closed	Closed

CACHE JUMPER CONFIGURATION (continued)				
Size	W10	W11	W12	W13
64KB	pins 1 & 2 closed	Open	pins 1 & 2 closed	pins 1 & 2 closed
128KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
256KB	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION		
Type	W2	W3
80486SX	pins 2 & 3 closed	Open
80487SX	pins 1 & 2, 3 & 4 closed	pins 1 & 2
80486DX	pins 1 & 2, 3 & 4 closed	pins 2 & 3
ODP486SX	pins 1 & 2, 3 & 4 closed	pins 1 & 2
80486DX2	pins 1 & 2, 3 & 4 closed	pins 2 & 3

VESA WAIT STATE/BUS SPEED CONFIGURATION			
CPU speed	Wait states	W4	W5
< 33MHz	0 wait states	Open	Open
> 33MHz	1 wait state	Closed	Closed