## **MAGITRONICS** 386-40UM

**Processor** 80386DX **Processor Speed** 33/40MHz **Chip Set** UMC Max. Onboard DRAM 32MB

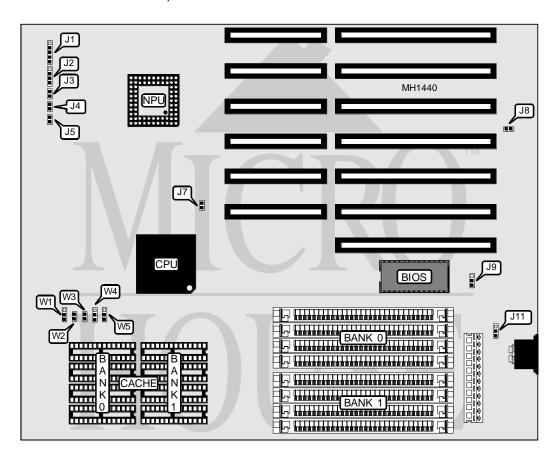
**SRAM Cache** 64/128/256KB

**BIOS** AMI

**Dimensions** 260mm x 220mm

I/O Options None

**NPU Options** 80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Turbo switch	J4
Speaker	J2	Turbo LED	J5
Reset switch	J3	External battery	J11

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USER CONFIGURABLE SETTINGS			
Function	Jumper/Switch	Position	
í NPU enabled	J7	Closed	
NPU disabled	J7	Open	
í Monitor type select color	J8	Closed	
Monitor type select monochrome	J8	Open	
í Battery select internal	J9	pins 2 & 3 closed	
Battery select external	J9	pins 1 & 2 closed	
í CMOS memory normal operation	J11	pins 2 & 3 closed	
CMOS memory clear	J11	pins 1 & 2 closed	
Note: When selecting an external battery (J9), the external battery must be connected to J11.			

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

SRAM CONFIGURATION				
Size	Cache	Location	TAG	
64KB	(8) 8K x 8	Banks 0 & 1	(1) 8K x 8	
128KB	(4) 32K x 8	Bank 0	(1) 8K x 8	
256KB	(8) 32K x 8	Banks 0 & 1	(1) 32K x 8	

SRAM JUMPER CONFIGURATION					
Size	W1	W2	W3	W4	W5
64KB	pins 2 & 3	Open	Open	pins 1 & 2	pins 1 & 2
128KB	pins 1 & 2	Open	Closed	pins 2 & 3	pins 1 & 2
256KB	pins 2 & 3	Closed	Closed	pins 2 & 3	pins 2 & 3
Note: Pins designated should be in the closed position					