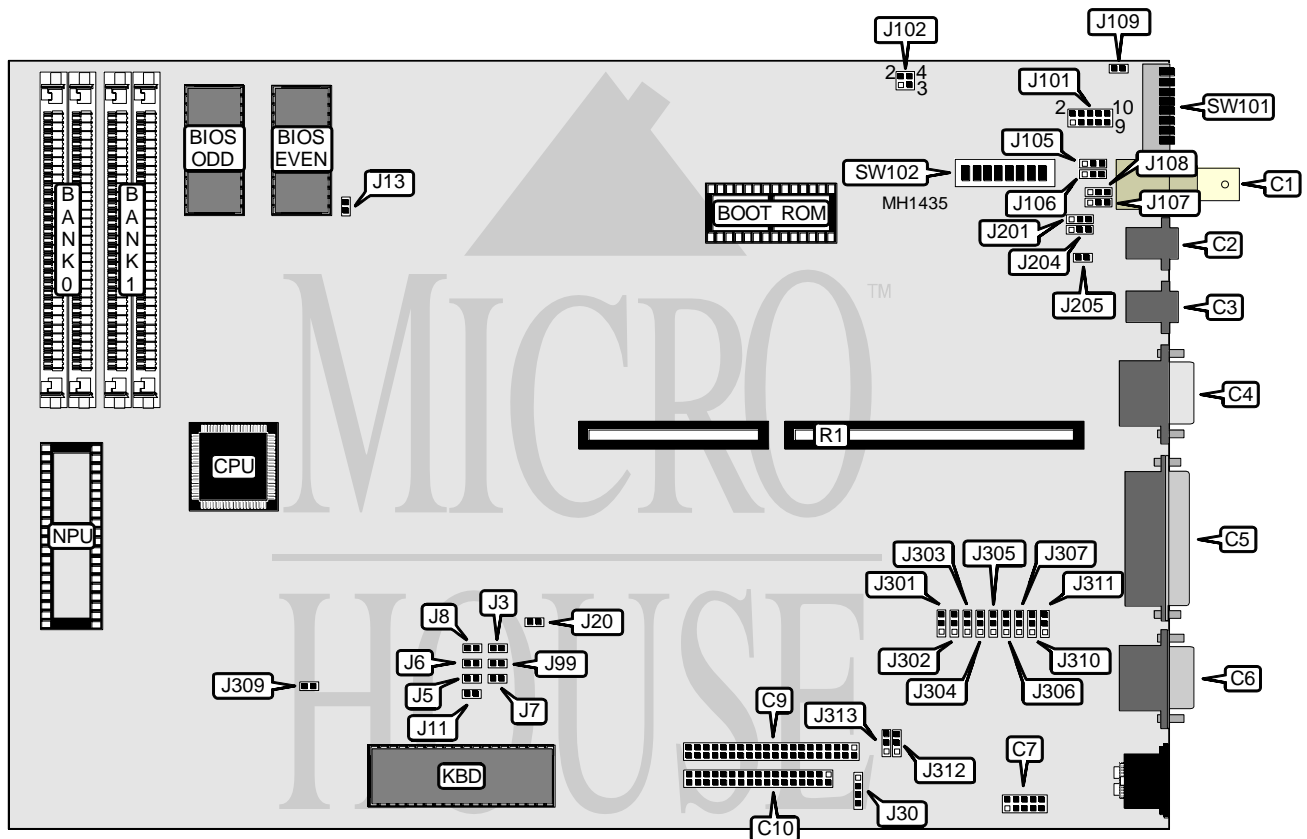


## XINETRON, INC.

## XINET XI-286/LS2121

<b>Processor</b>	80286
<b>Processor Speed</b>	12MHz
<b>Chip Set</b>	C & T
<b>Max. Onboard DRAM</b>	4MB
<b>SRAM Cache</b>	None
<b>BIOS</b>	Unknown
<b>Dimensions</b>	360mm x 340mm
<b>I/O Options</b>	Arcnet interfaces (BNC and twisted pair), floppy drive interface, IDE interface, parallel port, serial ports (2), VGA port
<b>NPU Options</b>	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
BNC network interface	C1	Serial port (COM2)	C7
RJ11 network interface	C2	IDE interface	C9
RJ11 network interface	C3	Floppy drive interface	C10
VGA port	C4	External battery	J30
Parallel port	C5	Riser card	R1
Serial port (COM1)	C6		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
Factory configured - do not alter	J3	Unknown
Factory configured - do not alter	J5	Unknown
í Monitor type select monochrome, EGA/VGA	J8	Open
Monitor type select color	J8	Closed
Factory configured - do not alter	J11	Unknown
í BIOS type select 27128	J13	Open
BIOS type select 27256	J13	Closed
Factory configured - do not alter	J99	Unknown
í Onboard terminator enabled	J109	Open
Onboard terminator disabled	J109	Closed
Factory configured - do not alter	J205	Unknown
í Floppy interface enabled	J306	pins 2 & 3 closed
Floppy interface disabled	J306	pins 1 & 2 closed
Factory configured - do not alter	J307	Unknown
Factory configured - do not alter	J309	Unknown
Factory configured - do not alter	J310	Unknown
Factory configured - do not alter	J311	Unknown
í IDE interface enabled	J312 & J313	pins 2 & 3 closed
IDE interface disabled	J312 & J313	pins 1 & 2 closed
Note: If the system is on either end of a linear bus network segment, or used in a star network topology, the onboard terminator may be used instead of an external terminator.		

ONBOARD VIDEO CONFIGURATION		
Function	J201	J204
í Onboard video enabled	pins 2 & 3 closed	pins 2 & 3 closed
Onboard video disabled	pins 1 & 2 closed	pins 1 & 2 closed

SERIAL PORT CONFIGURATION				
COM1	COM2	J303	J304	J305
í Enabled	Enabled	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
Enabled	Disabled	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
Disabled	Enabled	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
Disabled	Disabled	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed

PARALLEL PORT 1 (C3) CONFIGURATION		
LPT	J301	J302
í LPT 1	pins 1 & 2 closed	pins 2 & 3 closed
LPT 2	pins 2 & 3 closed	pins 2 & 3 closed
LPT 3	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	pins 1 & 2 closed	pins 1 & 2 closed

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DRAM CONFIGURATION					
Size	J6	J7	J20	Bank 0	Bank 1
512KB	Closed	Closed	Closed	(2) 256K x 9	NONE
1MB	Closed	Open	Closed	(2) 256K x 9	(2) 256K x 9
2MB	Open	Open	Closed	(2) 1M x 9	NONE
4MB	Closed	Closed	Open	(2) 1M x 9	(2) 1M x 9

ARCNET NETWORK INTERFACE CONFIGURATION						
Function	J101	J103	J105	J106	J107	J108
í BNC connection	Closed	Closed	pins 2 & 3	pins 2 & 3	N/A	N/A
Twisted pair (1st pair)	Closed	Closed	pins 1 & 2	pins 1 & 2	pins 2 & 3	pins 2 & 3
Twisted pair (2nd pair)	Closed	Closed	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2
Disabled	Open	Open	N/A	N/A	N/A	N/A

ARCNET INTERRUPT REQUEST CONFIGURATION	
Setting	J101
í IRQ 2	pins 9 & 10 closed
IRQ 3	pins 7 & 8 closed
IRQ 4	pins 5 & 6 closed
IRQ 5	pins 3 & 4 closed
IRQ 7	pins 1 & 2 closed

NODE ADDRESS								
Node	SW101/1	SW101/2	SW101/3	SW101/4	SW101/5	SW101/6	SW101/7	SW101/8
0	-	-	-	-	-	-	-	-
1	On	On	On	On	On	On	On	Off
2	On	On	On	On	On	On	Off	On
3	On	On	On	On	On	On	Off	Off
4	On	On	On	On	On	Off	On	On
251	Off	Off	Off	Off	Off	On	Off	Off
252	Off	Off	Off	Off	Off	Off	On	On
253	Off	Off	Off	Off	Off	Off	On	Off
254	Off	Off	Off	Off	Off	Off	Off	On
255	Off	Off	Off	Off	Off	Off	Off	Off

Notes: Node address 0 is used for messaging between nodes and must not be used.  
A total of 255 node address settings are available. The switches are a binary representation of the decimal node addresses. Switch 8 is the Least Significant Bit and switch 1 is the Most Significant Bit. The switches have the following decimal values: switch 1=128, 2=64, 3=32, 4=16, 5=8, 6=4, 7=2, 8=1. Turn Off the switches and add the values of the Off switches to obtain the correct node address. (On=0, Off=1)

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RESPONSE/RECONFIGURATION TIMEOUTS				
Response Time	Idle Time	Reconfig. Time	J102/pins 1 & 2	J102/pins 3 & 4
1130μ	1237μ	1680ms	Closed	Closed
563μ	624μ	1680ms	Closed	Open
285μ	316μ	1680ms	Open	Closed
78μ	86μ	840ms	Open	Open

BASE MEMORY ADDRESS AND BOOT ROM ADDRESS						
Base Address	Boot ROM Address	SW102/1	SW102/2	SW102/3	SW102/4	SW102/5
C0000-C07FFh	C2000-C3FFFh	On	On	On	On	On
C0800-C0FFFh	C2000-C3FFFh	Off	On	On	On	On
C1000-C17FFh	C2000-C3FFFh	On	Off	On	On	On
C1800-C1FFFh	C2000-C3FFFh	Off	Off	On	On	On
C4000-C47FFh	C6000-C7FFFh	On	On	Off	On	On
C4800-C4FFFh	C6000-C7FFFh	Off	On	Off	On	On
C5000-C57FFh	C6000-C7FFFh	On	Off	Off	On	On
C5800-C5FFFh	C6000-C7FFFh	Off	Off	Off	On	On
CC000-CC7FFh	CE000-CFFFFh	On	On	On	Off	On
CC800-CCFFFh	CE000-CFFFFh	Off	On	On	Off	On
CD000-CD7FFh	CE000-CFFFFh	On	Off	On	Off	On
CD800-CDFFFh	CE000-CFFFFh	Off	Off	On	Off	On
D0000-D07FFh	D2000-D3FFFh	On	On	Off	Off	On
D0800-D0FFFh	D2000-D3FFFh	Off	On	Off	Off	On
D1000-D17FFh	D2000-D3FFFh	On	Off	Off	Off	On
D1800-D1FFFh	D2000-D3FFFh	Off	Off	Off	Off	On
D4000-D47FFh	D6000-D7FFFh	On	On	On	On	Off
D4800-D4FFFh	D6000-D7FFFh	Off	On	On	On	Off
D5000-D57FFh	D6000-D7FFFh	On	Off	On	On	Off
D5800-D5FFFh	D6000-D7FFFh	Off	Off	On	On	Off
D8000-D87FFh	DA000-DBFFFh	On	On	Off	On	Off
D8800-D8FFFh	DA000-DBFFFh	Off	On	Off	On	Off
D9000-D97FFh	DA000-DBFFFh	On	Off	Off	On	Off
D9800-D9FFFh	DA000-DBFFFh	Off	Off	Off	On	Off
DC000-DC7FFh	DE000-DFFFFh	On	On	On	Off	Off
DC800-DCFFFh	DE000-DFFFFh	Off	On	On	Off	Off
DD000-DD7FFh	DE000-DFFFFh	On	Off	On	Off	Off
DD800-DDFFFh	DE000-DFFFFh	Off	Off	On	Off	Off
E0000-E07FFh	E2000-E3FFFh	On	On	Off	Off	Off
E0800-E0FFFh	E2000-E3FFFh	Off	On	Off	Off	Off
E1000-E17FFh	E2000-E3FFFh	On	Off	Off	Off	Off
E1800-E1FFFh	E2000-E3FFFh	Off	Off	Off	Off	Off

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BASE MEMORY ADDRESS CONFIGURATION			
Address	SW102/6	SW102/7	SW102/8
260-26Fh	Off	Off	Off
290-29Fh	On	Off	Off
2E0-2EFh	Off	On	Off
2F0-2FFh	On	On	Off
300-30Fh	Off	Off	On
350-35Fh	On	Off	On
380-38Fh	Off	On	On
3E0-3EFh	On	On	On