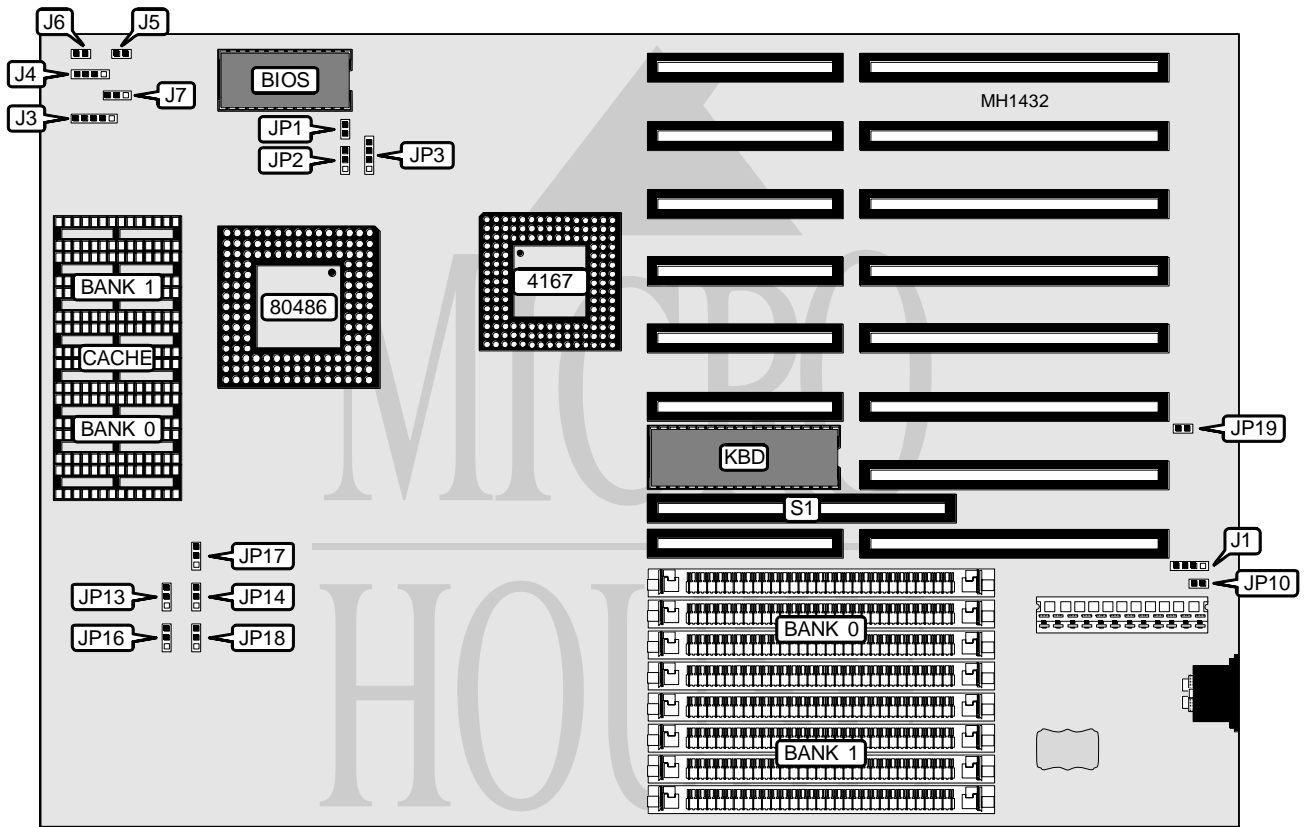


# ADVANCED INTREGRATION RESEARCH, INC.

## 486UF

<b>Processor</b>	80486SX/80487SX/80486DX/80486DX2
<b>Processor Speed</b>	20/25/33/40/50(internal)/50/66(internal)MHz
<b>Chip Set</b>	UMC
<b>Max. onboard DRAM</b>	32MB
<b>Cache</b>	64/128/256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 220mm
<b>I/O Options</b>	32-bit external memory card
<b>NPU Options</b>	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	J6
Power LED & keylock	J3	Turbo LED	J7
Speaker	J4	32-bit memory card	S1
Reset	J5		

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## 486UF

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CPU speed select iOSC/2	JP8	Closed
CPU speed select iOSC/1	JP8	Open
í Monitor type select color	JP10	Open
Monitor type select monochrome	JP10	Closed
í CMOS memory normal operation	JP19	Open
CMOS memory clear	JP19	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
7MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9
8MB	(4)1M x 9	(4)1M x 9	NONE	NONE
9MB	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	NONE
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
28MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9
32MB	(4)4M x 9	(4)4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9

Note: Banks 2 & 3 are located on the optional external memory card inserted into S1.

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# ADVANCED INTREGRATION RESEARCH, INC.

## 486UF

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CPU TYPE CONFIGURATION			
CPU	JP1	JP2	JP3
80486SX	Open	pins 2 & 3 closed	pins 3 & 4 closed
80487SX	Closed	pins 1 & 2 closed	pins 2 & 3 closed
80486DX/DX2	Closed	pins 1 & 2 closed	pins 1 & 2 closed

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8

CACHE JUMPER CONFIGURATION					
Size	JP13	JP14	JP16	JP17	JP18
64KB	pins 1 & 2	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 1 & 2
128KB	pins 2 & 3	pins 1 & 2	pins 2 & 3	pins 2 & 3	pins 1 & 2
256KB	pins 1 & 2	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3
Note: Pins designated should be in the closed position.					