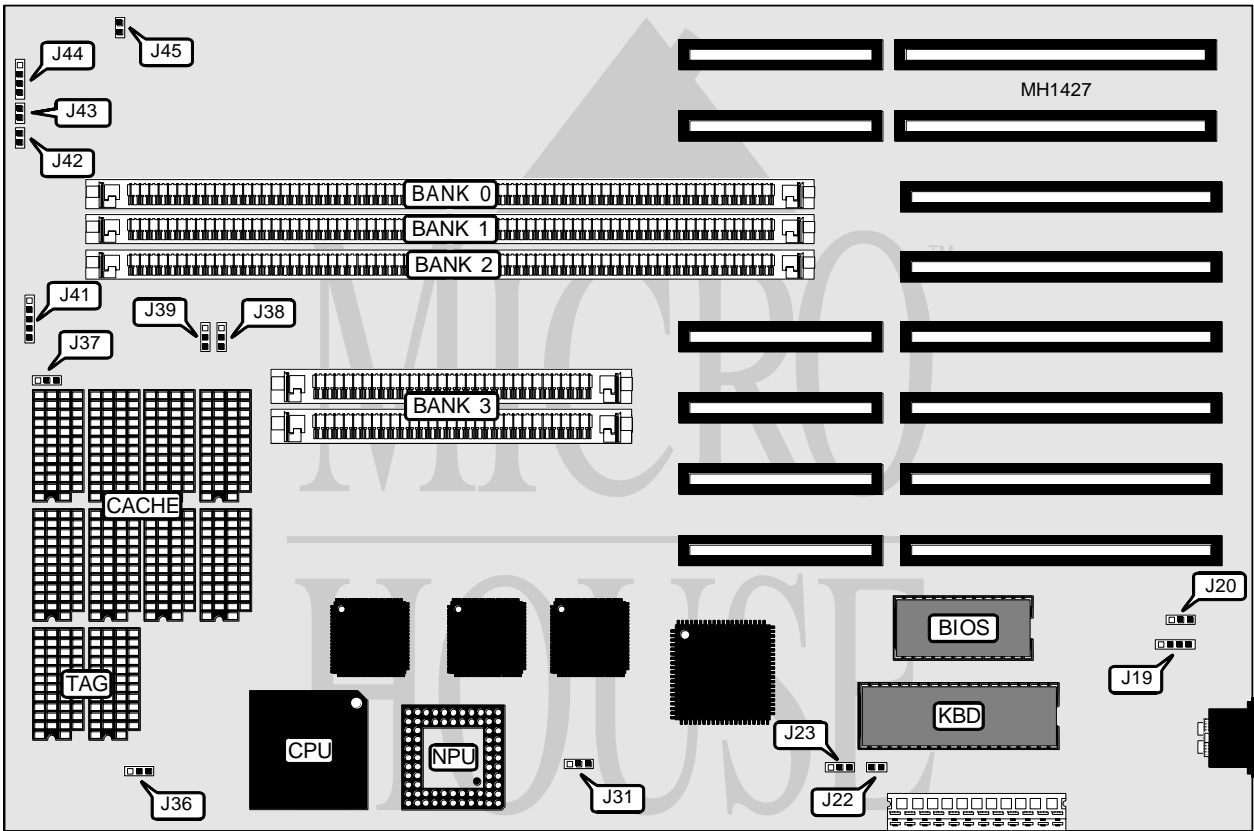


# UNIDENTIFIED BABY 386-33CII

<b>Processor</b>	80386DX
<b>Processor Speed</b>	25/33 MHz
<b>Chip Set</b>	SIS
<b>Max. Onboard DRAM</b>	16MB
<b>SRAM Cache</b>	64/256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External Battery	J19	Turbo LED	J43
Front Panel	J41	Speaker	J44
Reset switch	J42		

Continued on next page . . .

# UNIDENTIFIED

## BABY 386-33CII

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í CMOS memory clear	J20	pins 1 & 2 closed
CMOS memory normal	J20	pins 2 & 3 closed
í Monitor type select color	J22	closed
Monitor type select monochrome	J22	open
í Factory configured - do not alter	J23	unknown
í NPU enabled	J31	pins 1 & 2 closed
NPU disabled	J31	pins 2 & 3 closed

CPU CONFIGURATION			
Type	CPU Speed	OSC	J45
80386DX	25MHz	50MHz	closed
80386DX	33MHz	66MHz	open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9

SRAM JUMPER CONFIGURATION				
Size	J36	J37	J38	J39
64KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
256KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed

SRAM CONFIGURATION			
Size	Cache	Location	TAG
64KB	(8) 16K x 4	CACHE	(2) 16K x 4
256KB	(8) 64k x 4	CACHE	(2) 64K x 4

