ADVANCED INTREGRATION RESEARCH, INC.
486UL REV. 1.0

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2/ODP586SX
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	UMC
Max. onboard DRAM	32MB
SRAM Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 220mm
I/O Options	32-bit external memory card, 32-bit VESA local bus slots (3)
NPU Options	None
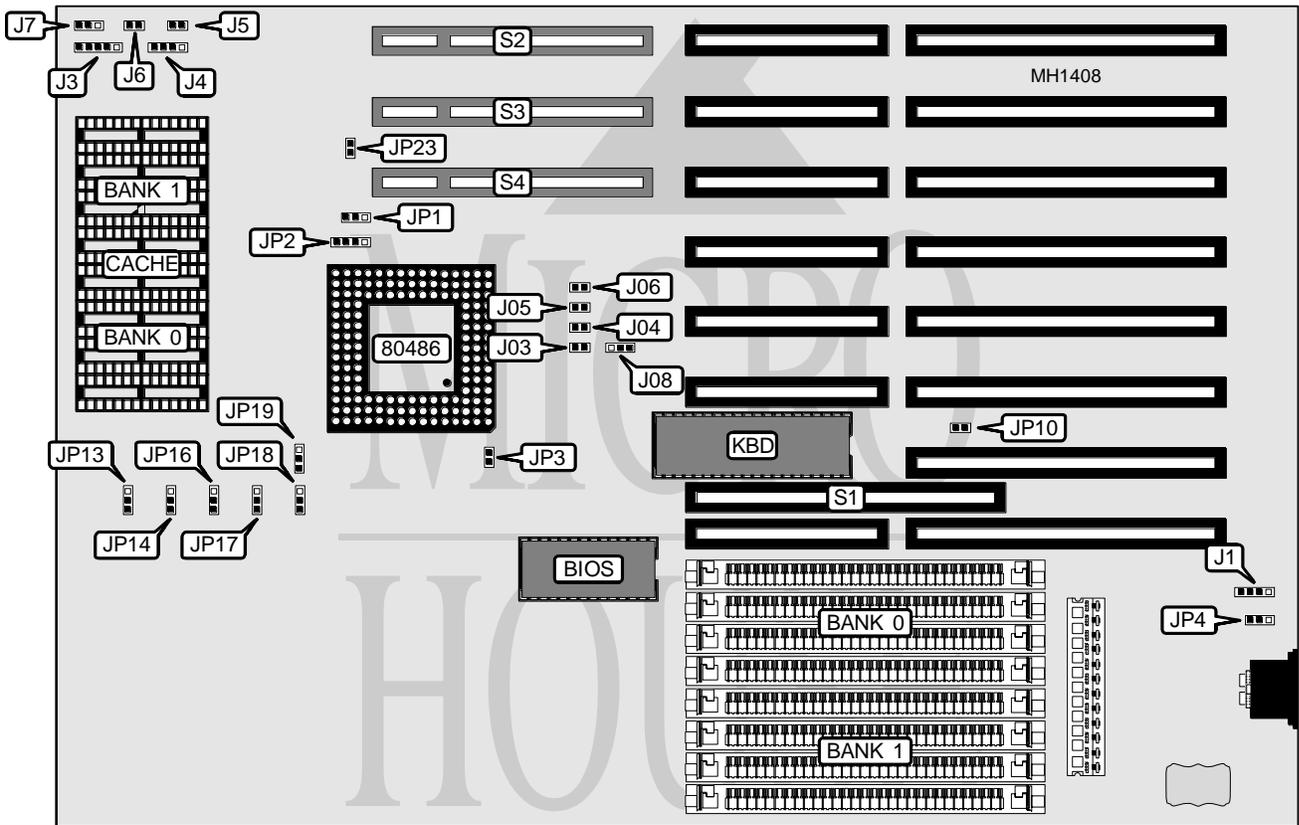
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	J6
Power LED & keylock	J3	Turbo LED	J7
Speaker	J4	32-bit memory card	S1
Reset	J5	32-bit VESA local bus slots	S2, S3, & S4

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Keyboard clock select 8MHz	J08	pins 1 & 2 closed
Keyboard clock select iOSC derived	J08	pins 2 & 3 closed
í CMOS memory normal operation	JP4	pins 1 & 2 closed
CMOS memory clear	JP4	pins 2 & 3 closed
í Onboard battery enabled	JP8	Closed
Onboard battery disabled	JP8	Open
í Monitor type select color	JP10	Closed
Monitor type select monochrome	JP10	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
7MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9
8MB	(4)1M x 9	(4)1M x 9	NONE	NONE
9MB	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	NONE
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
28MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9
32MB	(4)4M x 9	(4)4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
Note: Banks 2 & 3 are located on the optional external memory card inserted into S1.				

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CPU TYPE CONFIGURATION			
CPU	JP1	JP2	JP3
80486SX	Open	pins 2 & 3 closed	Open
80487SX/ODP486SX	pins 2 & 3 closed	pins 1 & 2, 3 & 4 closed	Open
80486DX/DX2	pins 1 & 2 closed	pins 1 & 2, 3 & 4 closed	Open

CPU SPEED CONFIGURATION				
Speed	J03	J04	J05	J06
20MHz	Closed	Closed	Closed	Open
25/50i MHz	Closed	Closed	Open	Closed
33/66i MHz	Closed	Open	Closed	Closed
40MHz	Open	Open	Closed	Open
50MHz	Open	Open	Open	Open

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8

CACHE JUMPER CONFIGURATION						
Size	JP13	JP14	JP16	JP17	JP18	JP19
64KB	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2
128KB	pins 2 & 3	pins 1 & 2	pins 2 & 3	pins 1 & 2	pins 2 & 3	pins 1 & 2
256KB	pins 1 & 2	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 2 & 3
Note: Pins designated should be in the closed position.						

VESA BUS SPEED (ID3) CONFIGURATION	
CPU speed	JP23 (ID3)
$\leq$ 33MHz	Open
$>$ 33MHz	Closed