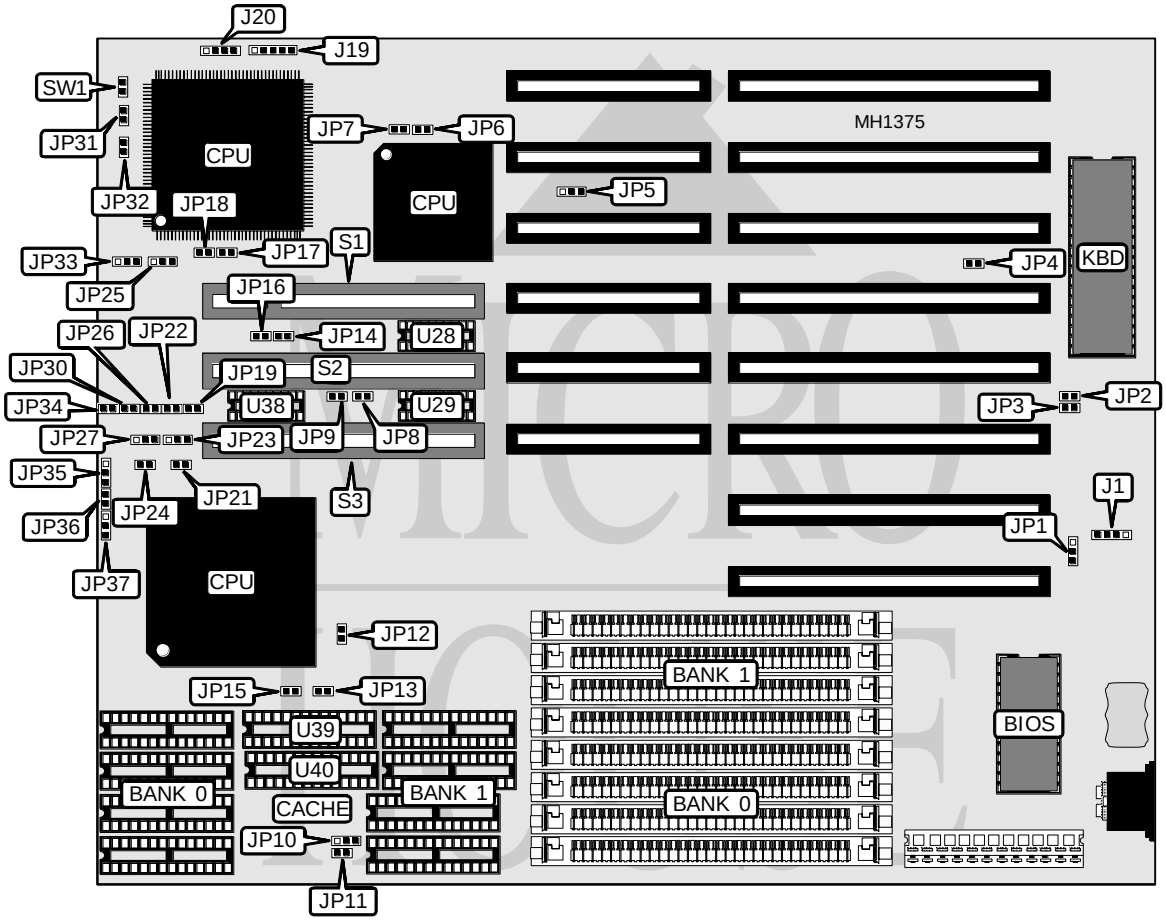
AUVA COMPUTER INC.
CAM 25-P9, CAM 33-P9, CAM 33-F
J-MARK COMPUTER CORPORATION
OPTI-495SX 3/486WB
SEE-THRU DATA SYSTEMS, LTD.
STO 98-5VL

Processor	80386DX/CX486DLC/80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
SRAM Cache	64/128/256KB
BIOS	MR
Dimensions	235mm x 220mm
I/O Options	VESA local bus slots (3)
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo LED	JP32
Power LED & keylock	J19	Reset switch	RST
Speaker	J20	32-bit VESA Local bus slots (Slave)	S2 & S3
Turbo switch	JP31	32-bit VESA Local bus slots (Master)	S2 & S3
Note: VESA Local bus slot S1 should only be used for a VGA video card.			

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Factory configured - do not alter	JP2	open
Bus clock speed select iOSC/4	JP3	closed
Bus clock speed select iOSC/5	JP3	open
í Monitor type select color	JP4	closed
Monitor type select monochrome	JP4	open
í Factory configured - do not alter	JP16	open
80486 PQFP enabled	JP17	open
80486 PQFP disabled	JP17	closed
80386 PQFP enabled	JP18	open
80386 PQFP disabled	JP18	closed
í VESA bus speed select ≤ 33MHz	JP22	open
VESA bus speed select > 33MHz	JP22	closed
í VESA bus wait states select 0 (CPU ≤ 33MHz)	JP26	open
VESA bus wait state select 1 (CPU > 33MHz)	JP26	closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

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SRAM CONFIGURATION				
Size	Cache	Location	TAG(U39)	TAG(U40)
64KB	(8) 8K x 8	Banks 0 & 1	(1) 8K x 8	NONE
128KB	(4) 32K x 8	Bank 0	(1) 8K x 8	NONE
256KB	(8) 32K x 8	Banks 0 & 1	(1) 32K x 8	NONE
256KB	(8) 32K x 8	Banks 0 & 1	(1) 8K x 8	(1) 8K x 8

SRAM JUMPER CONFIGURATION				
Size	JP10	JP11	JP13	JP15
64KB	pins 2 & 3 closed	open	open	open
128KB	pins 1 & 2 closed	closed	closed	open
256KB	pins 2 & 3 closed	closed	closed	closed

CPU CONFIGURATION									
CPU	JP5	JP6	JP7	JP12	JP30	JP34	JP35	JP36	JP37
80386DX	2&3	open	open	open	closed	open	N/A	N/A	N/A
CX486DLC	2&3	closed	closed	open	closed	open	N/A	N/A	N/A
80486SX	1&2	open	open	closed	open	closed	open	open	2&3
80487SX	1&2	open	open	closed	open	closed	2&3	closed	1&2
80486DX	1&2	open	open	closed	open	closed	1&2	closed	1&2
ODP487SX	1&2	open	open	closed	open	closed	2&3	closed	1&2
80486DX2	1&2	open	open	closed	open	closed	1&2	closed	1&2
Note: Pins designated should be in the closed position.									

CLOCK SPEED SELECT				
Speed	JP25	JP28	JP29	JP33
25MHz	pins 1 & 2 closed	closed	open	pins 1 & 2 closed
33MHz(386/486DLC)	pins 2 & 3 closed	closed	open	pins 1 & 2 closed
33MHz(486)	pins 1 & 2 closed	open	closed	pins 1 & 2 closed
40MHz(486)	pins 1 & 2 closed	open	open	pins 1 & 2 closed
40MHz(386/486DLC)	pins 2 & 3 closed	open	closed	pins 1 & 2 closed
50iMHz	pins 2 & 3 closed	closed	open	pins 2 & 3 closed
50MHz	pins 1 & 2 closed	closed	open	pins 1 & 2 closed
66iMHz	pins 2 & 3 closed	open	closed	pins 2 & 3 closed

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VESA SLOT (S2 & S3) MASTER/SLAVE CONFIGURATION		
Jumper	Master/Slave	Slave only
JP8	open	closed
JP9	open	closed
JP14	open	closed
JP19	open	closed
JP21	open	closed
JP23	pins 1 & 2 closed	pins 2 & 3 closed
JP24	open	closed
JP27	pins 1 & 2 closed	pins 2 & 3 closed
U29 (PAL socket)	Installed	N/A
U38 (PAL socket)	Installed	N/A
Note: When the Master/Slave setting is used slots S2 & S3 can use Bus-master cards or slave cards. When the Slave only setting is used non-Bus-master cards can be used.		