## ASUS COMPUTER INTERNATIONAL

## ISA-486SIQ

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
SRAM Cache
BIOS
Dimensions
I/O Options
NPU Options

80486SX/80487SX/80486DX/80486DX2
20/25/33/50(internal)/50/66(internal)MHz
OPTI
32 MB
64/128/256KB
AMI
$220 \mathrm{~mm} \times 240 \mathrm{~mm}$
None
None


| CONNECTIONS |  |  |  |
| :--- | :---: | :--- | :---: |
| Purpose | Location | Purpose | Location |
| Reset | CON1 | Turbo LED | CON4 |
| Speaker | CON2 | Turbo switch | CON5 |
| Power LED \& keylock | CON3 |  |  |

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| UsER CONFIGURABLE SETTINGS |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Function |  |  |  | Jumper | Position |
| í CPU speed select $25 / 33 / 50 \mathrm{MHz}$ | JP1 | pins $2 \& 3$ closed |  |  |  |
| CPU speed select 20 MHz | JP1 | pins $1 \& 2$ closed |  |  |  |
| í Monitor type select monochrome | JP2 | pins $1 \& 2$ closed |  |  |  |
| Monitor type select color | JP2 | pins $2 \& 3$ closed |  |  |  |


| DRAM CONFIGURATION |  |  |
| :---: | :---: | :---: |
| Size | Bank 0 | Bank 1 |
| 1 MB | (4) $256 \mathrm{~K} \times 9$ | NONE |
| 2 MB | (4) $256 \mathrm{~K} \times 9$ | (4) $256 \mathrm{~K} \times 9$ |
| 4 MB | (4) $1 \mathrm{M} \times 9$ | NONE |
| 8 MB | (4) $1 \mathrm{M} \times 9$ | $(4) 1 \mathrm{M} \times 9$ |
| 16 MB | (4) $4 \mathrm{M} \times 9$ | NONE |
| 20 MB | (4) $4 \mathrm{M} \times 9$ | (4) $1 \mathrm{M} \times 9$ |
| 32 MB | (4) $4 \mathrm{M} \times 9$ | (4) $4 \mathrm{M} \times 9$ |


| SRAM CONFIGURATION |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Size | Cache | Location | TAG |  |
| 64 KB | (8) $8 \mathrm{~K} \times 8$ | Banks 0 \& 1 | $(1) 8 \mathrm{~K} \times 8$ |  |
| 128 KB | (4) $32 \mathrm{~K} \times 8$ | Bank 0 | (1) $8 \mathrm{~K} \times 8$ |  |
| 256 KB | (8) $32 \mathrm{~K} \times 8$ | Banks 0 \& 1 | (1) $32 \mathrm{~K} \times 8$ |  |


| SRAM JUMPER CONFIGURATION |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Size | Jumper CJ1 | Jumper CJ2 | Jumper CJ3 |  |
| 64 KB | pins 1 \& 2 closed | pins 1 \& 2 closed | open |  |
| 128 KB | pins 2 \& 3 closed | open | closed |  |
| 256 KB | pins 2 \& 3 closed | pins 2 \& 3 closed | open |  |


| CPU JUMPER CONFIGURATION |  |  |  |
| :---: | :---: | :---: | :---: |
| CPU | Jumper CPJ1 | Jumper CPJ2 | Jumper CPJ3 |
| 80486 DX | pins 2 \& 3 closed | pins 1 \& 2 and 3 \& 4 closed | pins 2 \& 3 closed |
| 80487 SX | pins 1 \& 2 closed | pins 1 \& 2 and 3 \& 4 closed | pins 1 \& 2 closed |
| 80486 SX | open | pins 2 \& 3 closed | pins 2 \& 3 closed |

