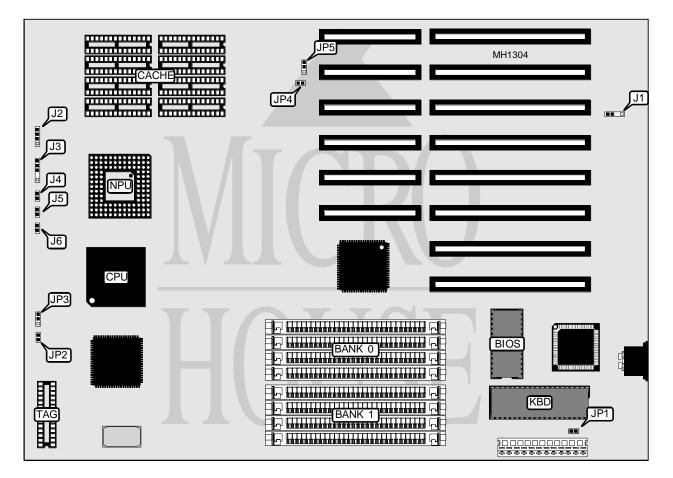
MICRO COMPUTER SYSTEMS CORPORATION M C S 3 8 6 - A G 4 2

0386DX
3/40MHz
TEQ
2MB
4/256KB
30.2mm x 218.4mm
MI
lone
0387/3167



CONNECTIONS							
Purpose	Location	Purpose	Location				
External battery	J1	Turbo switch	J4				
Speaker	J2	Turbo LED	J5				
Power LED & keylock	J3	Reset switch	J6				

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MICRO COMPUTER SYSTEMS CORPORATION M C S 3 8 6 - A G 4 2

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USER CONFIGURABLE SETTINGS						
Function	Jumper	Position				
í CPU speed select switchable from turbo switch	JP1	open				
í CPU speed select switchable from turbo switch or keyboard	JP1	closed				

DRAM CONFIGURATION					
Size	Bank 0	Bank 1			
1MB	(4) 256K x 9	NONE			
2MB	(4) 256K x 9	(4) 256K x 9			
4MB	(4) 1M x 9	NONE			
8MB	(4) 1M x 9	(4) 1M x 9			
16MB	(4) 4M x 9	NONE			
32MB	(4) 4M x 9	(4) 4M x 9			

SRAM CONFIGURATION							
Size	Cache	TAG	JP2 & JP4	JP3	JP5		
64KB	(8) 8K x 8	(1) 8K x 8	open	pins 1 & 2 closed	pins 1 & 2 closed		
256KB	(8) 32K x 8	(1) 32K x 8	closed	pins 2 & 3 closed	pins 1 & 2 closed		