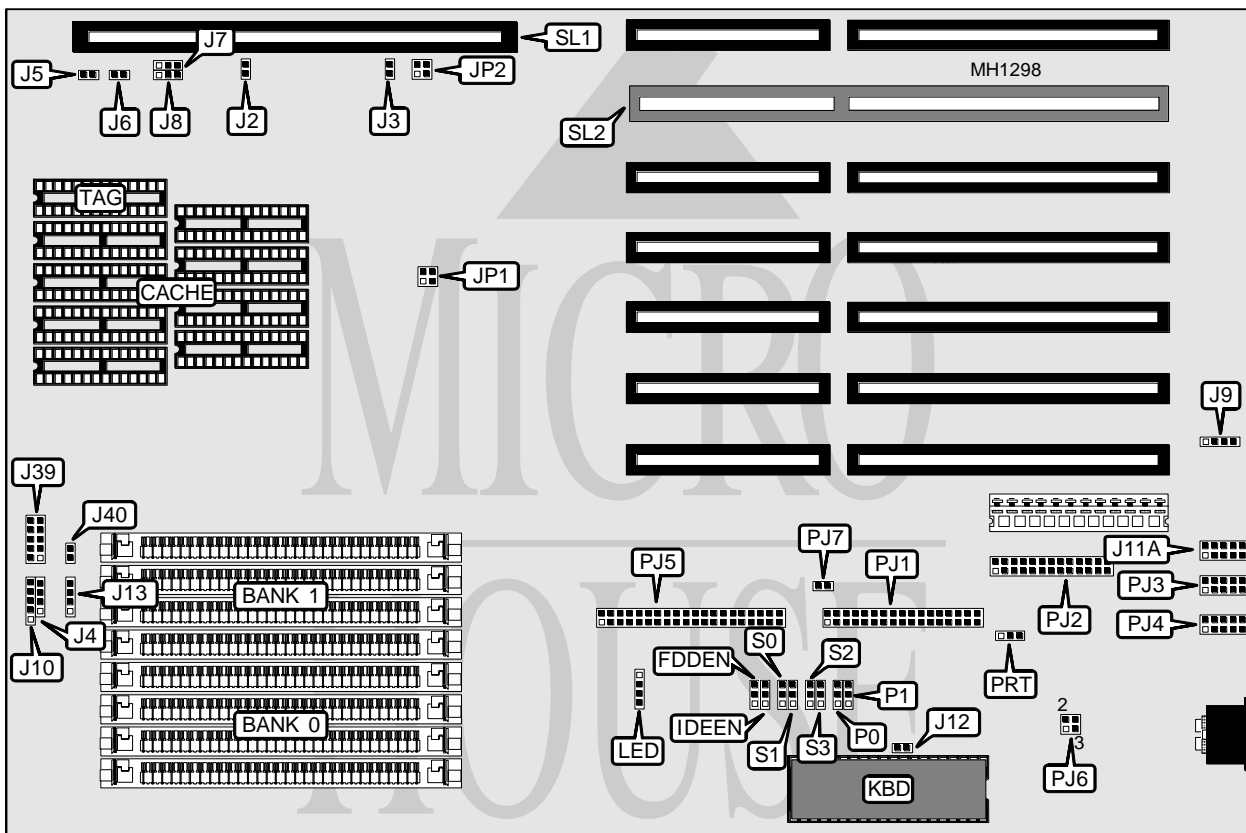


CSS LABORATORIES, INC.

PREFERRED 433GA

Processor	80486DX
Processor Speed	33MHz (upgradable to 50MHz with CPU module)
Chip Set	C & T
Max. Onboard DRAM	32MB
SRAM Cache	64/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit external CPU/NPU card slot, 32-bit local video card slot, auxiliary keyboard, floppy interface, IDE interface, parallel port, serial port (2)
NPU Options	4167 (on 32-bit external CPU/NPU card)



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J4	IDE interface LED	LED
External battery	J9	Floppy drive interface	PJ1
Power LED & keylock	J10	Parallel port	PJ2
Auxiliary keyboard	J11A	Serial port 1	PJ3
Reset switch	J13/pins 1 & 2	Serial port 2	PJ4
Turbo switch	J13/pins 3 & 4	IDE interface	PJ5
Front panel switches & LEDs	J39	32-bit external CPU/NPU card	SL1
Hard drive access LED	J40	32-bit MaxGraphics video card	SL2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Floppy interface enabled	FDDEN	pins 2 & 3 closed
Floppy interface disabled	FDDEN	pins 1 & 2 closed
í IDE interface enabled	IDEEN	pins 2 & 3 closed
IDE interface disabled	IDEEN	pins 1 & 2 closed
í Factory configured - do not alter	J3	open
í Monitor type select color	J12	pins 2 & 3 closed
Monitor type select monochrome	J12	pins 1 & 2 closed
í Factory configured - do not alter	JP1 & JP2	unknown

SRAM JUMPER CONFIGURATION					
Size	J2	J5	J6	J7	J8
64KB	open	open	open	pins 2 & 3	pins 2 & 3
256KB	closed	closed	closed	pins 1 & 2	pins 1 & 2

Note: Pins designated should be in the closed position.

SRAM CONFIGURATION		
Size	Cache SRAM	TAG
64KB	(8) 8K x 8	(1) 8K x 8
256KB	(8) 32K x 8	(1) 32K x 8

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

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SERIAL PORT 1 (PJ3) CONFIGURATION					
COM	IRQ Interrupt	I/O Address	Jumper PJ6	S0	S1
COM 2	IRQ3	2F8h	pins 1 & 3	pins 1 & 2	pins 2 & 3
COM 1	IRQ4	3F8h	pins 1 & 2	pins 2 & 3	pins 2 & 3
Disabled	N/A	N/A	N/A	pins 1 & 2	pins 1 & 2

Note: Pins designated should be in the closed position.

SERIAL PORT 2 (PJ4) CONFIGURATION					
COM	IRQ Interrupt	I/O Address	Jumper PJ6	S2	S3
COM 1	IRQ4	3F8h	pins 2 & 4	pins 1 & 2	pins 2 & 3
COM 2	IRQ3	2F8h	pins 3 & 4	pins 2 & 3	pins 2 & 3
Disabled	N/A	N/A	N/A	pins 1 & 2	pins 1 & 2

Note: Pins designated should be in the closed position.

PARALLEL PORT 1 (PJ2) CONFIGURATION					
LPT	IRQ Interrupt	I/O Address	Jumper P0	Jumper P1	Jumper PRT
LPT 1	IRQ7	3F8h	pins 1 & 2	pins 2 & 3	pins 2 & 3
LPT 2	IRQ5	2F8h	pins 2 & 3	pins 2 & 3	pins 1 & 2
LPT 3	IRQ7	3BCh	pins 2 & 3	pins 1 & 2	pins 2 & 3
Disabled	N/A	N/A	pins 1 & 2	pins 1 & 2	N/A

Note: Pins designated should be in the closed position.