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DASH COMPUTER, INC.

DC3486 VESA

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Factory configured - do not alter	JP2	open
AT Bus speed select CLK/5 with 80386 CPU	JP3	open
AT Bus speed select CLK/4 with 80386 CPU	JP3	closed
í Monitor type select color	JP4	open
Monitor type select monochrome	JP4	closed

CACHE JUMPER CONFIGURATION				
Size	JP10	JP11	JP13	JP15
64KB	pins 2 & 3 closed	open	open	open
128KB	pins 1 & 2 closed	closed	closed	open
256KB	pins 2 & 3 closed	closed	closed	closed

CACHE CONFIGURATION				
Size	Cache	Location	TAG (U39)	TAG (U40)
64KB	(8) 8K x 8	Banks 0 & 1	(1) 8K x 8	NONE
128KB	(4) 32K x 8	Bank 0	(1) 8K x 8	(1) 8K x 8
256KB	(8) 32K x 8	Banks 0 & 1	(1) 8K x 8	(1) 8K x 8

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

SLOTS 3 & 4 BUS CONFIGURATION						
Bus Type	JP8 & 9	JP14 & 16	JP23 & 27	JP19	JP21	JP24
í VESA	open	open	pins 1 & 2	open	open	open
AT	closed	closed	pins 2 & 3	closed	closed	closed
Note: Slots 3 & 4 must both be configured as either 16-bit or 32-bit.						
Note: Pins designated should be in the closed position.						

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CONFIGURATION FOR 80486DX & 80486DX2 CPU						
Jumper	Position	25MHz	33MHz	50iMHz	50MHz	66iMHz
JP5	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2
JP6	open	open	open	open	open	open
JP7	open	open	open	open	open	open
JP12	closed	closed	closed	closed	closed	closed
JP17	closed	closed	closed	closed	closed	closed
JP18	open	open	open	open	open	open
JP22	N/A	open	open	open	closed	open
JP25	N/A	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 2 & 3
JP26	N/A	open	open	open	closed	open
JP28	N/A	closed	open	closed	closed	open
JP29	N/A	open	closed	open	open	closed
JP30	N/A	open	open	open	open	open
JP33	N/A	pins 2 & 3	pins 2 & 3	pins 2 & 3	pins 1 & 2	pins 2 & 3
JP34	N/A	closed	closed	closed	closed	closed
JP35	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2
JP36	closed	closed	closed	closed	closed	closed
JP37	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2	pins 1 & 2
Note: Pins designated should be in the closed position.						

CONFIGURATION FOR 80486SX CPU			
Jumper	Position	25MHz	33MHz
JP5	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
JP6	open	open	open
JP7	open	open	open
JP12	closed	closed	closed
JP17	see note ¹	see note ¹	see note ¹
JP18	open	open	open
JP22	N/A	open	open
JP25	N/A	pins 2 & 3 closed	pins 2 & 3 closed
JP26	N/A	open	open
JP28	N/A	closed	closed
JP29	N/A	open	closed
JP30	N/A	open	open
JP33	N/A	pins 2 & 3 closed	pins 2 & 3 closed
JP34	N/A	closed	closed
JP35	open	open	open
JP36	open	open	open
JP37	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
Note ¹ : Jumper JP17 should be closed when a PGA cpu is installed, open for PQFP.			

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CONFIGURATION FOR 80386DX & CX486DLC CPU			
Jumper	Position	33MHz	40MHz
JP5	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
JP6	see note ¹	see note ¹	see note ¹
JP7	open	open	open
JP12	open	open	open
JP17	open	open	open
JP18	see note ²	see note ²	see note ²
JP22	N/A	closed	open
JP25	N/A	pins 2 & 3 closed	pins 2 & 3 closed
JP26	N/A	open	closed
JP28	N/A	open	open
JP29	N/A	closed	open
JP30	N/A	closed	closed
JP33	N/A	pins 1 & 2 closed	pins 1 & 2 closed
JP34	N/A	open	open
JP35	open	open	open
JP36	open	open	open
JP37	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
Note ¹ : Jumper JP6 should be closed when a CX486DLC is installed, open for 80386DX.			
Note ² : Jumper JP18 should be closed when a PGA cpu is installed, open for PQFP.			